

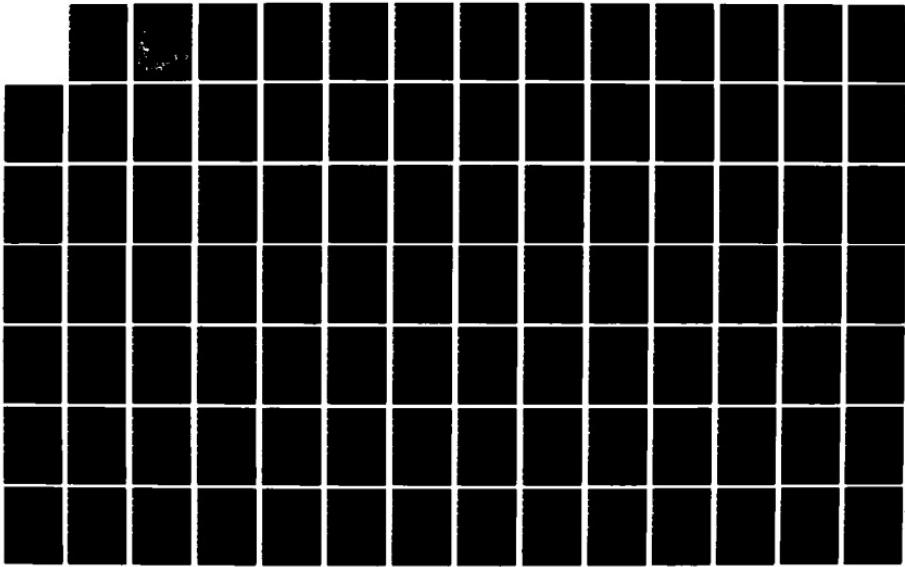
AD-A166 497 UE BENCH TEST PLANS AND REQUIREMENTS VOLUME 1 MAGNAVOX
UE SET(U) ESSCUBE ENGINEERING INC MARLBOROUGH NJ SEP 84
CER/GPS-85-156-00-030-VOL-1 N62269-82-D-0059

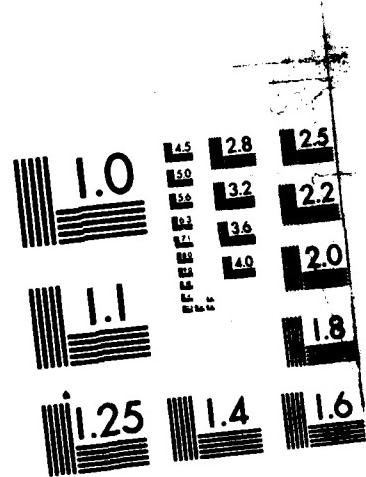
1/1

UNCLASSIFIED

F/G 14/2

ML





MICROCOPY RESOLUTION TEST CHART
NATIONAL BUREAU OF STANDARDS 1963

AD-A166 497

(2)

GPS-85-156-00-030

RF LABORATORY

UE BENCH TEST PLANS AND REQUIREMENTS

VOLUME 1. MAGNAVOX UE SET

Esscube Engineering Inc.
Marlton, NJ 08053
Contract No. N62269-82-D-0059

DTIC FILE COPY



SEPTEMBER 1984



PREPARED FOR NAVAL ELECTRONIC SYSTEMS COMMAND

DISTRIBUTION STATEMENT A
Approved for public release
Distribution Unlimited

86 3 19 031

UE BENCH TEST PLANS
AND REQUIREMENTS

VOLUME 1. MAGNAVOX UE SET

SEPTEMBER 1984

CEA/RFL-84-005

Prepared for:
Naval Air Development Center
Warminster, PA 18974

TABLE OF CONTENTS

<u>Section</u>		<u>Page</u>
	BACKGROUND.....	1
1.0	PURPOSE.....	3
	1.1 Scope.....	3
2.0	LIST OF DOCUMENTS.....	4
3.0	TEST REQUIREMENTS.....	5
	3.1 Magnavox UE Test Requirements.....	5
	3.1.1 Magnavox Test Procedures.....	6
	3.1.1.1 RF Synthesizer Module.....	7
	3.1.1.2 Converter/Channel Selector.... Module	64
	3.1.1.3 IF/BB/Coder Module.....	80
	3.1.1.4 Pre-Amplifier Unit Module....	102
	3.1.1.5 Reference Oscillator Module...	124
4.0	SOFTWARE REQUIREMENTS.....	140
	4.1 Magnavox Software Requirements.....	141
	4.1.1 RF Synthesizer Module.....	142
	4.1.2 Converter/Channel Switcher.....	164
	4.1.3 IF/BB/Coder Module.....	173
	4.1.4 Pre-Amplifier Unit Module.....	176
	4.1.5 Reference Oscillator Module.....	186

Accesion For	
NTIS CRA&I <input checked="" type="checkbox"/>	
DTIC TAB <input type="checkbox"/>	
Unannounced <input type="checkbox"/>	
Justification _____	
By <i>Ltr. on file</i>	
Distribution /	
Availability Codes	
Dist	Avail and/or Special
A-1	



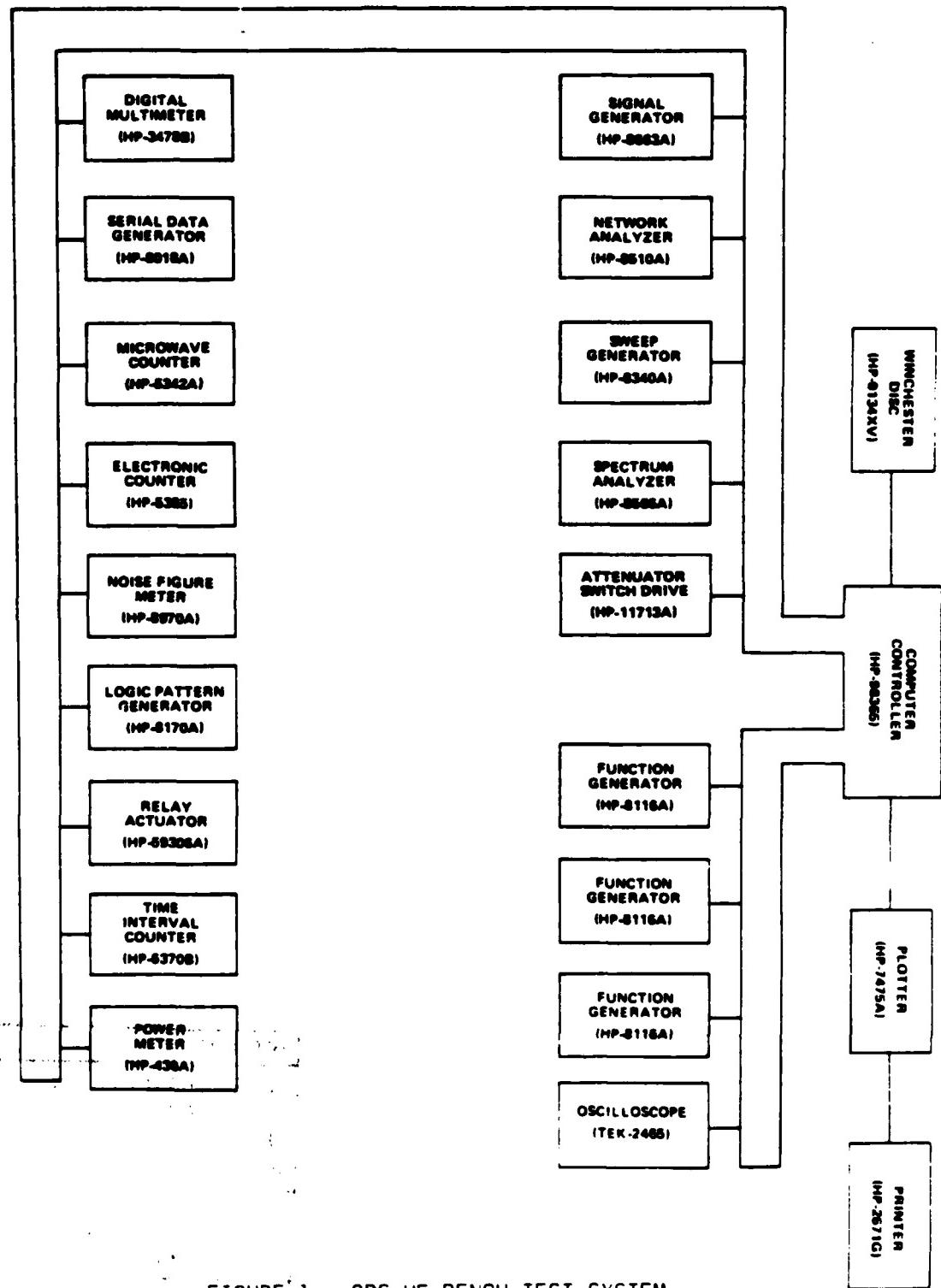


FIGURE 1. GPS UE BENCH TEST SYSTEM

BACKGROUND

When the GPS becomes operational, the RF Laboratory of the NAVAIRDEVCEN GPS Central Engineering Activity will be required to provide a capability to resolve problems which relate to the performance of the RF elements of the UE.

In order to meet these requirements, the RF Laboratory is planning to provide for:

- o computer analysis of the problem using a UE RF Emulator
- o RF bench test of the UE down to the component level using modern automatic testing techniques
- o system level testing of the UE and antenna system using a Satellite Signal Generator and mockups of actual antenna installations

The bench tests will involve many detailed measurements to determine the effect of UE components upon the performance of the UE as a receiver and processor of the GPS signal. Special purpose software and special purpose hardware will be required to perform tests, stimulate the various function generators, pattern generators, signal generators, sweep generators, jammers and noise sources, control the meters, analyzers, counters, oscilloscopes and data recording devices and to perform some real-time data reduction for quick-look analyses.

In order to define this special software and hardware, it was necessary to devise individual tests on UE RF components using available information on both the Magnavox and the Collins Phase II UE design.

The GPS UE Bench Test System is shown in Figure 1. It uses an IEEE-488 bus to control the required stimulus, measurement and recording equipment. The computer controller will have access to all test procedures and will be able to string a group of test procedures together for different tests using the same test setup. Test data will be reduced for quick look analysis and for comparison with standard or baseline data. The system will also produce discs for offline analysis and longterm data retention. Detailed specifications for each element of the UE Bench Test System can be found in Reference 4, Volume 1.

The UE Bench Test Procedures are organized into three volumes. Volume one contains the Magnavox Bench Test Procedures and software requirements, volume two contains the Rockwell-Collins Bench Test Procedures and software requirements and volume three contains the General Receiver Tests, Antenna Test Procedures for both contractors and a section on special purpose hardware requirements.

It is intended that this document will be expanded and refined as more detailed performance information becomes available on UE RF components.

GPS UE BENCH TEST PLANS AND REQUIREMENTS

VOLUME 1

1.0 PURPOSE

The purpose of this report is to provide a basis for defining the hardware and software required to perform RF related bench tests of the Magnavox UE in the RF Laboratory of the GPS Central Engineering Activity at NAVAIRDEVcen.

1.1 SCOPE

This report contains general test requirements for each test defining the:

- level of test (UE, board, component)
- test objective (parameter/function)
- test inputs
- test outputs
- equipment required for each input/output
- initial test procedure
- data reduction requirements
- summary list of test equipment (standard/special)
- block diagram of test)

Since most of these tests will be computer controlled, special software will be required to control the conditions, parameters and data collection needs of the test.

A section on special software requirements is provided which defines the software needs of each test in terms of flowcharts.

2.0 LIST OF REFERENCE DOCUMENTS

The following documents were used as references for UE RF configuration and performance requirements:

1.	ICD-GPS-204	GPS Instrumentation and Connector Stds.	5 June 1981
2.	SS-US-200	System Segment Specification	4 Nov. 1982
3.	Magnavox	Critical Design Review	3 Sept. 1982
4.	ESSCUBE N62269-82-D-0059	Specification for a GPS UE Bench Test System	1 May 1984

3.0 TEST REQUIREMENTS

The test requirements contained herein deal with the Magnavox UE set and are written at the board level. Specific values of parameters, ranges, tolerances and granularities are provided if known. As more detailed information on the sets become available, missing values will be provided and the test requirements will be refined and expanded down to the component level.

3.1 MAGNAVOX UE TEST REQUIREMENTS

This section contains the test requirements, procedures and block diagrams for board level testing of the Magnavox UE set. Initial functional test procedures have been compiled for the following boards or units:

1. RF Synthesizer
2. Converter/Channel Switcher
3. IF/BB/Coder
4. Pre Amplifier Unit Module
5. Reference Oscillator

Although information is available for these boards, there is not enough to completely identify board input and output levels for stimulation and measurement purposes. For example, some of the boards require digital input signals as stimuli. There is not enough information available to determine the required input levels for these signals or the levels for output signals. These digital signals also present a problem in that special purpose hardware may be required to interface the test equipment to the board being tested. This problem will be discussed further in Volume 3, Section 5.0 (Special Purpose Hardware Requirements).

No information has been located concerning Magnavox's Antenna Electronics or Power Supply. Test requirements for these units will be provided as information becomes available.

3.1.1 Magnavox Test Procedures

The following sheets contain the initial test requirements and procedures as listed in section 1.1 for each of the boards identified in section 3.1. These sheets will be refined and expanded as more information becomes available. Also as more detailed data on the boards becomes available sheets will be added for testing to the component level.

3.1.1.1 RF SYNTHESIZER MODULE

Contractor: Magnavox
Board Tested: RF Synthesizer
Test Objective: Verify GAIN AF1 operational

Inputs

<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1. Carrier Select A	TBD	Logic Pattern Gen. (HP-8170A)
2. BITE SW	TBD	Logic Pattern Gen. (HP-8170A)
3. Noise On/Off	TBD	Logic Pattern Gen. (HP-8170A)
4. 2F	10.23MHz @ TBD	Sweep Gen. (HP-8340A)
5. L1/L2	-120dBm @ L1/L2	Signal Gen. (HP-8663A)
6. AGC S/F1	TBD	Function Gen. (HP-8116A)
7.		
8.		
9.		
10.		
11.		
12.		

Outputs

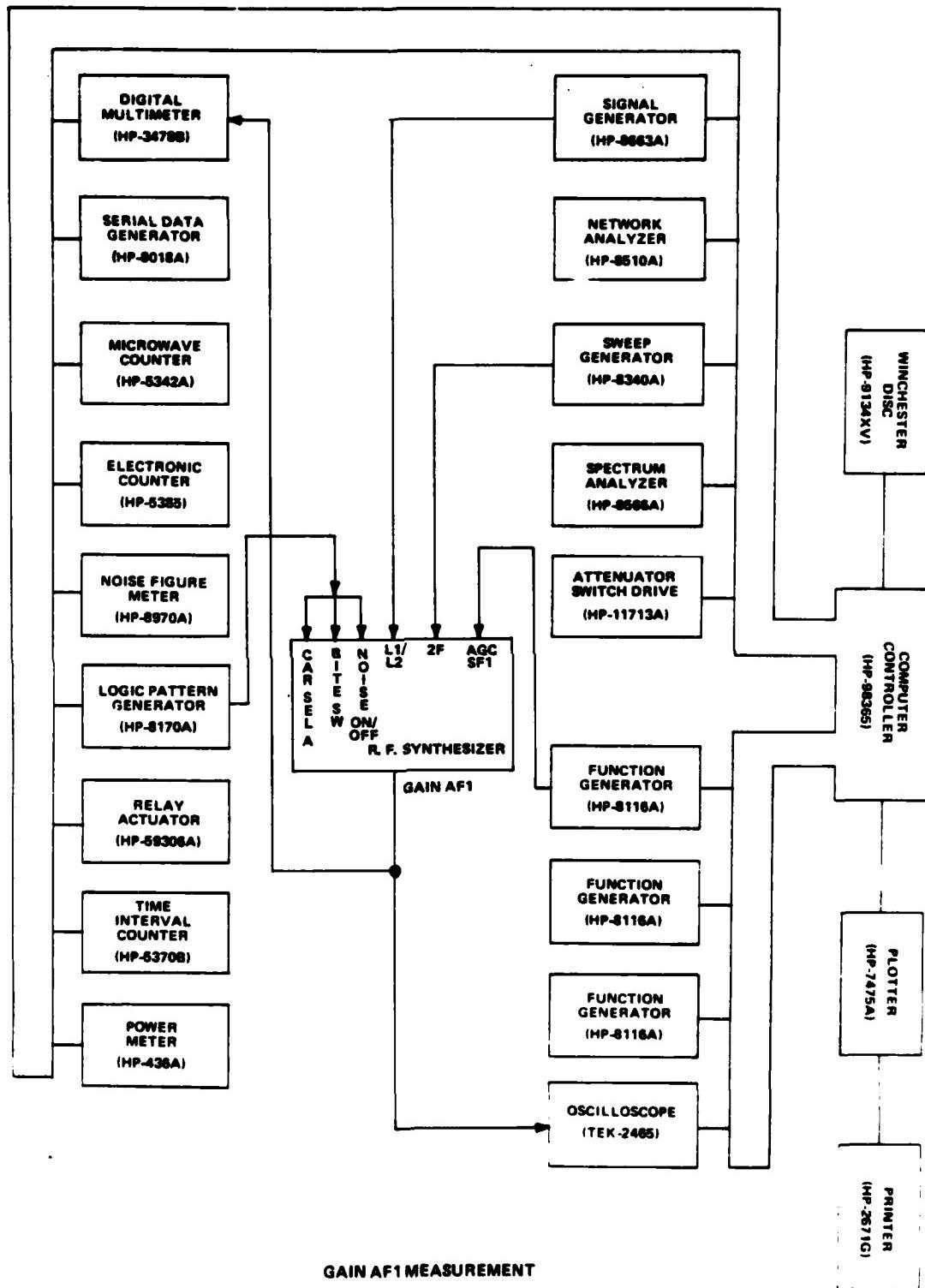
<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. GAIN AF 1	TBD	Digital Multi (HP-3478B)
2.		Oscilloscope (TEK-2465)
3.		
4.		
5.		

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Apply proper signal levels to all inputs of RF Synthesizer. Measure GAIN AF1 with Digital Multimeter. An Oscilloscope will be used to display waveform.

Data Reduction: Send input levels and output of Digital Multimeter to printer. Output of Oscilloscope to plotter.

Equipment List:

- | | | |
|--------------------------------|------------------------------|----------------------------|
| 1. Logic Patt. Gen. (HP-8170A) | 4. Function Gen. (HP-8116A) | 7. Computer (HP-9836S) |
| 2. Sweep Gen. (HP-8340A) | 5. Digital Multi. (HP-3478B) | 8. Oscilloscope (TEK-2465) |
| 3. Sig. Gen. (HP-8663A) | 6. Printer (HP-2671G) | 9. Plotter (HP-7475A) |



Contractor: Magnavox

Board Tested: RF Synthesizer

Test Objective: Verify RF GAIN AF1 operational

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	RF GAIN AF1	TBD	Function Gen. (HP-8116A)
2.			
3.			
4.			
5.			
6.			
7.			
8.			
9.			
10.			
11.			
12.			

Outputs

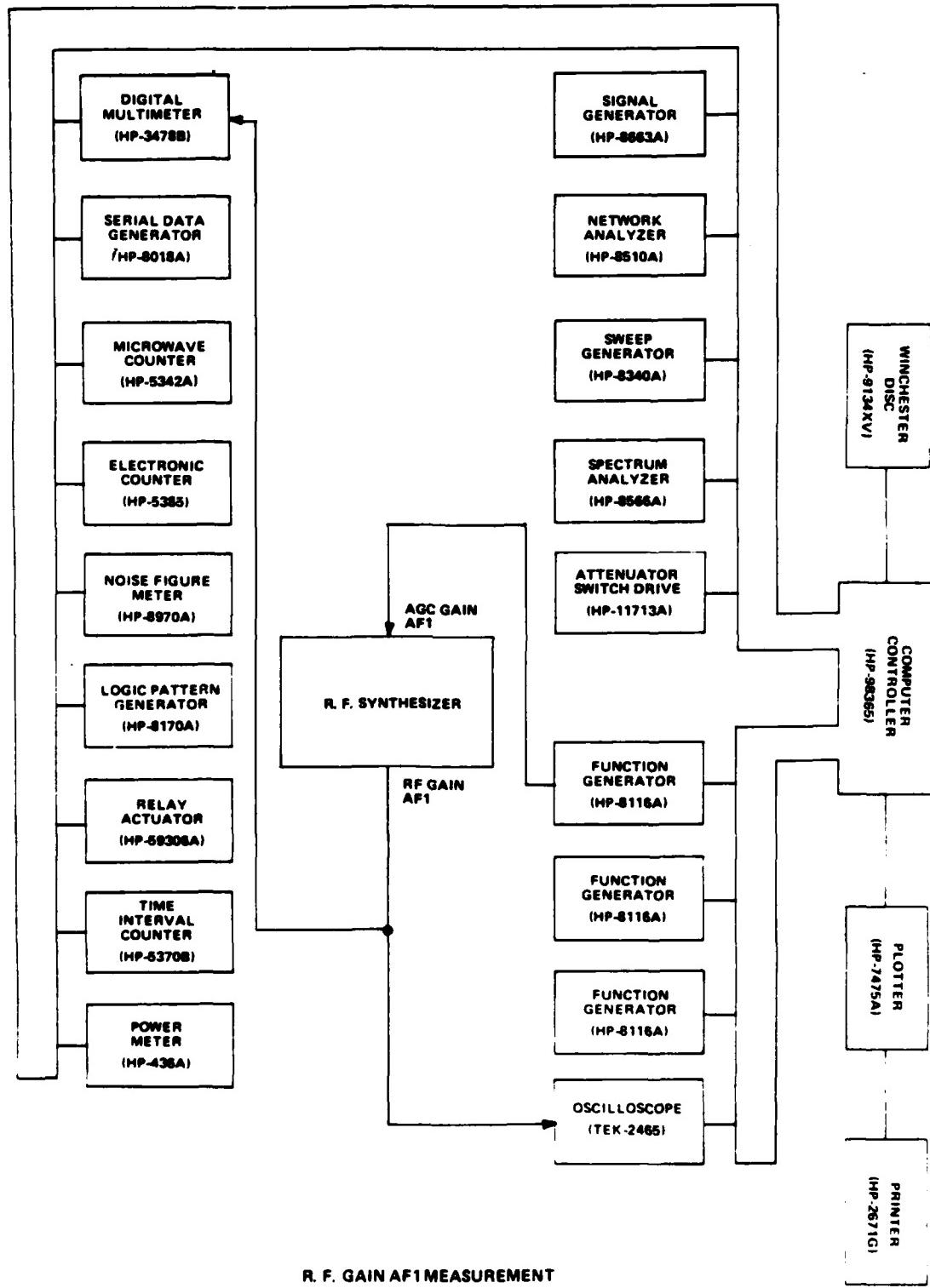
<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. RF GAIN AF1	TBD	Digital Multi. (HP-3478B)
2.		Oscilloscope (TEK-2465)
3.		
4.		
5.		

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Apply AGC GAIN AF1 to RF Synthesizer board. Measure RF GAIN AF1 with Digital Multimeter. An Oscilloscope will be used to display waveform.

Data Reduction: Send input level and output of Digital Multimeter to printer. Output of Oscilloscope to plotter.

Equipment List:

- | | | |
|------------------------------|----------------------------|----|
| 1. Function Gen. (HP-8116A) | 4. Computer (HP-9836S) | 7. |
| 2. Digital Multi. (HP-3478B) | 5. Oscilloscope (TEK-2465) | 8. |
| 3. Printer (HP-2671G) | 6. Plotter (HP-7475A) | 9. |



R. F. GAIN AF1 MEASUREMENT

Contractor: Magnavox
Board Tested: RF Synthesizer
Test Objective: Measure 36F(IF) output frequency and spectral content.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	Carrier Select A	TBD	Logic Patt. Gen. (HP-8170A)
2.	BITE SW	TBD	Logic Patt. Gen. (HP-8170A)
3.	NOISE On/Off	TBD	Logic Patt. Gen. (HP-8170A)
4.	L1/L2	-120dBm @ L1/L2	Sig. Gen. (HP-8663A)
5.	2F	10.23MHz @ TBD	Sweep Gen. (HP-8340A)
6.	AGC SF1	TBD	Function Gen. (HP-8116A)
7.			
8.			
9.			
10.			
11.			
12.			

Outputs

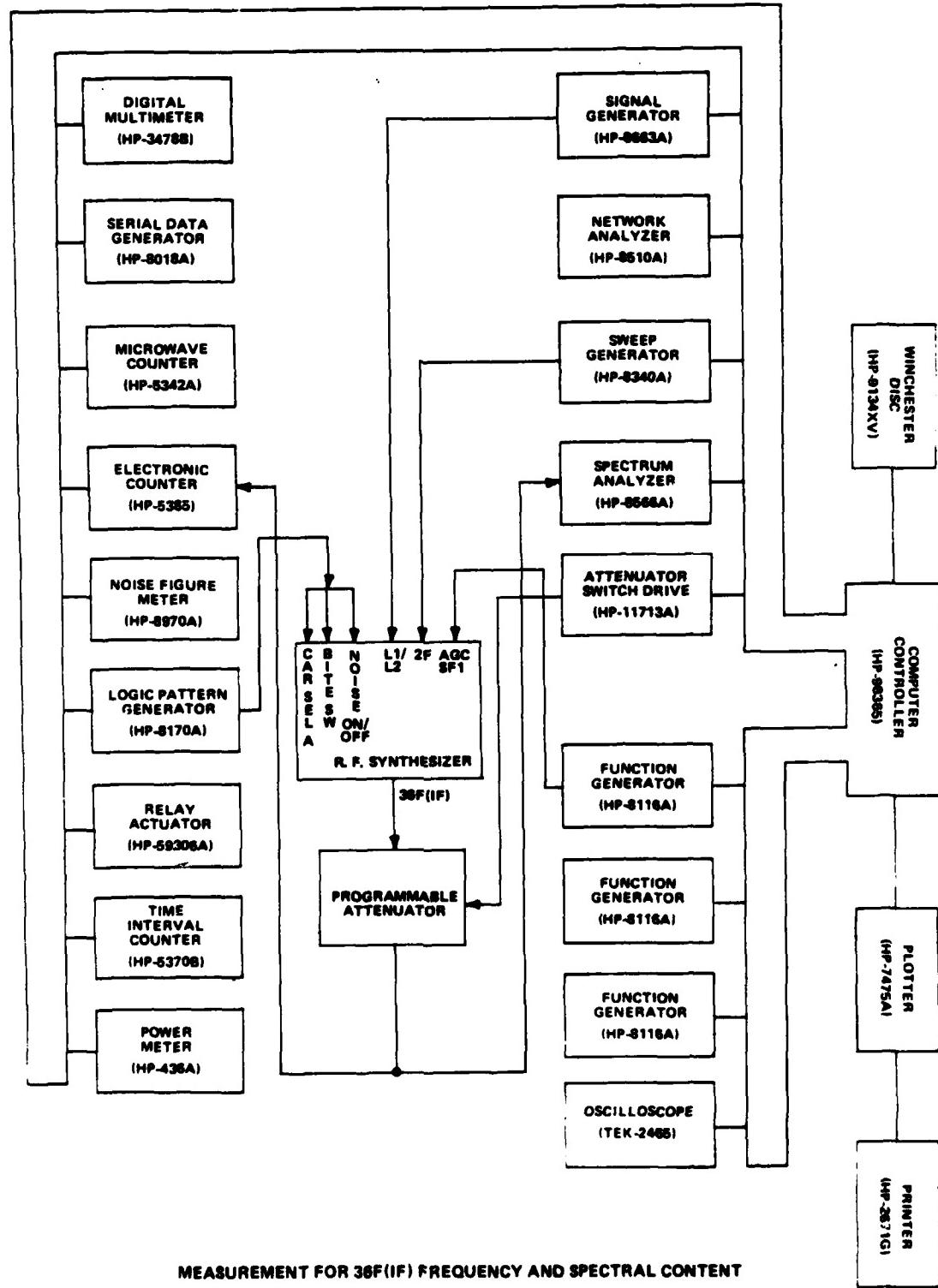
<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. 36F(IF)	-24dBm @ 184.14MHz	Spectrum Anal. (HP-8566A)
2.		Electronic Ctr. (HP-5385)
3.		Att. Sw Drive (HP-11713A)
4.		Prog. Att. (HP-8494G)
5.		

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Apply proper signal levels to all inputs of RF Synthesizer. Measure frequency of 36F(IF) output on Electronic Counter and check spectral content on Spectrum Analyzer.

Data Reduction: Send input levels and output of Electronic Counter to printer. Output of Spectrum Analyzer to plotter.

Equipment List:

- | | | |
|--------------------------------|------------------------------|------------------------------|
| 1. Logic Patt. Gen. (HP-8170A) | 4. Func. Gen. (HP-8116A) | 7. Att. Sw Drive (HP-11713A) |
| 2. Sig. Gen. (HP-8663A) | 5. Spectrum Anal. (HP-8566A) | 8. Prog. Attn. (HP-8494G) |
| 3. Sweep Gen. (HP-8340A) | 6. Electronic Ctr. (HP-5385) | 9. Printer (HP-2671G) |
| 10. Plotter (HP-7475A) | 11. Computer (HP-9836S) | |



MEASUREMENT FOR 38F(IF) FREQUENCY AND SPECTRAL CONTENT

Contractor: Magnavox
Board Tested: RF Synthesizer
Test Objective: Power measurement for 36F(IF)

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	Carrier Select A	TBD	Logic Patt. Gen. (HP-8170A)
2.	BITE SW	TBD	Logic Patt. Gen. (HP-8170A)
3.	Noise On/Off	TBD	Logic Patt. Gen. (HP-8170A)
4.	L1/L2	-120dBm @ L1/L2	Sig. Gen. (HP-8663A)
5.	2F	10.23MHz @ TBD	Sweep Gen. (HP-8340A)
6.	AGC SF 1	TBD	Function Gen. (HP-8116A)
7.			
8.			
9.			
10.			
11.			
12.			

Outputs

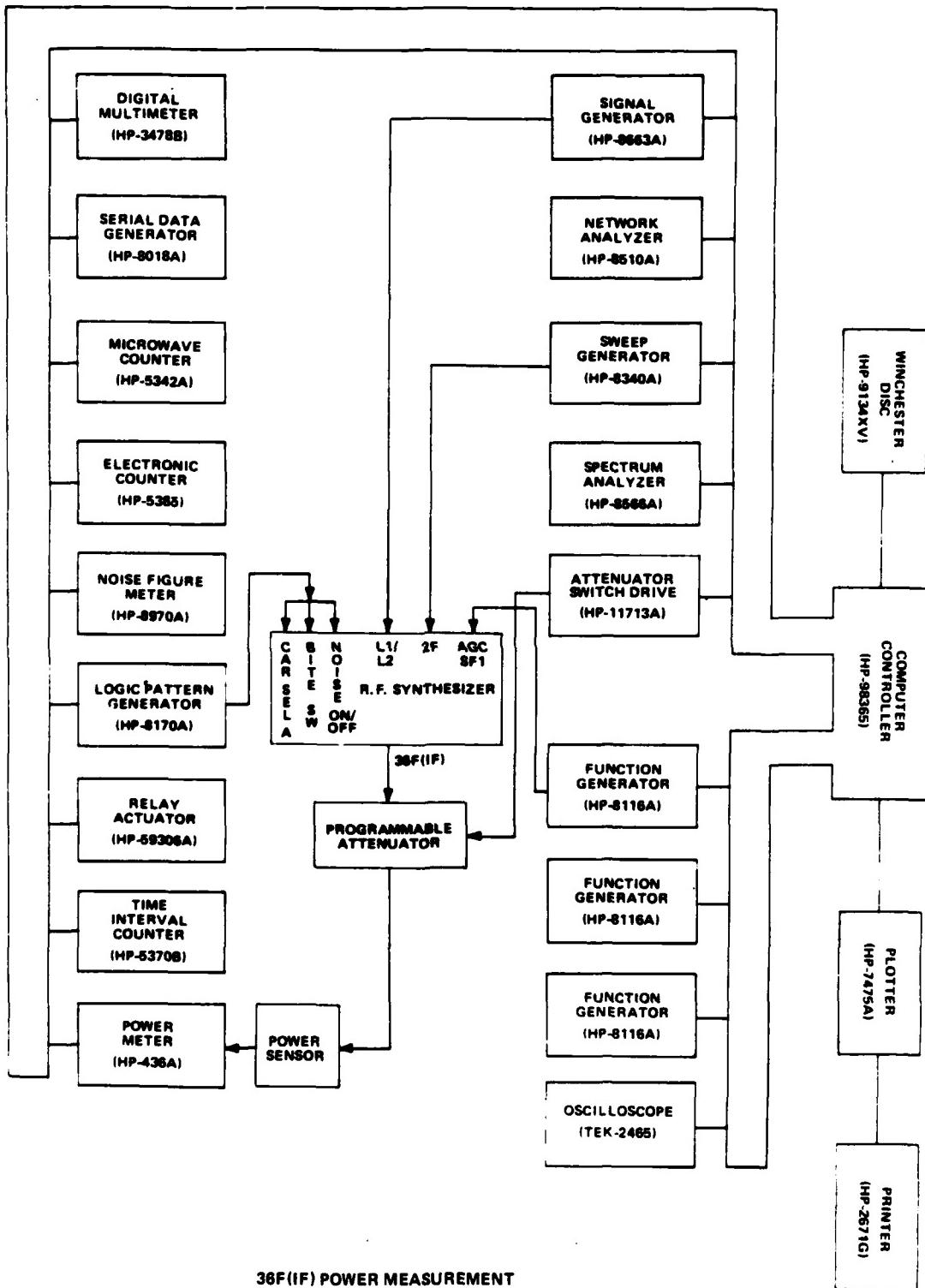
<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. 36F(IF)	-24dBm	Power Meter (HP-436A)
2.		Power Sensor (HP-8481A)
3.		Att. Sw Drive (HP-11713A)
4.		Prog. Att. (HP-8494G)
5.		

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Apply proper signal levels to all inputs of RF Synthesizer. Measure 36F(IF) output power with Power Meter.

Data Reduction: Send input levels and output of Power Meter to printer.

Equipment List:

- | | | |
|--------------------------------|----------------------------|----------------------------------|
| 1. Logic Patt. Gen. (HP-8170A) | 4. Sweep Gen. (HP-8340A) | 7. Printer (HP-2671G) |
| 2. Sig. Gen. (HP-8663A) | 5. Power Meter (HP-436A) | 8. Computer (HP-9836S) |
| 3. Function Gen. (HP-8116A) | 6. Power Sensor (HP-8481A) | 9. Att. Switch Drive (HP-11713A) |
| 10. Prog. Att. (HP-8494G) | | |



Contractor: Magnavox

Board Tested: RF Synthesizer

Test Objective: Measure VSWR and impedance of 36F(IF) output.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	S-Par. Test Set	TBD	Sweep Gen. (HP-8340A)
2.			
3.			
4.			
5.			
6.			
7.			
8.			
9.			
10.			
11.			
12.			

Outputs

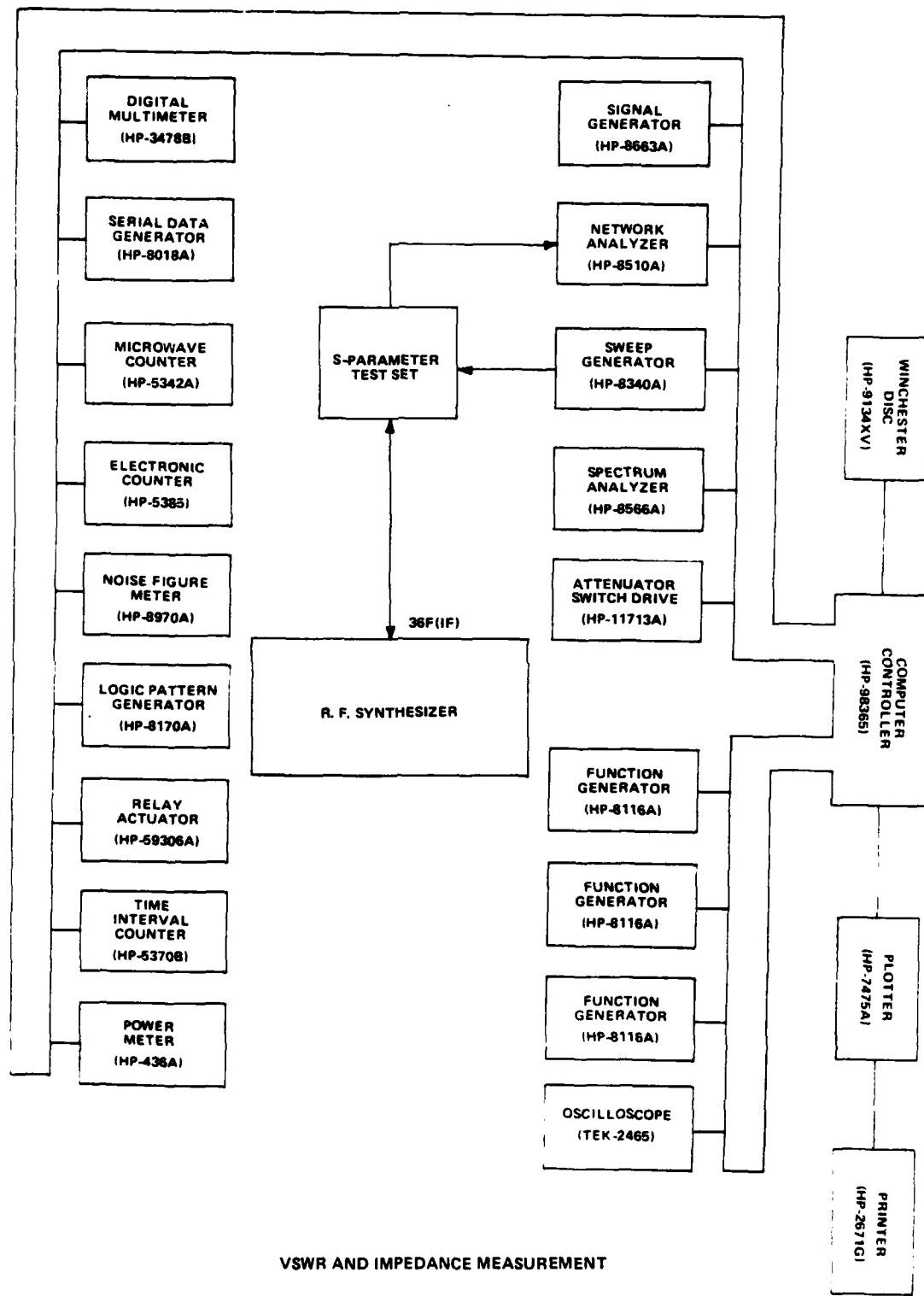
<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. 36F(IF)	TBD	Network Anal. (HP-8510A)
2.		S-Par. Test Set (HP-8515A)
3.		
4.		
5.		

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Apply input signal to S-Parameter Test Set from Sweep Generator. Measure VSWR and impedance with Network Analyzer.

Data Reduction: Send input levels to printer. Output of Network Analyzer to plotter.

Equipment List:

- | | | |
|------------------------|------------------------------|----------|
| 1. Computer (HP-9836S) | 4. S-Par Test Set (HP-8515A) | 7. _____ |
| 2. Printer (HP-2671G) | 5. Network Ana. (HP-8510A) | 8. _____ |
| 3. Plotter (HP-7475A) | 6. Sweep Gen. (HP-8340A) | 9. _____ |



VSWR AND IMPEDANCE MEASUREMENT

Contractor: Magnavox
Board Tested: RF Synthesizer
Test Objective: Measure noise figure of 36F(IF) output.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	Carrier Select A	TBD	Logic Patt. Gen. (HP-8170A)
2.	BITE SW	TBD	Logic Patt. Gen. (HP-8170A)
3.	Noise On/Off	TBD	Logic Patt. Gen. (HP-8170A)
4.	Noise Source	TBD	Noise Source (HP-346B)
5.	2F	10.23MHz @ TBD	Signal Gen. (HP-8663A)
6.	AGC SF 1	TBD	Func. Gen. (HP-8116A)
7.			
8.			
9.			
10.			
11.			
12.			

Outputs

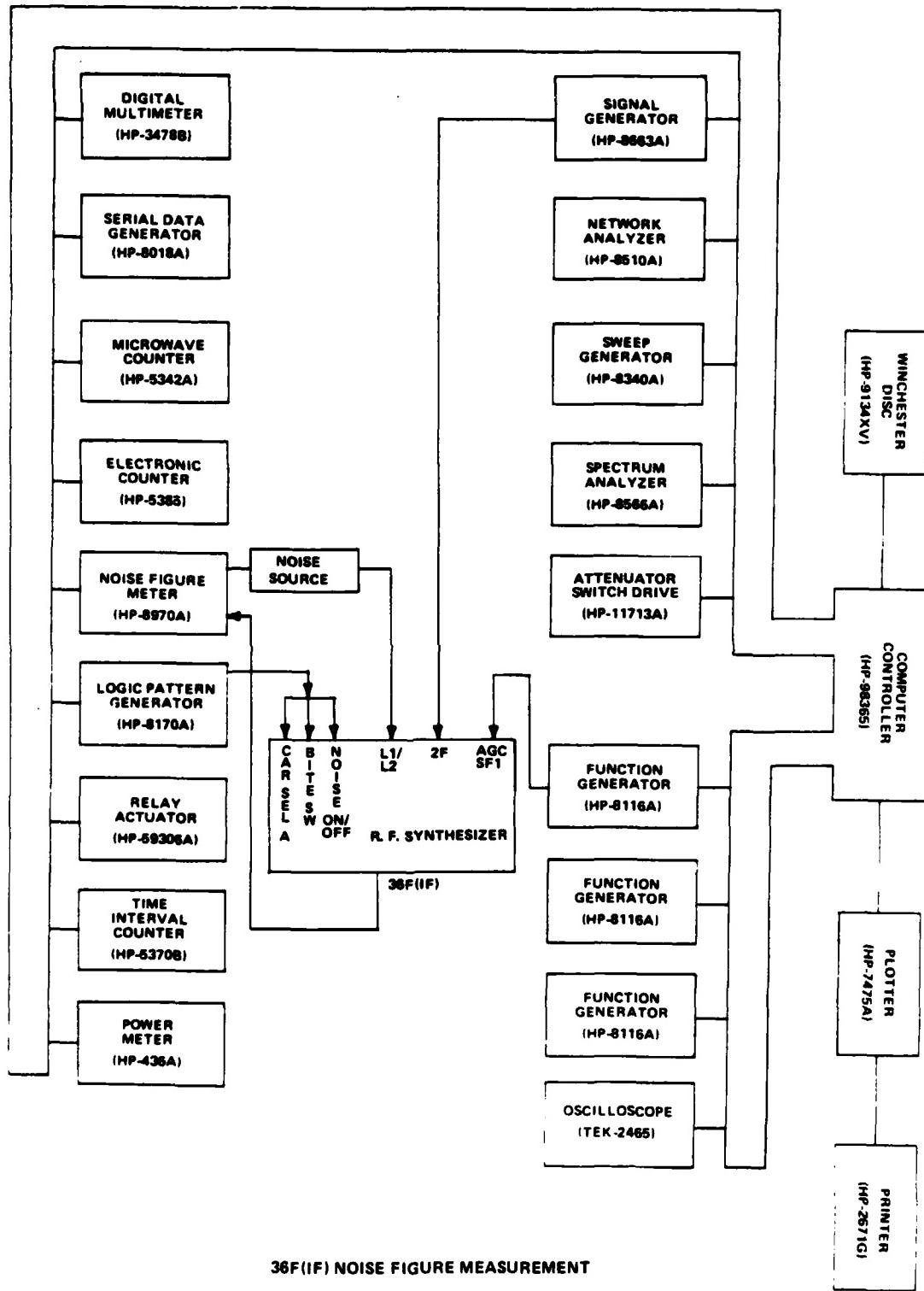
<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. 36F(IF)	TBD	Noise Figure Meter (HP-8970A)
2.		
3.		
4.		
5.		

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Apply proper signal levels to all inputs of RF Synthesizer, including Noise Source. Measure noise figure of 36F(IF) output with Noise Figure Meter.

Data Reduction: Send input levels and output of Noise Figure Meter to printer.

Equipment List:

- | | | |
|--------------------------------|---------------------------|------------------------|
| 1. Logic Patt. Gen. (HP-8170A) | 4. Noise Source (HP-346B) | 7. Computer (HP-9836S) |
| 2. Signal Gen. (HP-8663A) | 5. N.F. Meter (HP-8970A) | 8. |
| 3. Function Gen. (HP-8116A) | 6. Printer (HP-2671G) | 9. |



Contractor: Magnavox

Board Tested: RF Synthesizer

Test Objective: Measure frequency of 68F output.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	2F	10.23MHz @ TBD	Signal Gen. (HP-8663A)
2.			
3.			
4.			
5.			
6.			
7.			
8.			
9.			
10.			
11.			
12.			

Outputs

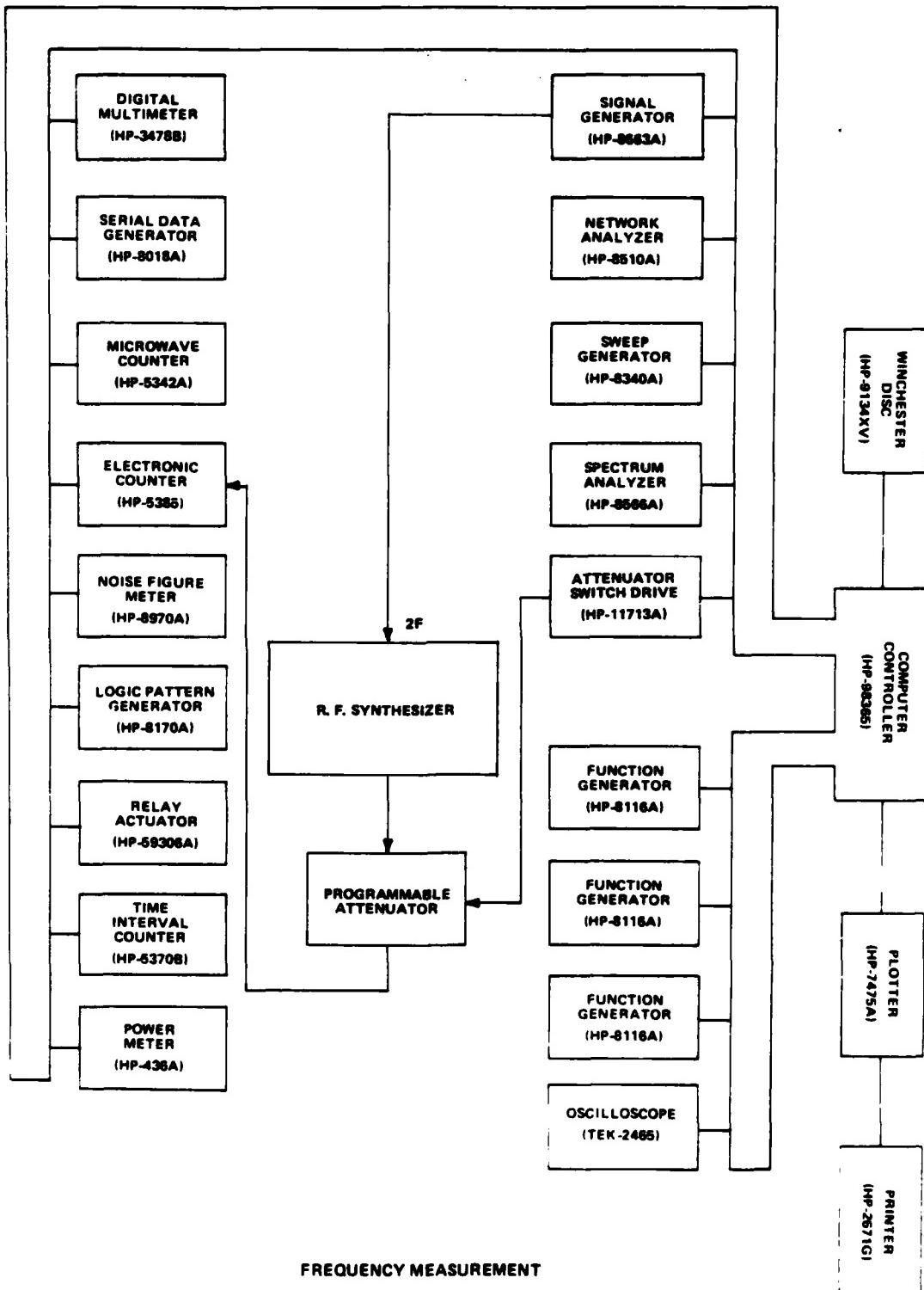
<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. 68F	347.82MHz	Electronic Ctr. (HP-5385)
2.		Att. Switch Drive (HP-11713A)
3.		Prog. Att. (HP-8494G)
4.		
5.		

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Apply a 2F frequency to RF Synthesizer reference oscillator input. Measure output frequency at 68F output with Electronic Counter.

Data Reduction: Send input levels and output of Electronic Counter to printer.

Equipment List:

- | | | |
|----------------------------|------------------------------|----------|
| 1. Signal Gen. (HP-8663A) | 4. Electronic Ctr. (HP-5385) | 7. _____ |
| 2. Att. Sw Dr. (HP-11713A) | 5. Printer (HP-2671G) | 8. _____ |
| 3. Prog. Att. (HP-8494G) | 6. Computer (HP-9836S) | 9. _____ |



Contractor: Magnavox

Board Tested: RF Synthesizer

Test Objective: Measure output power of 68F output.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	2F	10.23MHz @ TBD	Signal Gen. (HP-8663A)
2.			
3.			
4.			
5.			
6.			
7.			
8.			
9.			
10.			
11.			
12.			

Outputs

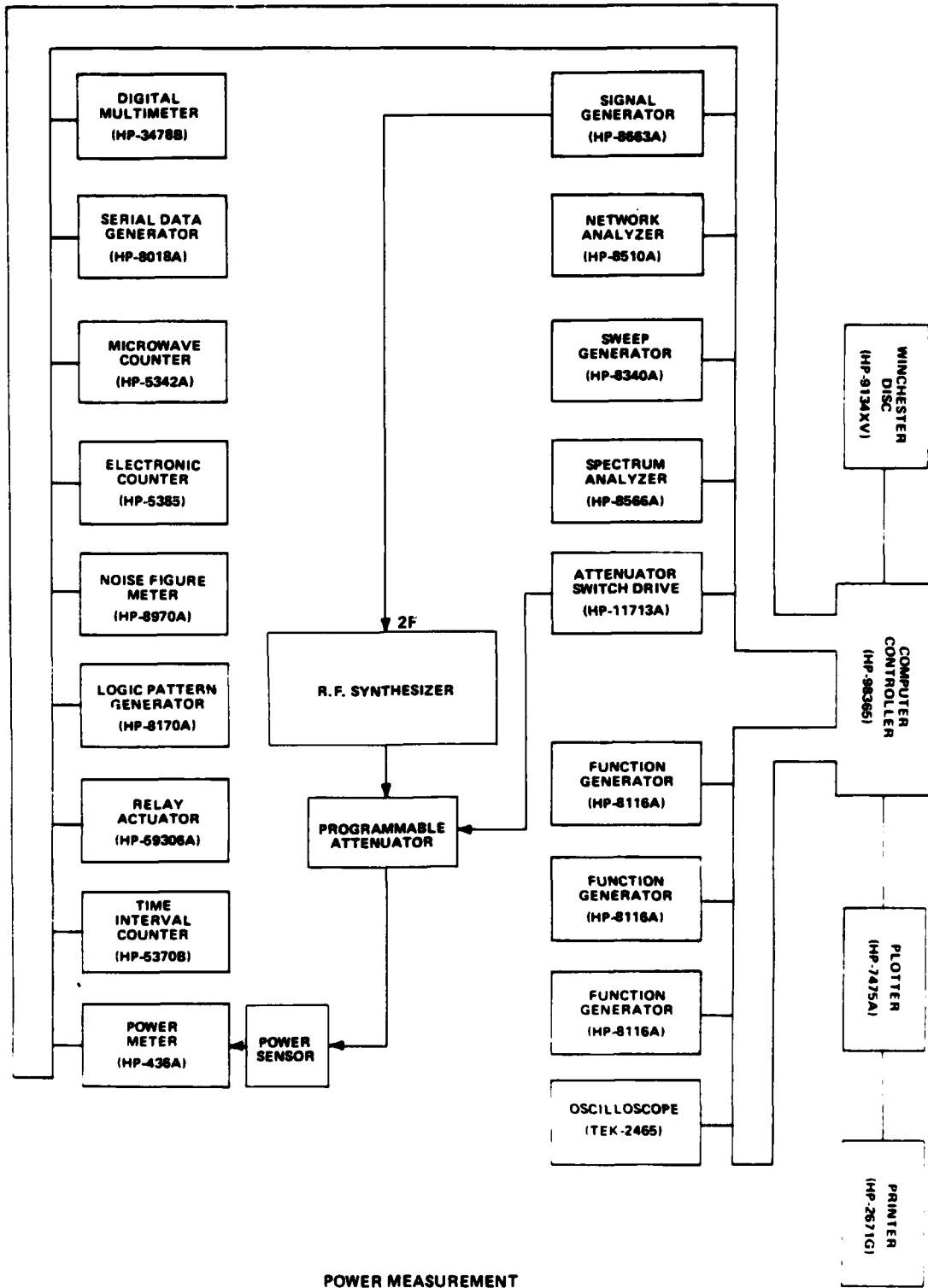
	<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1.	68F	-10dBm	Power Meter (HP-436A)
2.			Power Sensor (HP-8481A)
3.			Att. Sw Drive (HP-11713A)
4.			Prog. Att. (HP-8494G)
5.			

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Apply a 2F signal to the RF Synthesizer's reference oscillator input. Measure output power at 68F output with Power Meter.

Data Reduction: Send input levels and output of Power Meter to printer.

Equipment List:

- | | | |
|----------------------------|----------------------------|------------------------|
| 1. Sig. Gen. (HP-8663A) | 4. Att. Sw Dr. (HP-11713A) | 7. Computer (HP-9836S) |
| 2. Power Meter (HP-436A) | 5. Prog. Att. (HP-8494G) | 8. _____ |
| 3. Power Sensor (HP-8481A) | 6. Printer (HP-2671G) | 9. _____ |



POWER MEASUREMENT

Contractor: Magnavox

Board Tested: RF Synthesizer

Test Objective: Check 68F output for spectral content.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	2F	10.23MHz @ TBD	Sig. Gen. (HP-8663A)
2.			
3.			
4.			
5.			
6.			
7.			
8.			
9.			
10.			
11.			
12.			

Outputs

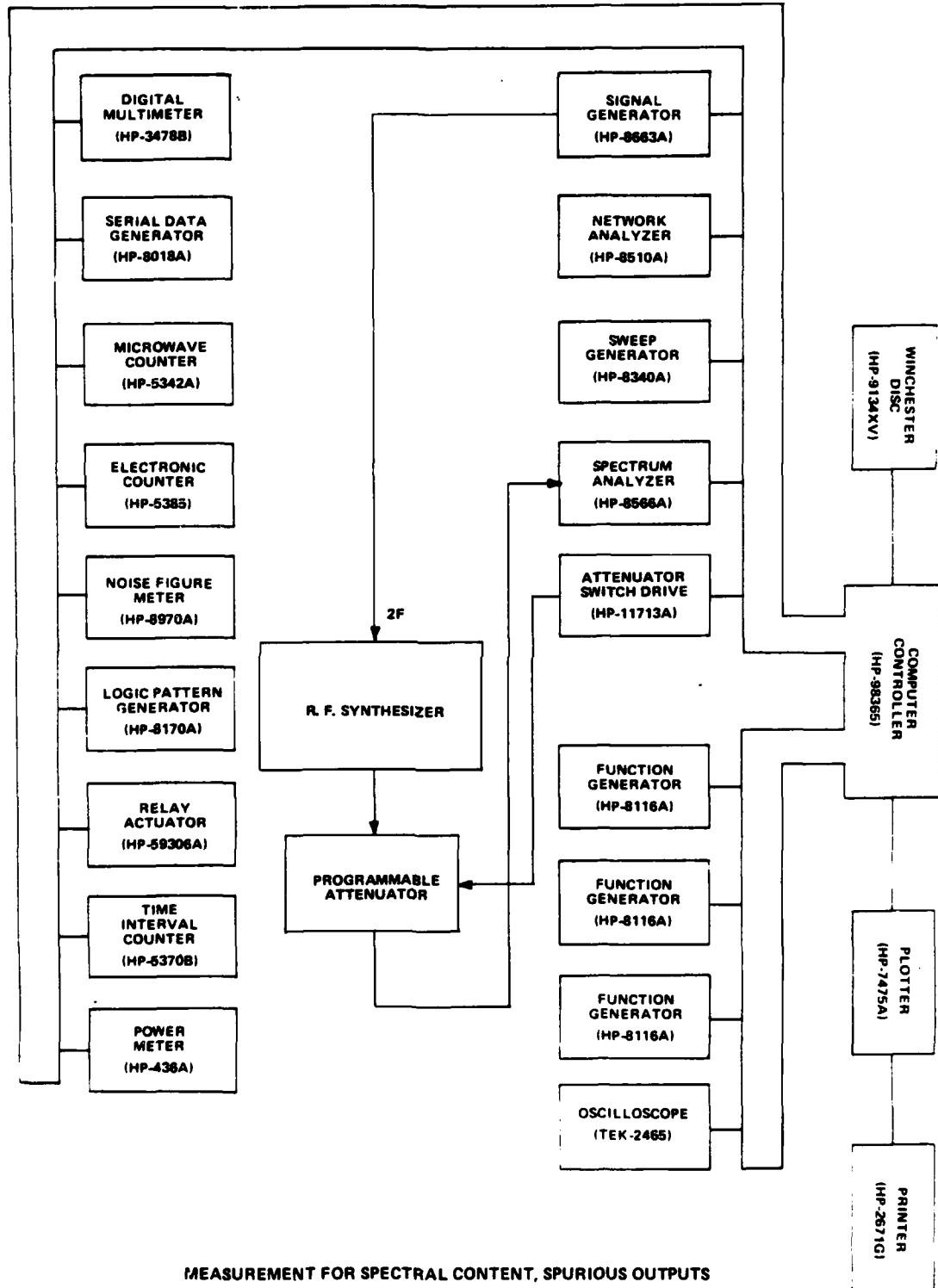
<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. 68F	-10dBm @ 347.82MHz	Spectrum Anal. (HP-8566A)
2.		Att. Sw Drive (HP-11713A)
3.		Prog. Att. (HP-8494G)
4.		
5.		

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Apply a 2F signal to the RF Synthesizer's reference oscillator input. Measure the spectral content of the 68F output with Spectrum Analyzer.

Data Reduction: Send input levels to printer. Output of Spectrum Analyzer to plotter.

Equipment List:

- | | | |
|------------------------------|--------------------------|-----------------------|
| 1. Signal Gen. (HP-8663A) | 4. Prog. Att. (HP-8494G) | 7. Printer (HP-2671G) |
| 2. Spectrum Anal. (HP-8566A) | 5. Plotter (HP-7475A) | 8. |
| 3. Att. Sw Dr. (HP-11713A) | 6. Computer (HP-9836S) | 9. |



MEASUREMENT FOR SPECTRAL CONTENT, SPURIOUS OUTPUTS

Contractor: Magnavox

Board Tested: RF Synthesizer

Test Objective: Measure impedance and VSWR of 68F output.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	S-Par Test Set	TBD	Sweep Gen. (HP-8340A)
2.			
3.			
4.			
5.			
6.			
7.			
8.			
9.			
10.			
11.			
12.			

Outputs

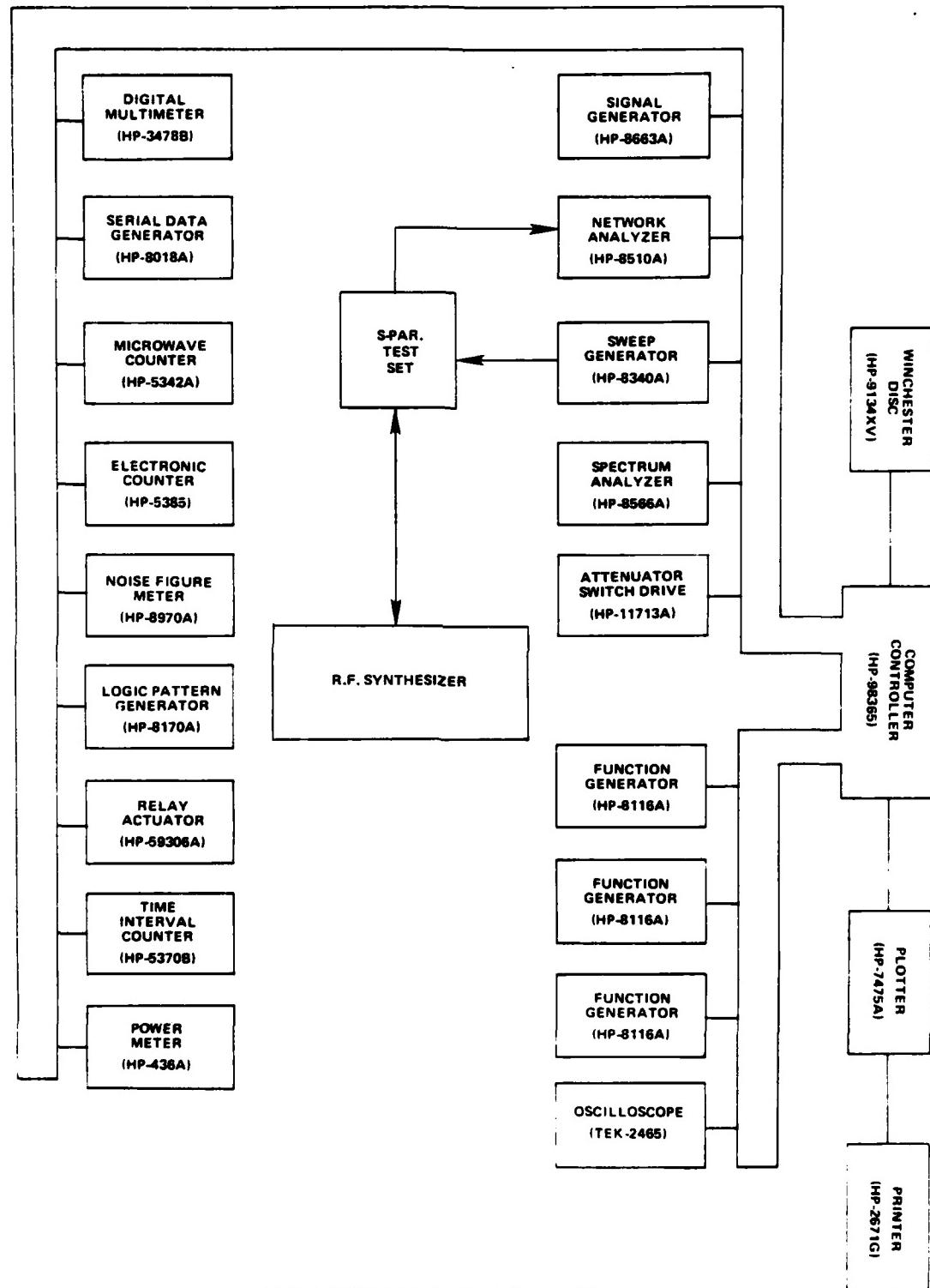
<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. 68F	TBD	Network Anal. (HP-8510A)
2.		S-Par Test Set (HP-8515A)
3.		
4.		
5.		

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Apply input signal to S-Parameter Test Set from Sweep Generator. Measure VSWR and impedance with Network Analyzer.

Data Reduction: Send input levels to printer. Output of Network Analyzer to plotter.

Equipment List:

- | | | |
|------------------------|------------------------------|----|
| 1. Computer (HP-9836S) | 4. S-Par Test Set (HP-8515A) | 7. |
| 2. Printer (HP-2671G) | 5. Network Anal. (HP-8510A) | 8. |
| 3. Plotter (HP-7475A) | 6. Sweep Gen. (HP-8340A) | 9. |



VSWR AND IMPEDANCE MEASUREMENT

Contractor: Magnavox

Board Tested: RF Synthesizer

Test Objective: Measure output frequency of 29⁷/₈F output.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	2F	10.23MHz @ TBD	Signal Gen. (HP-8663A)
2.			
3.			
4.			
5.			
6.			
7.			
8.			
9.			
10.			
11.			
12.			

Outputs

	<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1.	29 ⁷ /8F	152.81MHz	Electronic Ctr (HP-5385)
2.			Att. Sw Dr. (HP-11713A)
3.			Prog. Att. (HP-8494G)
4.			
5.			

Test Procedure: Turn on test equipment. Set measurement equipment

to proper ranges. Apply a 2F signal to the RF Synthesizer's reference oscillator input. Measure output frequency at 29⁷/8F output with Electronic Counter.

Data Reduction: Send input levels and output of Electronic Counter

to printer.

Equipment List:

- | | | |
|-----------------------------|-----------------------------|----------|
| 1. Computer (HP-9836S) | 4. Att. Sw Dr. (HP -11713A) | 7. _____ |
| 2. Sig. Gen. (HP-8663A) | 5. Prog. Att. (HP-8494G) | 8. _____ |
| 3. Electronic Ctr (HP-5385) | 6. Printer (HP-2671G) | 9. _____ |

Contractor: Magnavox

Board Tested: RF Synthesizer

Test Objective: Measure output power at 29⁷/8F output.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	2F	10.23MHz @ TBD	Signal Gen. (HP-8663A)
2.			
3.			
4.			
5.			
6.			
7.			
8.			
9.			
10.			
11.			
12.			

Outputs

<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. <u>29⁷/8F</u>	<u>-10dBm</u>	<u>Power Meter (HP-436A)</u>
2.		<u>Power Sensor (HP-8481A)</u>
3.		<u>Att. Sw Dr. (HP-11713A)</u>
4.		<u>Prog. Att. (HP-8494G)</u>
5.		

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Apply a 2F signal to the RF Synthesizer's reference oscillator input. Measure output power at 29⁷/8F output with Power Meter.

Data Reduction: Send input levels and output of Power Meter to printer.

Equipment List:

- | | | |
|---------------------------------|-----------------------------------|------------------------------|
| 1. <u>Computer (HP-9836S)</u> | 4. <u>Power Sensor (HP-8481A)</u> | 7. <u>Printer (HP-2671G)</u> |
| 2. <u>Sig. Gen. (HP-8663A)</u> | 5. <u>Att. Sw Dr. (HP-11713A)</u> | 8. _____ |
| 3. <u>Power Meter (HP-436A)</u> | 6. <u>Prog. Att. (HP-8494G)</u> | 9. _____ |

Contractor: Magnavox

Board Tested: RF Synthesizer

Test Objective: Measure spectral content of 29⁷/8F output.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	2F	10.23MHz @ TBD	Signal Gen. (HP-8663A)
2.			
3.			
4.			
5.			
6.			
7.			
8.			
9.			
10.			
11.			
12.			

Outputs

<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. <u>29⁷/8F</u>	<u>-10dBm @ 152.81MHz</u>	<u>Spectrum Anal. (HP-8566A)</u>
2.		<u>Prog. Att. (HP-8494G)</u>
3.		<u>Att. Sw Dr. (HP-11713A)</u>
4.		
5.		

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Apply a 2F signal to the RF Synthesizer's reference oscillator input. Measure spectral content of 29⁷/8F output on Spectrum Analyzer.

Data Reduction: Send input levels to printer. Output of Spectrum Analyzer to plotter.

Equipment List:

- | | | |
|------------------------------|----------------------------|-----------------------|
| 1. Computer (HP-9836S) | 4. Att. Sw Dr. (HP-11713A) | 7. Printer (HP-2671G) |
| 2. Sig. Gen. (HP-8663A) | 5. Prog. Att. (HP-8494G) | 8. |
| 3. Spectrum Anal. (HP-8566A) | 6. Plotter (HP-7475A) | 9. |

Contractor: Magnavox
Board Tested: RF Synthesizer
Test Objective: Measure output impedance and VSWR of 29⁷/₈F output.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	S-Par Test Set	TBD	Sweep Gen. (HP-8340A)
2.			
3.			
4.			
5.			
6.			
7.			
8.			
9.			
10.			
11.			
12.			

Outputs

<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. <u>29⁷/8F</u>	<u>TBD</u>	<u>Network Analyzer (HP-8510A)</u>
2.		<u>S-Par Test Set (HP-8515A)</u>
3.		
4.		
5.		

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Apply input signal to S-Parameter Test Set from Sweep Generator. Measure VSWR and impedance with Network Analyzer.

Data Reduction: Send input levels to printer. Output of Network Analyzer to plotter.

Equipment List:

1. Computer (HP-9836S)
2. Printer (HP-2671C)
3. Plotter (HP-7475A)
4. S-Par Test Set (HP-8515A)
5. Network Anal. (HP-8510A)
6. Sweep Gen. (HP-8340A)
- 7.
- 8.
- 9.

Contractor: Magnavox
Board Tested: RF Synthesizer
Test Objective: Verify AF1 LOCK operational.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	2F	10.23MHz @ TBD	Signal Gen. (HP-8663A)
2.			
3.			
4.			
5.			
6.			
7.			
8.			
9.			
10.			
11.			
12.			

Outputs

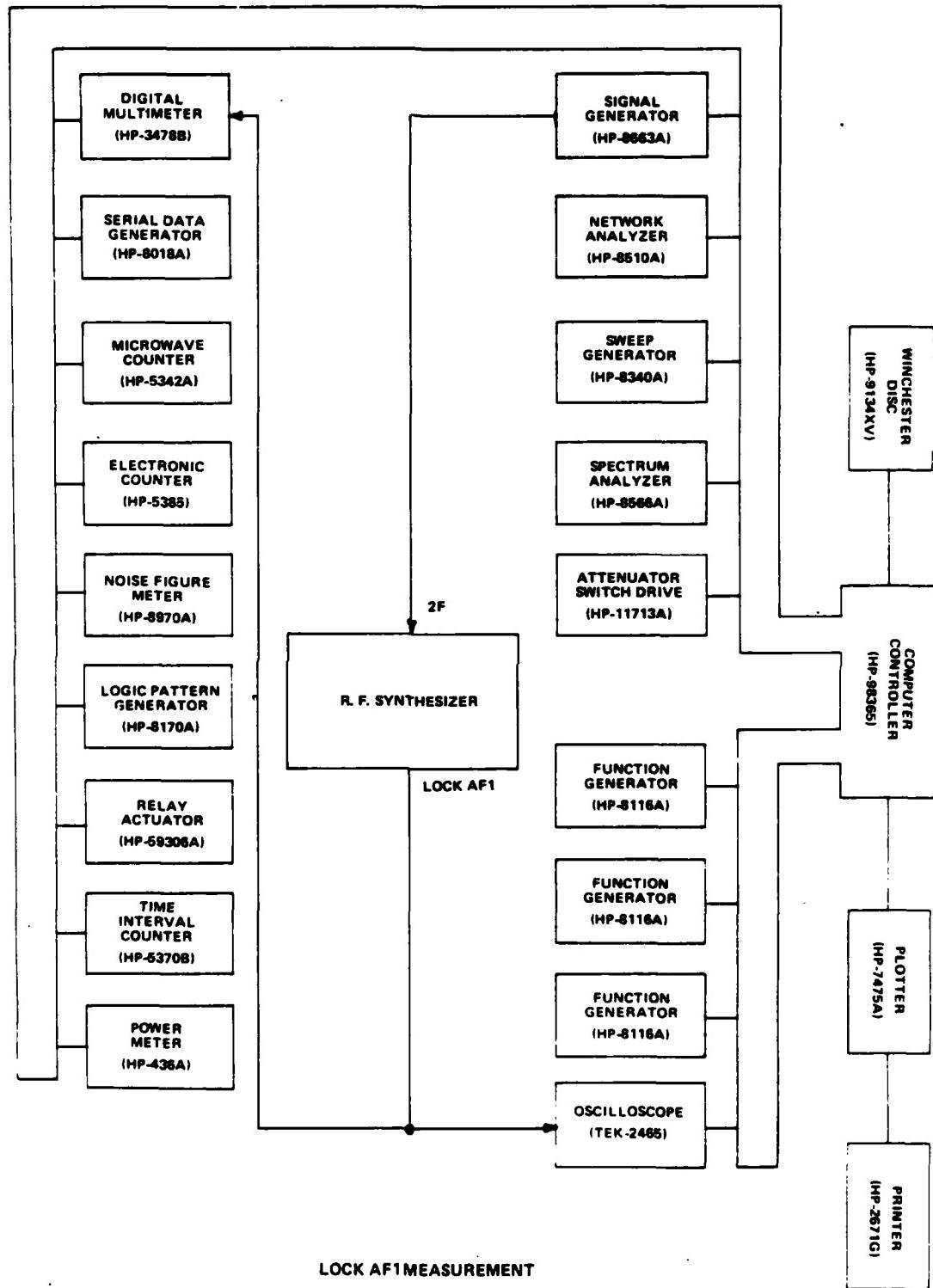
<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. <u>LOCK AF1</u>	<u>TBD</u>	<u>Digital Multi. (HP-3478)</u>
2.		<u>Oscilloscope (TEK-2465)</u>
3.		
4.		
5.		

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Apply a 2F signal to the RF Synthesizer's reference oscillator input. Measure AF1 LOCK with Digital Multimeter. An Oscilloscope will be used to display waveform.

Data Reduction: Send input levels and output of Digital Multimeter to printer. Output of Oscilloscope to plotter.

Equipment List:

- | | | |
|-------------------------------------|-----------------------------------|----------|
| 1. <u>Computer (HP-9836S)</u> | 4. <u>Printer (HP-2671G)</u> | 7. _____ |
| 2. <u>Sig. Gen. (HP-8663A)</u> | 5. <u>Oscilloscope (TEK-2465)</u> | 8. _____ |
| 3. <u>Digital Multi. (HP-3478B)</u> | 6. <u>Plotter (HP-7475A)</u> | 9. _____ |



Contractor: Magnavox

Board Tested: RF Synthesizer

Test Objective: Measure output frequency at 6F output.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	2F	10.23MHz @ TBD	Signal Gen. (HP-8663A)
2.			
3.			
4.			
5.			
6.			
7.			
8.			
9.			
10.			
11.			
12.			

Outputs

	<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1.	6F	30.69MHz	Electronic Ctr (HP-5385)
2.			Att. Sw Drive (HP-11713A)
3.			Prog. Att. (HP-8494G)
4.			
5.			

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Apply a 2F signal to the RF Synthesizer's reference oscillator input. Measure output frequency at 6F output with Electronic Counter.

Data Reduction: Send input levels and output of Electronic Counter to printer.

Equipment List:

- | | | |
|-------------------------|----------------------------|----------|
| 1. Computer (HP-9836S) | 4. Att. Sw Dr. (HP-11713A) | 7. _____ |
| 2. Sig. Gen. (HP-8663A) | 5. Prog. Att. (HP-8494G) | 8. _____ |
| 3. Elec. Ctr. (HP-5385) | 6. Printer (HP-2671G) | 9. _____ |

Contractor: Magnavox

Board Tested: RF Synthesizer

Test Objective: Measure output power at 6F output.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	2F	10.23MHz @ TBD	Signal Gen. (HP-8663A)
2.			
3.			
4.			
5.			
6.			
7.			
8.			
9.			
10.			
11.			
12.			

Outputs

<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. <u>6F</u>	<u>-30dBm</u>	<u>Power Meter (HP-436A)</u>
2.		<u>Power Sensor (HP-8481A)</u>
3.		<u>Att. Sw Dr. (HP-11713A)</u>
4.		<u>Prog. Att. (HP-8494G)</u>
5.		

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Apply a 2F signal to the RF Synthesizer's reference oscillator input. Measure output power at 6F output with Power Meter.

Data Reduction: Send input levels and output of Power Meter to printer.

Equipment List:

- | | | |
|---------------------------------|-----------------------------------|------------------------------|
| 1. <u>Computer (HP-9836S)</u> | 4. <u>Power Sensor (HP-8481A)</u> | 7. <u>Printer (HP-2671G)</u> |
| 2. <u>Sig. Gen. (HP-8663A)</u> | 5. <u>Att. Sw Dr. (HP-11713A)</u> | 8. _____ |
| 3. <u>Power Meter (HP-436A)</u> | 6. <u>Prog. Att. (HP-8494G)</u> | 9. _____ |

Contractor: Magnavox

Board Tested: RF Synthesizer

Test Objective: Measure spectral content of 6F output.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	2F	10.23MHz @ TBD	Signal Gen. (HP-8663A)
2.			
3.			
4.			
5.			
6.			
7.			
8.			
9.			
10.			
11.			
12.			

Outputs

<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. 6F	-30dBm @ 30.69MHz	Spectrum Anal. (HP-8566A)
2.		Att. Sw Drive (HP-11713A)
3.		Prog. Att. (HP-8494G)
4.		
5.		

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Apply a 2F signal to the RF Synthesizer's reference oscillator input. Measure spectral content of 6F output on Spectrum analyzer.

Data Reduction: Signal input levels to printer. Output of Spectrum Analyzer to plotter.

Equipment List:

- | | | |
|------------------------------|----------------------------|-----------------------|
| 1. Computer (HP-9836S) | 4. Att. Sw Dr. (HP-11713A) | 7. Printer (HP-2671G) |
| 2. Sig. Gen. (HP-8663A) | 5. Prog. Att. (HP-8494G) | 8. |
| 3. Spectrum Anal. (HP-8566A) | 6. Plotter (HP-7475A) | 9. |

Contractor: Magnavox

Board Tested: RF Synthesizer

Test Objective: Measure output frequency at 2F output.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	2F	10.23MHz @ TBD	Signal Gen. (HP-8663A)
2.			
3.			
4.			
5.			
6.			
7.			
8.			
9.			
10.			
11.			
12.			

Outputs

<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. 2F	10.23MHz	Electronic Ctr (HP-5385)
2.		Att. Sw Drive (HP-11713A)
3.		Prog. Att. (HP-8494G)
4.		
5.		

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Apply a 2F signal to the RF Synthesizer's reference oscillator input. Measure output frequency at 2F output with Electronic Counter.

Data Reduction: Send input levels and output of Electronic Counter to printer.

Equipment List:

- | | | |
|-----------------------------|----------------------------|----------|
| 1. Computer (HP-9836S) | 4. Att. Sw Dr. (HP-11713A) | 7. _____ |
| 2. Signal Gen. (HP-8663A) | 5. Prog. Att. (HP-8494) | 8. _____ |
| 3. Electronic Ctr (HP-5385) | 6. Printer (HP-2671G) | 9. _____ |

Contractor: Magnavox

Board Tested: RF Synthesizer

Test Objective: Measure output power at 2F output.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	2F	10.23MHz @ TBD	Signal Gen. (HP-8663A)
2.			
3.			
4.			
5.			
6.			
7.			
8.			
9.			
10.			
11.			
12.			

Outputs

<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. 2F	TBD	Power Meter (HP-436A)
2.		Power Sensor (HP-8481A)
3.		Att. Sw Dr. (HP-11713A)
4.		Prog. Att. (HP-8494G)
5.		

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Apply a 2F signal to the RF Synthesizer's reference oscillator input. Measure output power at 2F output with Power Meter.

Data Reduction: Send input levels and output of Power Meter to printer.

Equipment List:

- | | | |
|--------------------------|----------------------------|-----------------------|
| 1. Computer (HP-9836S) | 4. Power Sensor (HP-8481A) | 7. Printer (HP-2671G) |
| 2. Sig. Gen. (HP-8663A) | 5. Att. Sw Dr. (HP-11713A) | 8. |
| 3. Power Meter (HP-436A) | 6. Prog. Att. (HP-8494G) | 9. |

Contractor: Magnavox

Board Tested: RF Synthesizer

Test Objective: Measure spectral content of 2F output.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	2F	10.23MHz @ TBD	Signal Gen. (HP-8663A)
2.			
3.			
4.			
5.			
6.			
7.			
8.			
9.			
10.			
11.			
12.			

Outputs

<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. 2F	30.69MHz @ TBD	Spectrum Anal. (HP-8566A)
2.		Prog. Att. (HP-8494G)
3.		Att. Sw Drive (HP-11713A)
4.		
5.		

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Apply a 2F signal to the RF Synthesizer's reference oscillator input. Measure spectral content at 2F output with Spectrum Analyzer.

Data Reduction: Send input levels to printer. Output of Spectrum Analyzer to plotter.

Equipment List:

- | | | |
|------------------------------|----------------------------|-----------------------|
| 1. Computer (HP-9836S) | 4. Att. Sw Dr. (HP-11713A) | 7. Printer (HP-2671G) |
| 2. Sig. Gen. (HP-8663A) | 5. Prog. Att. (HP-8494G) | 8. |
| 3. Spectrum Anal. (HP-8566A) | 6. Plotter (HP-7475A) | 9. |

Contractor: Magnavox

Board Tested: RF Synthesizer

Test Objective: Verify 2F -F/154, 120 is operational.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	2F	10.23MHz @ TBD	Signal Gen. (HP-8663A)
2.	F/154, F/120	TBD	Function Gen. (HP-8116A)
3.			
4.			
5.			
6.			
7.			
8.			
9.			
10.			
11.			
12.			

Outputs

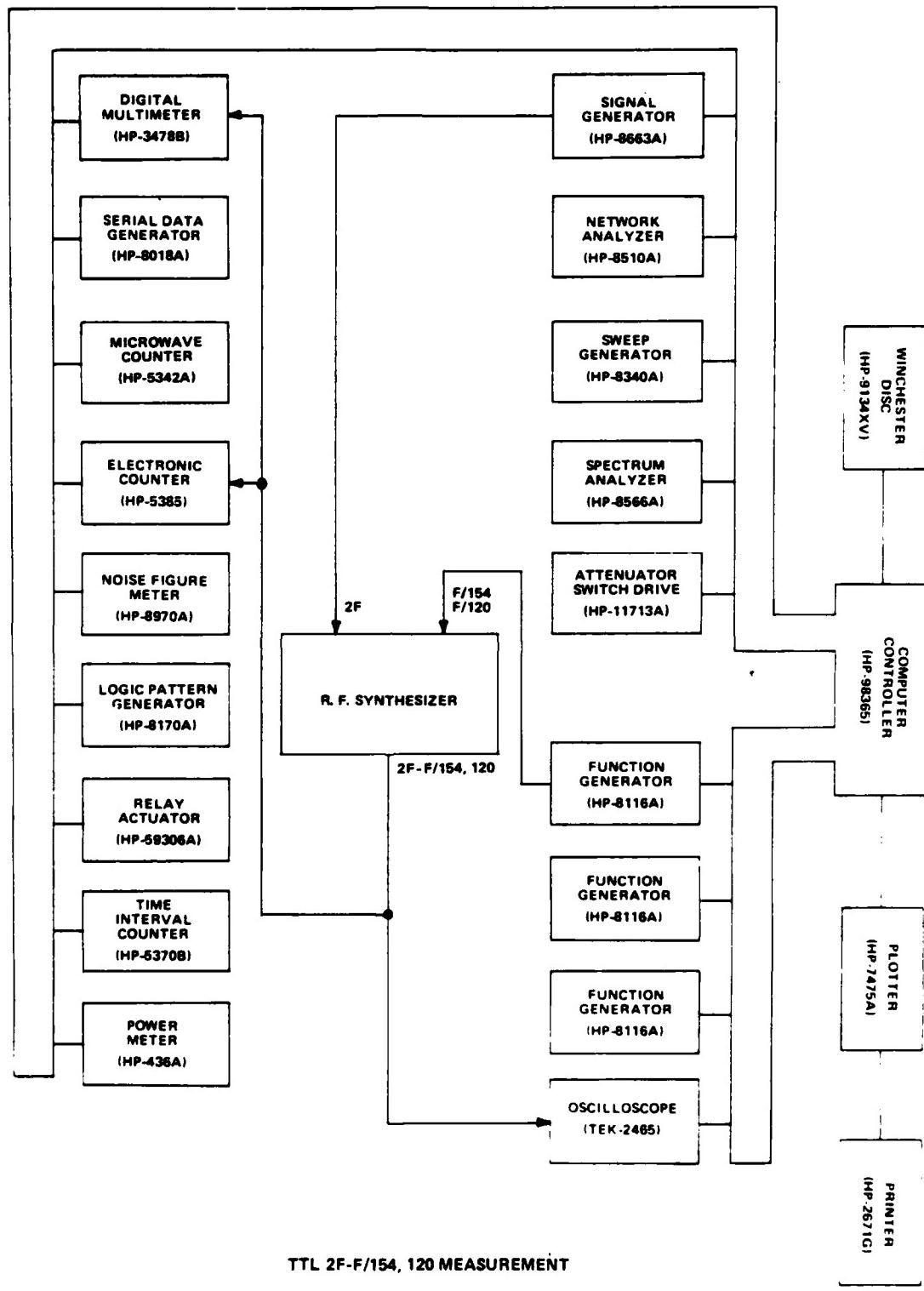
<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. <u>2F -F/154, 120</u>	<u>TBD</u>	<u>Digital Multi. (HP-3478B)</u>
2.		<u>Electronic Ctr (HP-5385)</u>
3.		<u>Oscilloscope (TEK-2465)</u>
4.		
5.		

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Apply proper signal levels to all inputs. Measure output frequency at 2F -F/154, 120 output with Electronic Counter. Measure signal strength with Digital Multimeter and an Oscilloscope will be used to display waveform.

Data Reduction: Send input levels and outputs of Electronic Counter and Digital Multimeter to printer. Output of Oscilloscope to plotter.

Equipment List:

- | | | |
|-------------------------------------|------------------------------------|-------------------------------|
| 1. <u>Sig. Gen. (HP-8663A)</u> | 4. <u>Electronic Ctr (HP-5385)</u> | 7. <u>Computer (HP-9836S)</u> |
| 2. <u>Func. Gen. (HP-8116A)</u> | 5. <u>Printer (HP-2671G)</u> | 8. <u>Plotter (HP-7475A)</u> |
| 3. <u>Digital Multi. (HP-3478B)</u> | 6. <u>Oscilloscope (TEK-2465)</u> | 9. _____ |



TTL 2F-F/154, 120 MEASUREMENT

3.1.1.2 CONVERTER/CHANNEL SELECTOR MODULE

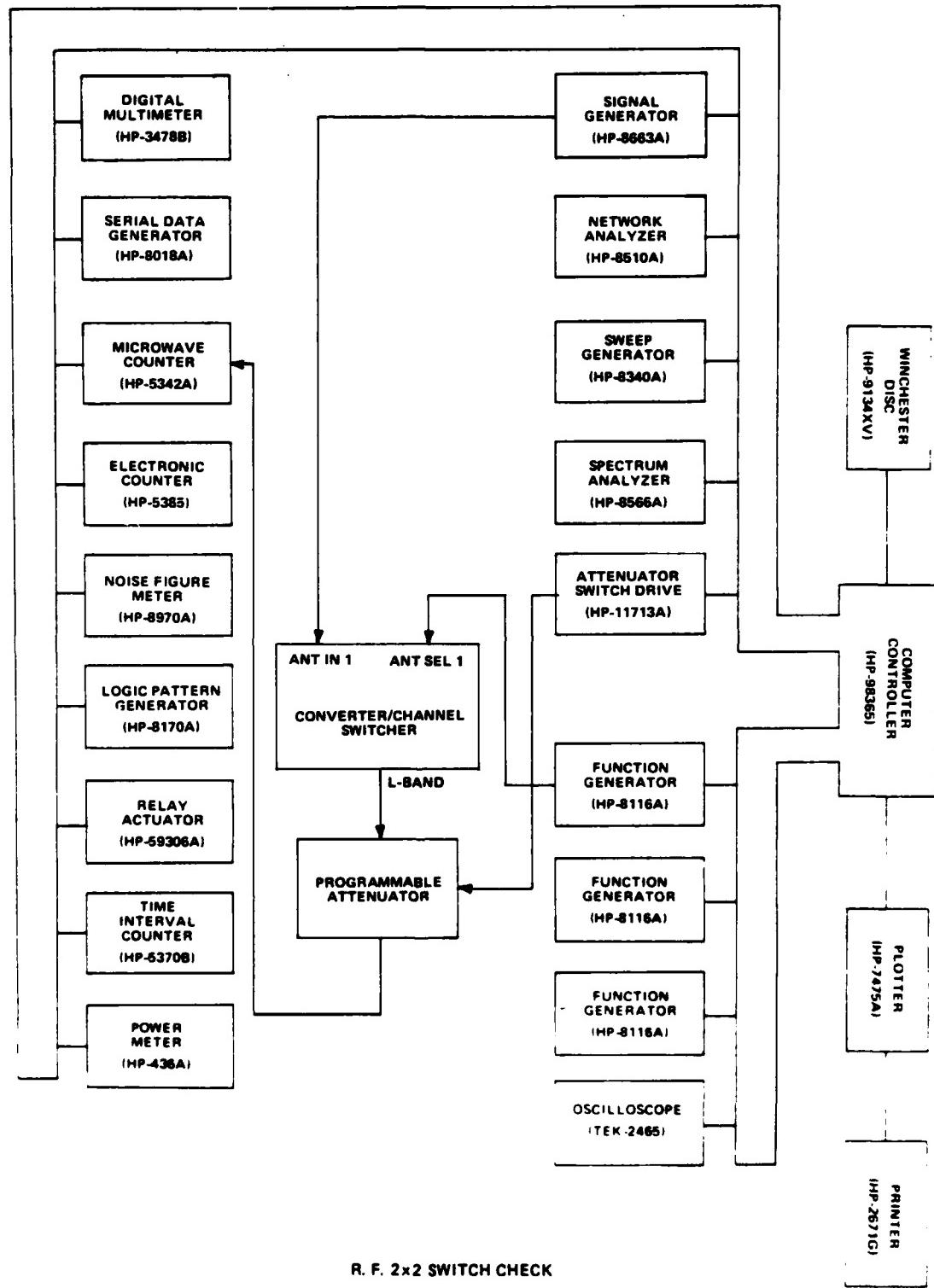
Contractor: Magnavox

Board Tested: Converter/Channel Switch

Test Objective: Verify RF 2 x 2 switch operation.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	Antenna Select 1	TBD	Function Gen. (HP-8116A)
2.	Antenna IN 1	-120dBm @ L1/L2	Signal Gen. (HP-8663A)
3.			
4.			
5.			
6.			
7.			
8.			
9.			
10.			
11.			
12.			



Contractor: Magnavox
Board Tested: Converter/Channel Switch
Test Objective: To verify AGC GAIN AF1 is operational.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	Antenna Select 1	TBD	Logic Patt. Gen. (HP-8170A)
2.	Antenna Select 2	TBD	Logic Patt. Gen. (HP-8170A)
3.	20 MUT	TBD	Logic Patt. Gen. (HP-8170A)
4.	1 UT	TBD	Logic Patt. Gen. (HP-8170A)
5.	BITE SW, A, B, C	TBD	Logic Patt. Gen. (HP-8170A)
6.	Carrier Select B	TBD	Logic Patt. Gen. (HP-8170A)
7.	L1/L2	-120dBm @ L1/L2	Signal Generator (HP-8663A)
8.	68F	347.82MHz @ -10dBm	Sweep Generator (HP-8340A)
9.	AGC S/F 2	TBD	Function Gen. (HP-8116A)
10.			
11.			
12.			

Outputs

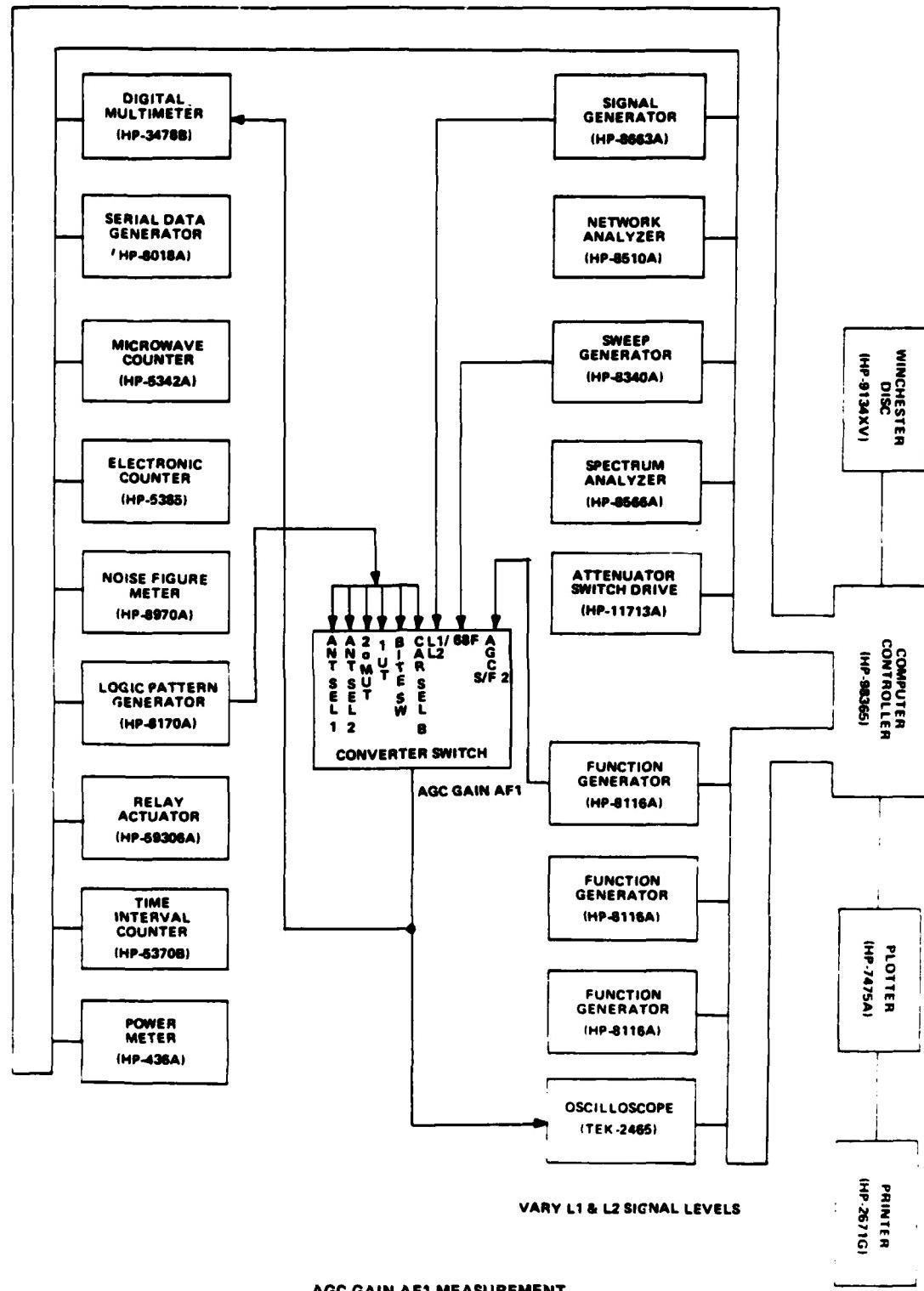
<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. <u>AGC GAIN AF1</u>	<u>TBD</u>	<u>Digital Multi. (HP-3478B)</u>
2.		<u>Oscilloscope (TEK-2465)</u>
3.		
4.		
5.		

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Apply proper signals to all inputs. Measure AGC GAIN AF1 output with Digital Multimeter to verify operation. An Oscilloscope will also be used to display waveform.

Data Reduction: Send input levels and output of Digital Multimeter to printer. Output of Oscilloscope to plotter.

Equipment List:

- | | | |
|---------------------------------------|-------------------------------------|-----------------------------------|
| 1. <u>Logic Patt. Gen. (HP-8170A)</u> | 4. <u>Sweep Gen. (HP-8340A)</u> | 7. <u>Computer (HP-9836S)</u> |
| 2. <u>Func. Gen. (HP-8116A)</u> | 5. <u>Digital Multi. (HP-3478B)</u> | 8. <u>Oscilloscope (TEK-2465)</u> |
| 3. <u>Signal Gen. (HP-8663A)</u> | 6. <u>Printer (HP-2671G)</u> | 9. <u>Plotter (HP-7475A)</u> |



Contractor: Magnavox
Board Tested: Converter/Channel Switch
Test Objective: Verify 36F(IF) switch operation.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	Antenna Select 1	TBD	Logic Patt. Gen. (HP-8170A)
2.	Antenna Select 2	TBD	Logic Patt. Gen. (HP-8170A)
3.	20 MUT	TBD	Logic Patt. Gen. (HP-8170A)
4.	1 UT	TBD	Logic Patt. Gen. (HP-8170A)
5.	BITE SW A, B, C	TBD	Logic Patt. Gen. (HP-8170A)
6.	Carrier Select B	TBD	Logic Patt. Gen. (HP-8170A)
7.	RF Select Ø	TBD	Logic Patt. Gen. (HP-8170A)
8.	L1/L2	-120dBm @ L1/L2	Signal Generator (HP-8663A)
9.	68F	-10dBm @ 347.82MHz	Sweep Generator (HP-8340A)
10.	36F	-24dBm @ 184.14MHz	External Oscillator
11.	AGC S/F 2	TBD	Function Gen. (HP-8116A)
12.			

Outputs

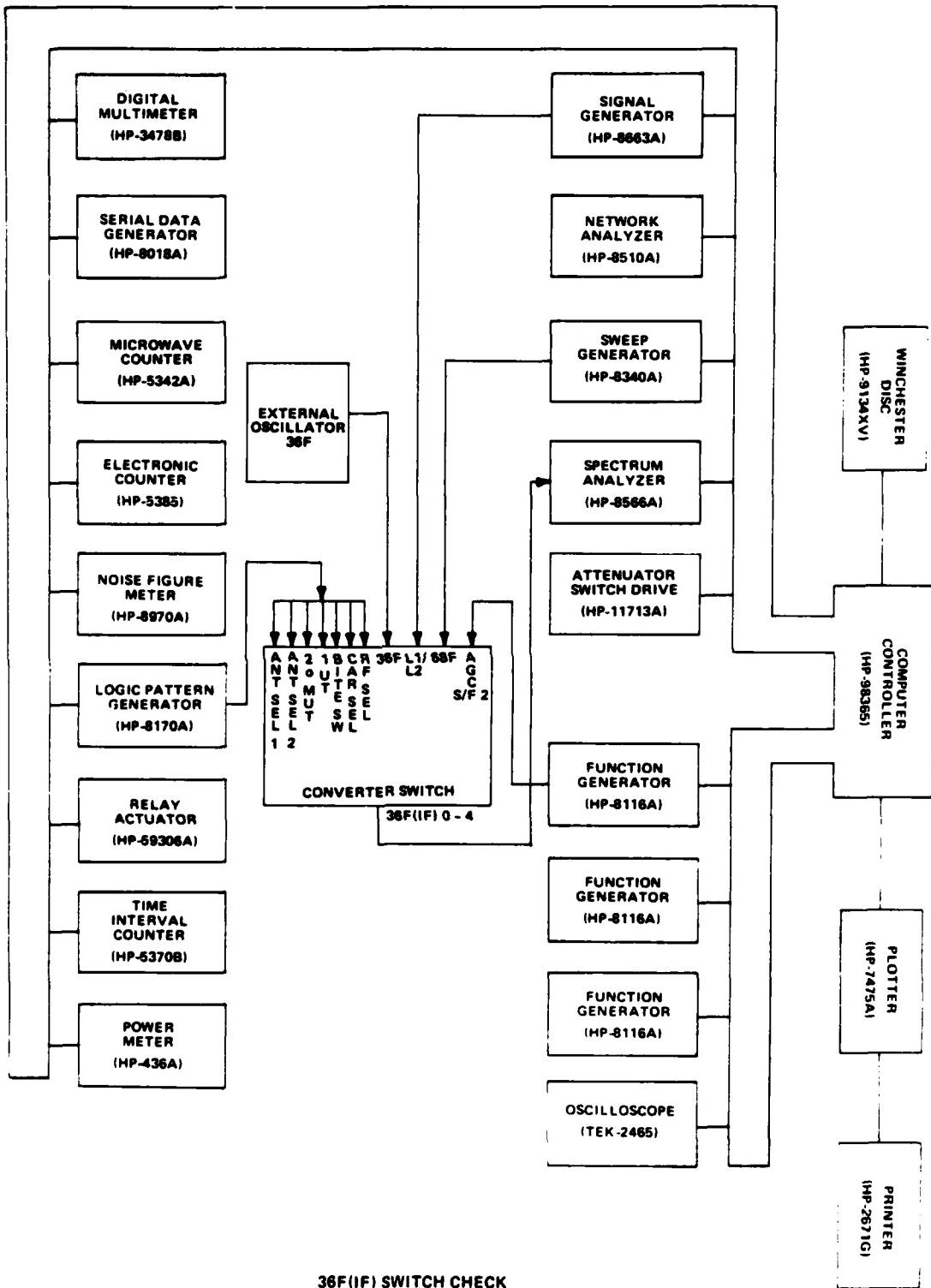
<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. <u>36F(IF) Output</u>	<u>-24dBm @ 184.14MHz</u>	<u>Spectrum Anal. (HP-8556A)</u>
2.		
3.		
4.		
5.		

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Apply proper signals to all inputs. Measure output frequency and power on Spectrum Analyzer to verify switch operation.
(Repeat for all 5 channels by changing RF Select Input)

Data Reduction: Send input levels to printer. Output of Spectrum Analyzer to plotter.

Equipment List:

- | | | |
|---------------------------------------|-------------------------------------|-------------------------------|
| 1. <u>Logic Patt. Gen. (HP-8170A)</u> | 4. <u>Func. Gen. (HP-8116A)</u> | 7. <u>Computer (HP-9836S)</u> |
| 2. <u>Signal Gen. (HP-8663A)</u> | 5. <u>Spectrum Anal. (HP-8566A)</u> | 8. <u>External Oscillator</u> |
| 3. <u>Sweep Gen. (HP-8340A)</u> | 6. <u>Plotter (HP-7475A)</u> | 9. <u>Printer (HP-2671G)</u> |



36F(IF) SWITCH CHECK

Contractor:

Magnavox

Board Tested:

Converter/Channel Switch

Test Objective:

Verify operation of Carrier Select outputs.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	RF Select 0	TBD	Logic Patt. Gen. (HP-8170A)
2.	RF Select 1	TBD	Logic Patt. Gen. (HP-8170A)
3.	RF Select 2	TBD	Logic Patt. Gen. (HP-8170A)
4.	RF Select 3	TBD	Logic Patt. Gen. (HP-8170A)
5.	RF Select 4	TBD	Logic Patt. Gen. (HP-8170A)
6.	Carrier Select A	TBD	Logic Patt. Gen. (HP-8170A)
7.	Carrier Select B	TBD	Logic Patt. Gen. (HP-8170A)
8.			
9.			
10.			
11.			
12.			

Outputs

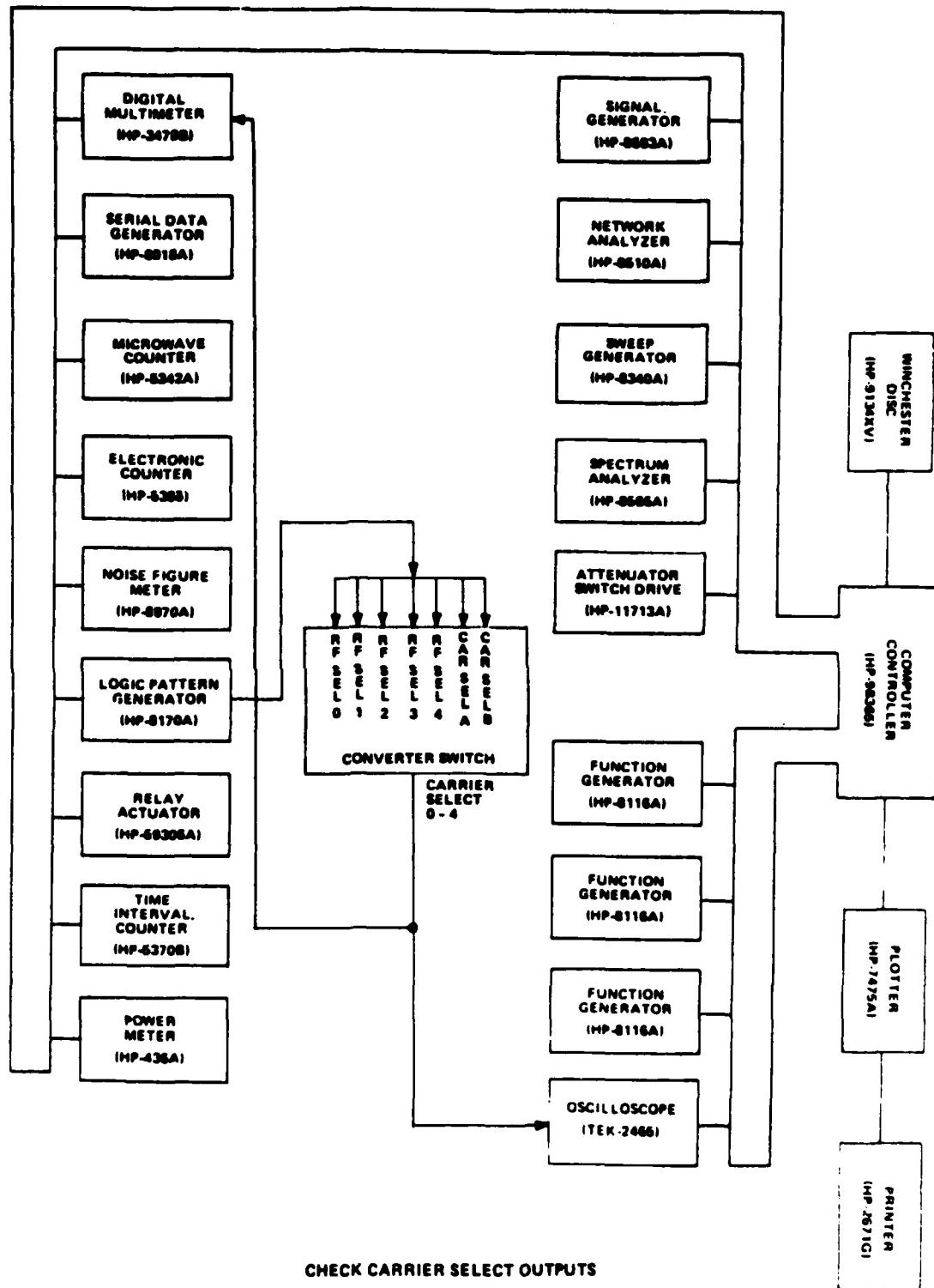
<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. Carrrier Select 0-4	TBD	Digital Multi. (HP-3478B)
2.		Oscilloscope (TEK-2465)
3.		
4.		
5.		

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Apply proper signal levels to inputs. Measure Carrier Select output with Digital Multimeter. An Oscilloscope will be used to display waveform. (Repeat for all Carrier Select outputs)

Data Reduction: Send input levels and output of Digital Multimeter to printer. Output of Spectrum Analyzer to plotter.

Equipment List:

- | | | |
|--------------------------------|----------------------------|----|
| 1. Logic Patt. Gen. (HP-8170A) | 4. Computer (HP-9836S) | 7. |
| 2. Digital Multi. (HP-3478B) | 5. Oscilloscope (TEK-2465) | 8. |
| 3. Printer (HP-2671G) | 6. Plotter (HP-7475A) | 9. |



Contractor: Magnavox
Board Tested: Converter/Channel Switch
Test Objective: Verify Delta Diff outputs operational.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	RF Select 0	TBD	Logic Patt. Gen. (HP-8170A)
2.	RF Select 1	TBD	Logic Patt. Gen. (HP-8170A)
3.	RF Select 2	TBD	Logic Patt. Gen. (HP-8170A)
4.	RF Select 3	TBD	Logic Patt. Gen. (HP-8170A)
5.	RF Select 4	TBD	Logic Patt. Gen. (HP-8170A)
6.	2F	10.23MHz @ TBD	Signal Generator (HP-8663A)
7.	F/120 UT	42625kHz @ TBD	Func. Generator (HP-8116A)
8.	F/154 UT	33214.3kHz @ TBD	Func. Generator (HP-8116A)
9.	2F - F/120, 154	10.23MHz - 42625kHz/ 33214.3kHz @ TBD	Func. Generator (HP-8116A)
10.			
11.			
12.			

Outputs

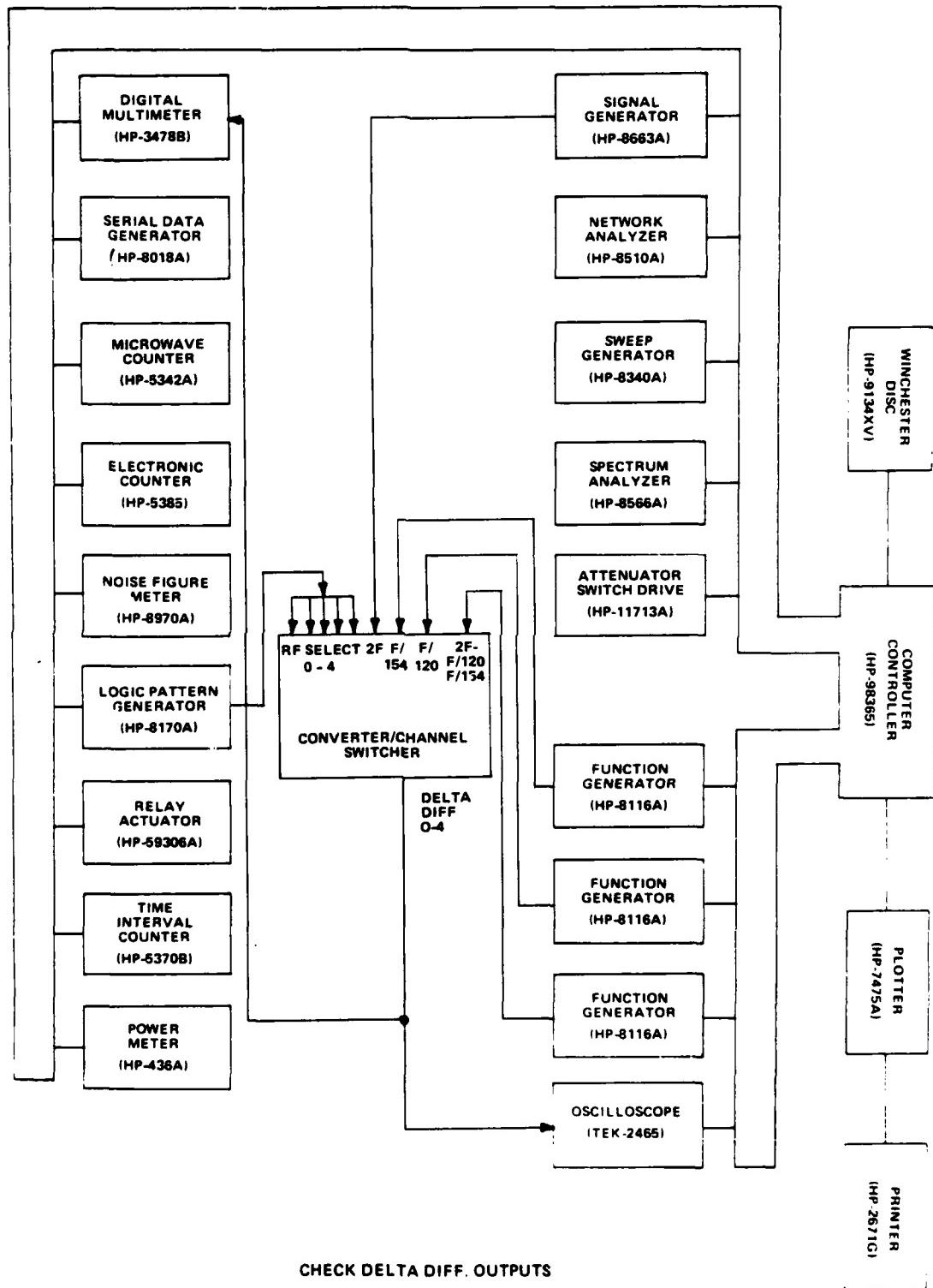
<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. <u>Delta Diff Ø-4</u>	<u>TBD</u>	<u>Digital Multi. (HP-3478B)</u>
2.		<u>Oscilloscope (TEK-2465)</u>
3.		
4.		
5.		

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Apply proper signal levels to inputs. Measure Delta Diff output with Digital Multimeter. An Oscilloscope will be used to display waveform. (Repeat for all Delta Diff outputs).

Data Reduction: Send input levels and output of Digital Multimeter to printer. Output of Oscilloscope to plotter.

Equipment List:

- | | | |
|---------------------------------------|------------------------------------|-----------------------------------|
| 1. <u>Logic Patt. Gen. (HP-8170A)</u> | 4. <u>Digital Multi (HP-3478B)</u> | 7. <u>Oscilloscope (TEK-2465)</u> |
| 2. <u>3-Func. Gens. (HP-8116A)</u> | 5. <u>Printer (HP-2671G)</u> | 8. <u>Plotter (HP-7475A)</u> |
| 3. <u>Signal Gen. (HP-8663A)</u> | 6. <u>Computer (HP-9836S)</u> | 9. _____ |



3.1.1.3 IF/BB/CODER MODULE

Contractor: Magnavox
Board Tested: IF/BB/Coder
Test Objective: Verify presence and proper operation of SLEW
DNE Signal.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>Δ Difference</u>	TBD	Function Gen. (HP-8116A)
2.	<u>* 36F + Δ</u>	-24dBm	Signal Gen. (HP-8663A)
3.	<u>** 29⁷/8F (2 inputs)</u>	-10dBm	Sweep Gen. (HP-8340A)
4.	<u>** 2F (2 inputs)</u>	TBD	Function Gen. (HP-8116A)
5.	<u>** 6F (2 inputs)</u>	-30dBm	Function Gen. (HP-8116A)
6.	<u>Serial Data Bus</u>	TBD	Serial Data Gen. (HP-8018A)
7.	<u>X P Code St</u>	TBD	Log Patt. Gen. (HP-8170A)
8.	<u>X 20 MUT</u>	TBD	Log Patt. Gen. (HP-8170A)
9.	<u>X CARR SEL</u>	TBD	Log Patt. Gen. (HP-8170A)
10.	<u>X 1 MUT</u>	TBD	Log Patt. Gen. (HP-8170A)
11.			
12.			

* Signal Generator output must be modulated by an in house designed code and data generator.

** Power splitters needed for these six inputs.

X Breakout board needed to provide various levels for theses inputs.

Outputs

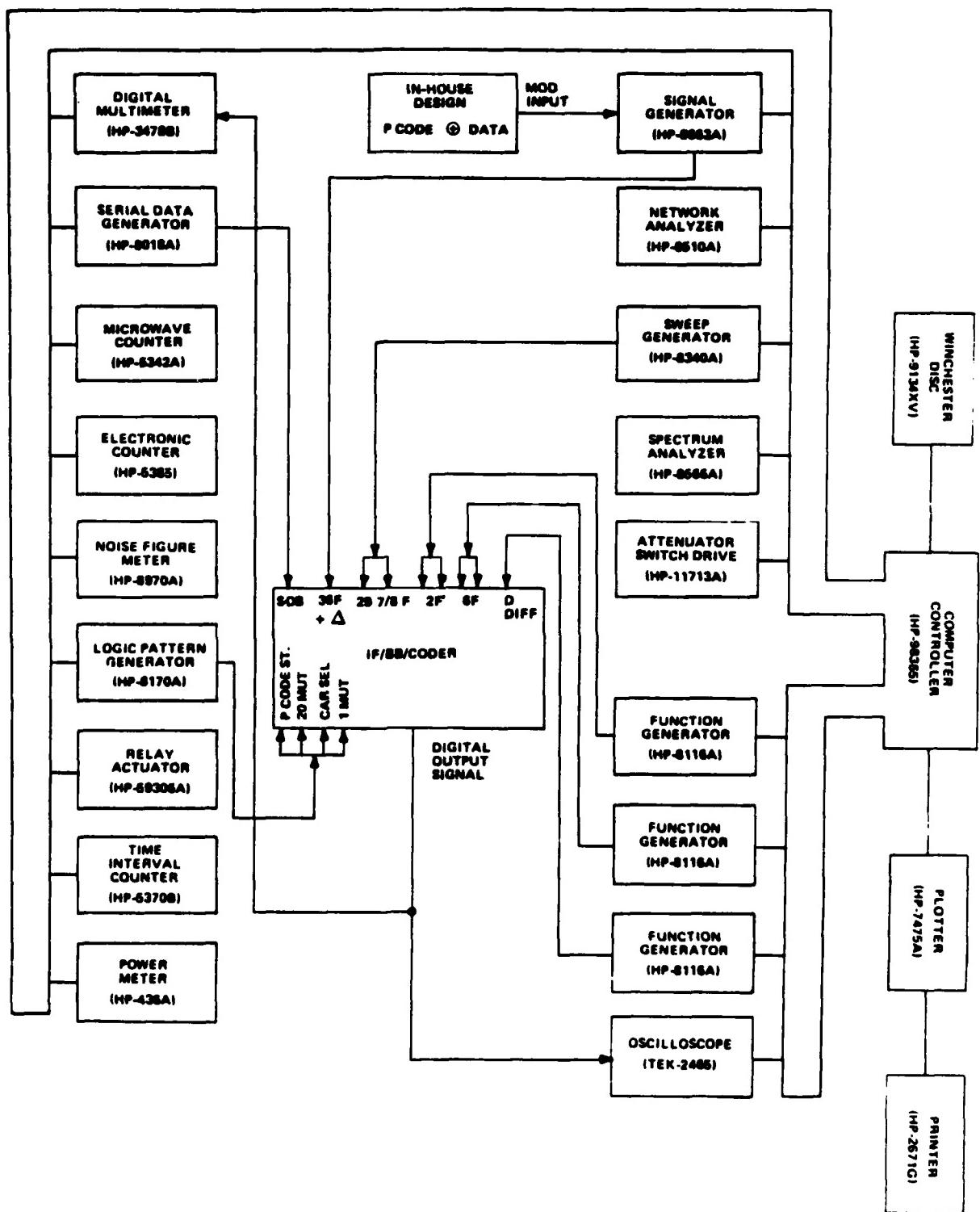
<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. SLEW DNE	TBD	DMM (HP-3478B)
2.		Scope (TEK-2465)
3.		
4.		
5.		

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Apply proper signal levels to all inputs of IF/BB/Coder module. Measure output of SLEW DNE Signal with DMM. Observe signal presence and level on scope.

Data Reduction: Send input levels and output of Digital Multimeter to printer. Output of Oscilloscope to plotter.

Equipment List:

- | | | |
|--------------------------|-------------------------------|---|
| 1. Func. Gen. (HP-8116A) | 4. Ser. Data Gen. (HP-8018A) | 7. Oscilloscope (TEK-2465) |
| 2. Sig. Gen. (HP-8663A) | 5. Log. Patt. Gen. (HP-8170A) | 8. Special In house designed equipment (See notes on previous page) |
| 3. Sweep Gen. (HP-8340A) | 6. DMM (HP-3478B) | 9. |
| 10. Computer (HP-9836S) | 11. Printer (HP-2671G) | 12. Plotter (HP-7475A) |



Contractor: Magnavox
Board Tested: IF/BB/Coder
Test Objective: Verify presence and proper level of Course Range
 Epoch (CRE) output signal.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>Δ Difference</u>	TBD	<u>Function Gen. (HP-8116A)</u>
2.	<u>* 36F + Δ</u>	-24dBm	<u>Signal Gen. (HP-8663A)</u>
3.	<u>** 29⁷/8F (2 inputs)</u>	-10dBm	<u>Sweep Gen. (HP-8340A)</u>
4.	<u>** 2F (2 inputs)</u>	TBD	<u>Function Gen. (HP-8116A)</u>
5.	<u>** 6F (2 inputs)</u>	-30dBm	<u>Function Gen. (HP-8116A)</u>
6.	<u>Serial Data Bus</u>	TBD	<u>Serial Data Gen. (HP-8018A)</u>
7.	<u>X P Code St</u>	TBD	<u>Log Patt. Gen. (HP-8170A)</u>
8.	<u>X 20 MUT</u>	TBD	<u>Log Patt. Gen. (HP-8170A)</u>
9.	<u>CARR SEL</u>	TBD	<u>Log Patt. Gen. (HP-8170A)</u>
10.	<u>1 MUT</u>	TBD	<u>Log Patt. Gen. (HP-8170A)</u>
11.			
12.			

- * Signal Generator output must be modulated by an in house designed Code and data generator.
- ** Power splitters needed for these six inputs.
- X Breakout board needed to provide various levels for these inputs.

Outputs

	<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1.	CRE	TBD	DMM (HP-3478B)
2.			Scope (TEK-2465)
3.			
4.			
5.			

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Apply proper signal levels to all inputs of IF/BB/Coder Module. Measure output of CRE with DMM. Observe signal presence and level on scope.

Data Reduction: Send input levels and output of Digital Multimeter to printer. Output of Oscilloscope to plotter.

Equipment List:

- | | | |
|-----------------------------|------------------------------|---|
| 1. Func. Gen. (HP-8116A)(3) | 4. Ser. Data Gen. (HP-8018A) | 7. Oscilloscope (TEK-2465) |
| 2. Signal Gen. (HP-8663A) | 5. Log Patt. Gen. (HP-8170A) | 8. Special In house designed equipment (See notes on previous page) |
| 3. Sweep Gen. (HP-8340A) | 6. DMM (HP-3478B) | 9. Plotter (HP-7475A) |
| 10. Computer (HP-9836S) | 11. Printer (HP-2671G) | 12. |

Contractor: Magnavox
Board Tested: IF/BB/Coder
Test Objective: Verify presence and proper level of Fine Range
 Epoch (FRE) output signal.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>Δ Difference</u>	TBD	Function Gen. (HP-8116A)
2.	* 36F + Δ	-24dBm	Signal Gen. (HP-8663A)
3.	** 29 ⁷ /8F (2 inputs)	-10dBm	Sweep Gen. (HP-8340A)
4.	** 2F (2 inputs)	TBD	Function Gen. (HP-8116A)
5.	** 6F (2 inputs)	-30dBm	Function Gen. (HP-8116A)
6.	<u>Serial Data Bus</u>	TBD	Serial Data Gen. (HP-8018A)
7.	X P Code St	TBD	Log Patt. Gen. (HP-8170A)
8.	X 20 MUT	TBD	Log Patt. Gen. (HP-8170A)
9.	X CARR SEL	TBD	Log Patt. Gen. (HP-8170A)
10.	X 1 MUT	TBD	Log Patt. Gen. (HP-8170A)
11.			
12.			

- * Signal Generator output must be modulated by an in house designed Code and data generator.
- ** Power splitters needed for these six inputs.
- X Breakout board needed to provide various levels for these inputs.

Outputs

<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. FRE	TBD	DMM (HP-3478B)
2.		Scope (TEK-2465)
3.		
4.		
5.		

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Apply proper signal levels to all inputs of IF/BB/Coder module. Measure output of FRE with DMM. Observe signal presence and level on scope.

Data Reduction: Send input levels and output of Digital Multimeter to printer. Output of Oscilloscope to plotter.

Equipment List:

- | | | |
|---------------------------|------------------------------|---|
| 1. Func. Gen. (HP-8116A) | 4. Ser. Data Gen. (HP-8018A) | 7. Oscilloscope (TEK-2465) |
| 2. Signal Gen. (HP-8663A) | 5. Log Patt. Gen. (HP-8170A) | 8. Special In house designed equipment (See notes on previous page) |
| 3. Sweep Gen. (HP-8340A) | 6. DMM (HP-3478B) | 9. |
| 10. Computer (HP-9836S) | 11. Printer (HP-2671G) | 12. Plotter (HP-7475A) |

Contractor: Magnavox
Board Tested: IF/BB/Coder
Test Objective: Verify presence and proper operation of AGC
SF/1 signal.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>Δ Difference</u>	TBD	Function Gen. (HP-8116A)
2.	* 36F + Δ	-24dBm	Signal Gen. (HP-8663A)
3.	** 29 ⁷ /8F (2 inputs)	-10dBm	Sweep Gen. (HP-8340A)
4.	** 2F (2 inputs)	TBD	Function Gen. (HP-8116A)
5.	** 6F (2 inputs)	-30dBm	Function Gen. (HP-8116A)
6.	Serial Data Bus	TBD	Serial Data bus (HP-8018A)
7.	X P Code St	TBD	Log Patt. Gen. (HP-8170A)
8.	X 20 MUT	TBD	Log Patt. Gen. (HP-8170A)
9.	X CARR SEL	TBD	Log Patt. Gen. (HP-8170A)
10.	X 1 MUT	TBD	Log Patt. Gen. (HP-8170A)
11.			
12.			

- * Signal Generator output must be modulated by an in house designed Code and data generator.
- ** Power splitters needed for these six inputs.
- X Breakout board needed to provide various levels for these inputs.

Outputs

<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. AGC SF/1	TBD	DMM (HP-3478B)
2.		Scope (TEK-2465)
3.		
4.		
5.		

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Apply proper signal levels to all inputs of IF/BB/Coder module. Measure output level of AGC SF/1 signal with DMM. Observe signal presence and level on scope.

Data Reduction: Send input levels and output of Digital Multimeter to printer. Output of Oscilloscope to plotter.

Equipment List:

- | | | |
|--------------------------|------------------------------|---|
| 1. Func. Gen. (HP-8116A) | 4. Ser Data Gen. (HP-8018A) | 7. Oscilloscope (TEK-2465) |
| 2. Sig. Gen. (HP-8663A) | 5. Log Patt. Gen. (HP-8170A) | 8. Special In house designed equipment (See notes on previous page) |
| 3. Sweep Gen. (HP-8340A) | 6. DMM (HP-3478B) | 9. |
| 10. Computer (HP-9836S) | 11. Printer (HP-2671G) | 12. Plotter (HP-7475A) |

Contractor: Magnavox
Board Tested: IF/BB/Coder
Test Objective: Verify presence and proper operation of CARR
SEL A signal.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>Δ Difference</u>	TBD	<u>Function Gen. (HP-8116A)</u>
2.	<u>* 36F + Δ</u>	-24dBm	<u>Signal Gen. (HP-8663A)</u>
3.	<u>** 29⁷/8F (2 inputs)</u>	-10dBm	<u>Sweep Gen. (HP-8340A)</u>
4.	<u>** 2F (2 inputs)</u>	TBD	<u>Function Gen. (HP-8116A)</u>
5.	<u>** 6F (2 inputs)</u>	-30dBm	<u>Function Gen. (HP-8116A)</u>
6.	<u>Serial Data Bus</u>	TBD	<u>Serial Data Bus (HP-8018A)</u>
7.	<u>X P Code St</u>	TBD	<u>Log Patt. Gen. (HP-8170A)</u>
8.	<u>X 20 MUT</u>	TBD	<u>Log Patt. Gen. (HP-8170A)</u>
9.	<u>X CARR SEL</u>	TBD	<u>Log Patt. Gen. (HP-8170A)</u>
10.	<u>X 1 MUT</u>	TBD	<u>Log Patt. Gen. (HP-8170A)</u>
11.			
12.			

- * Signal Generator output must be modulated by an in house designed Code and data generator.
- ** Power splitters needed for these six inputs.
- X Breakout board needed to provide various levels for these inputs.

Outputs

<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. CARR SEL A	TBD	DMM (HP-3478B)
2.		Scope (TEK-2465)
3.		
4.		
5.		

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Apply proper signal levels to all inputs of IF/BB/Coder module. Measure output level of CARR SEL A signal with DMM. Observe signal presence and level on scope.

Data Reduction: Send input levels and output of Digital Multimeter to printer. Output of Oscilloscope to plotter.

Equipment List:

- | | | |
|---------------------------|------------------------------|---|
| 1. Func. Gen. (HP-8116A) | 4. Ser. Data Gen. (HP-8018A) | 7. Oscilloscope (TEK-2465) |
| 2. Signal Gen. (HP-8663A) | 5. Log Patt. Gen. (HP-8170A) | 8. Special In house designed equipment (See notes on previous page) |
| 3. Sweep Gen. (HP-8340A) | 6. DMM (HP-3478B) | 9. |
| 10. Computer (HP-9836S) | 11. Printer (HP-2671G) | 12. Plotter (HP-7-75A) |

Contractor: Magnavox
Board Tested: IF/BB/Coder
Test Objective: Verify presence and proper operation of CANCEL
 A Signal.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>Δ Difference</u>	TBD	<u>Function Gen. (HP-8116A)</u>
2.	<u>* 36F + Δ</u>	-24dBm	<u>Signal Gen. (HP-8663A)</u>
3.	<u>** 29⁷/8F (2 inputs)</u>	-10dBm	<u>Sweep Gen. (HP-8340A)</u>
4.	<u>** 2F (2 inputs)</u>	TBD	<u>Function Gen. (HP-8116A)</u>
5.	<u>** 6F (2 inputs)</u>	-30dBm	<u>Function Gen. (HP-8116A)</u>
6.	<u>Serial Data Bus</u>	TBD	<u>Serial Data Gen. (HP-8018A)</u>
7.	<u>X P Code St</u>	TBD	<u>Log Patt. Gen. (HP-8170A)</u>
8.	<u>X 20 MUT</u>	TBD	<u>Log Patt. Gen. (HP-8170A)</u>
9.	<u>X CARR SEL</u>	TBD	<u>Log Patt. Gen. (HP-8170A)</u>
10.	<u>X 1 MUT</u>	TBD	<u>Log Patt. Gen. (HP-8170A)</u>
11.			
12.			

* Signal Generator output must be modulated by an in house designed Code and data generator.

** Power splitters needed for these six inputs.

X Breakout board needed to provide various levels for these inputs.

Outputs

<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. CANCEL A	TBD	DMM (HP-3478B)
2.		Scope (TEK-2465)
3.		
4.		
5.		

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Apply proper signal levels to all inputs of IF/BB/Coder module. Measure output level of CANCEL A signal with DMM. Observe signal presence and level on scope.

Data Reduction: Send input levels and output of Digital Multimeter to printer. Output of Oscilloscope to plotter.

Equipment List:

- | | | |
|---------------------------|------------------------------|---|
| 1. Func. Gen. (HP-8116A) | 4. Ser. Data Gen. (HP-8018A) | 7. Oscilloscope (TEK-2465) |
| 2. Signal Gen. (HP-8663A) | 5. Log Patt. Gen. (HP-8170A) | 8. Special In house
designed equipment (See
notes on previous page) |
| 3. Sweep Gen. (HP-8340A) | 6. DMM (HP-3478B) | 9. |
| 10. Computer (HP-9836S) | 11. Printer (HP-2671G) | 12. Plotter (HP-7475A) |

RD-R166 497

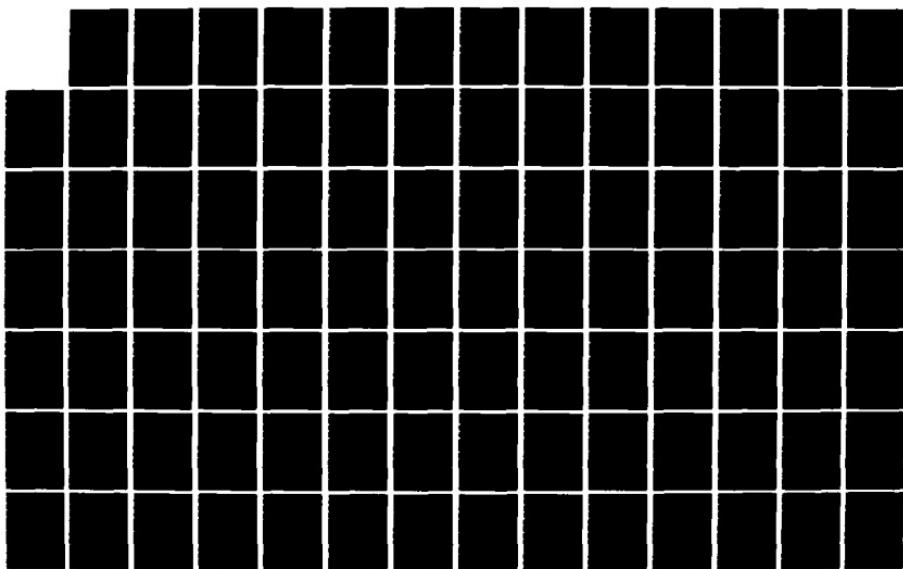
UE BENCH TEST PLANS AND REQUIREMENTS VOLUME 1 MAGNAVOX
UE SET(U) ESSCUBE ENGINEERING INC MARLTON NJ SEP 84
CEA/GPS-85-156-00-030-VOL-1 N62269-82-D-0059

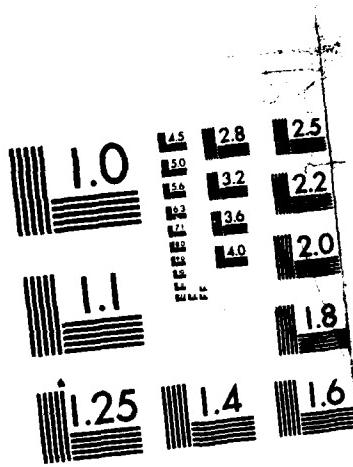
2/1

UNCLASSIFIED

F/G 14/2

NL





MICROCOPY RESOLUTION TEST CHART
NATIONAL BUREAU OF STANDARDS 1967 A

Contractor: Magnavox
Board Tested: IF/BB/Coder
Test Objective: Verify presence and proper operation of RF
SEL signal.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>A Difference</u>	TBD	Func. Gen. (HP-8116A)
2.	* 36F + A	-24dBm	Signal Gen. (HP-8663A)
3.	** 29 ⁷ /8F (2 inputs)	-10dBm	Sweep Gen. (HP-8340A)
4.	** 2F (2 inputs)	TBD	Function Gen. (HP-8116A)
5.	** 6F (2 inputs)	-30dBm	Function Gen. (HP-8116A)
6.	Serial Data Bus	TBD	Ser. Data Gen. (HP-8018A)
7.	X P Code St	TBD	Log Patt. Gen. (HP-8170A)
8.	X 20 MUT	TBD	Log Patt. Gen. (HP-8170A)
9.	X CARR SEL	TBD	Log Patt. Gen. (HP-8170A)
10.	X 1 MUT	TBD	Log Patt. Gen. (HP-8170A)
11.			
12.			

* Signal Generator output must be modulated by an in house designed Code and data generator.

** Power splitters needed for these six inputs.

X Breakout board needed to provide various levels for these inputs.

Outputs

<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. RF SEL	TBD	DMM (HP-3478B)
2.		Scope (TEK-2465)
3.		
4.		
5.		

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Apply proper signal levels to all inputs of IF/BB/Coder module. Measure output level of RF SEL with DMM. Observe signal presence and level on scope.

Data Reduction: Send input levels and output of Digital Multimeter to printer. Output of Oscilloscope to plotter.

Equipment List:

- | | | |
|--------------------------|------------------------------|---|
| 1. Func. Gen. (HP-8116A) | 4. Ser Data Gen. (HP-8018A) | 7. Oscilloscope (TEK-2465) |
| 2. Sig. Gen. (HP-8663A) | 5. Log Patt. Gen. (HP-8170A) | 8. Special In house |
| 3. Sweep Gen. (HP-8340A) | 6. DMM (HP-3478B) | designed equipment (See notes on previous page) |
| 10. Computer (HP-9836S) | 11. Printer (HP-2671G) | 12. Plotter (HP-7475A) |

Contractor: Magnavox
Board Tested: IF/BB/Coder
Test Objective: Verify presence and proper operation of Noise
On/Off Signal.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>Δ Difference</u>	TBD	<u>Function Gen. (HP-8116A)</u>
2.	<u>* 36F + Δ</u>	-24dBm	<u>Signal Gen. (HP-8663A)</u>
3.	<u>** 29⁷/8F (2 inputs)</u>	-10dBm	<u>Sweep Gen. (HP-8340A)</u>
4.	<u>** 2F (2 inputs)</u>	TBD	<u>Function Gen. (HP-8116A)</u>
5.	<u>** 6F (2 inputs)</u>	-30dBm	<u>Function Gen. (HP-8116A)</u>
6.	<u>Serial Data Bus</u>	TBD	<u>Ser. Data Gen. (HP-8116A)</u>
7.	<u>X P Code St</u>	TBD	<u>Log Patt. Gen. (HP-8170A)</u>
8.	<u>X 20 MUT</u>	TBD	<u>Log Patt. Gen. (HP-8170A)</u>
9.	<u>X CARR SEL</u>	TBD	<u>Log Patt. Gen. (HP-8170A)</u>
10.	<u>X 1 MUT</u>	TBD	<u>Log Patt. Gen. (HP-8170A)</u>
11.			
12.			

* Signal Generator output must be modulated and spread by in house designed P or C/A Code and data generator.

** Power splitters needed for these 6 inputs.

X Some sort of breakout board needed to provide different levels for these inputs.

Outputs

<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. <u>Noise On/Off</u>	<u>TBD</u>	<u>DMM (HP-3478B)</u>
2.		<u>Scope (TEK-2465)</u>
3.		
4.		
5.		

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Apply proper signal levels to all inputs of IF/BB/Coder module. Measure output level of Noise On/Off signal with DMM. Observe signal presence and level on scope.

Data Reduction: Send input levels and output of Digital Multimeter to printer. Output of Oscilloscope to plotter.

Equipment List:

- | | | |
|----------------------------------|-------------------------------------|-----------------------------------|
| 1. <u>Func. Gen. (HP-8116A)</u> | 4. <u>Ser. Data Gen. (HP-8018A)</u> | 7. <u>Oscilloscope (TEK-2465)</u> |
| 2. <u>Signal Gen. (HP-8663A)</u> | 5. <u>Log Patt. Gen. (HP-8170A)</u> | 8. <u>Special In house</u> |
| 3. <u>Sweep Gen. (HP-8340A)</u> | 6. <u>DMM (HP-3478B)</u> | <u>designed equipment (See</u> |
| 10. <u>Computer (HP-9836S)</u> | 11. <u>Printer (HP-2671G)</u> | <u>first page notes)</u> |
| | | 12. <u>Plotter (HP-7475A)</u> |

Contractor: Magnavox
Board Tested: IF/BB/Coder
Test Objective: Verify presence and proper level of ANT SEL signal.

Inputs

<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1. <u>Δ Difference</u>	TBD	Function Gen. (HP-8116A)
2. * <u>36F + Δ</u>	-24dBm	Signal Gen. (HP-8663A)
3. ** <u>29⁷/8F (2 inputs)</u>	-10dBm	Sweep Gen. (HP-8340A)
4. ** <u>2F (2 inputs)</u>	TBD	Function Gen. (HP-8116A)
5. ** <u>6F (2 inputs)</u>	-30dBm	Function Gen. (HP-8116A)
6. <u>Serial Data Bus</u>	TBD	Serial Data Gen. (HP-8018A)
7. X <u>P Code St</u>	TBD	Log Patt. Gen. (HP-8170A)
8. X <u>20 MUT</u>	TBD	Log Patt. Gen. (HP-8170A)
9. X <u>CARR SEL</u>	TBD	Log Patt. Gen. (HP-8170A)
10. X <u>1 MUT</u>	TBD	Log Patt. Gen. (HP-8170A)
11.		
12.		

- * Signal Generator output must be modulated by an in house designed Code and data generator.
- ** Power splitters needed for these six inputs.
- X Breakout board needed to provide various levels for these inputs.

Outputs

<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. ANT SEL	TBD	DMM (HP-3478B)
2.		Scope (TEK-2465)
3.		
4.		
5.		

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Apply proper signal levels to all inputs of IF/BB/Coder module. Measure output of ANT SEL with DMM. Observe signal presence and level on scope.

Data Reduction: Send input levels and output of Digital Multimeter to printer. Output of Oscilloscope to plotter.

Equipment List:

- | | | |
|------------------------------|------------------------------|---|
| 1. Func. Gen. (HP-8116A) (3) | 4. Ser. Data Gen. (HP-8018A) | 7. Oscilloscope (HP-2465) |
| 2. Signal Gen. (HP-8663A) | 5. Log Patt. Gen. (HP-8170A) | 8. Special In house |
| 3. Sweep Gen. (HP-8340A) | 6. DMM (HP-3478B) | designed equipment (See notes on previous page) |
| 10. Computer (HP-9836S) | 11. Printer (HP-2671G) | 12. Plotter (HP-7475A) |

Contractor: Magnavox
Board Tested: IF/BB/Coder
Test Objective: Verify presence and proper level of Bite SW output
signal.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>Δ Difference</u>	TBD	<u>Function Gen. (HP-8116A)</u>
2.	<u>* 36F + Δ</u>	-24dBm	<u>Signal Gen. (HP-8663A)</u>
3.	<u>** 29⁷/8F (2 inputs)</u>	-10dBm	<u>Sweep Gen. (HP-8340A)</u>
4.	<u>** 2F (2 inputs)</u>	TBD	<u>Function Gen. (HP-8116A)</u>
5.	<u>** 6F (2 inputs)</u>	-30dBm	<u>Function Gen. (HP-8116A)</u>
6.	<u>Serial Data Bus</u>	TBD	<u>Log Patt. Gen. (HP-8018A)</u>
7.	<u>X P Code St</u>	TBD	<u>Log Patt. Gen. (HP-8170A)</u>
8.	<u>X 20 MUT</u>	TBD	<u>Log Patt. Gen. (HP-8170A)</u>
9.	<u>X CARR SEL</u>	TBD	<u>Log Patt. Gen. (HP-8170A)</u>
10.	<u>X 1 MUT</u>	TBD	<u>Log Patt. Gen. (HP-8170A)</u>
11.			
12.			

* Signal Generator output must be modulated by an in house designed Code and data generator.

** Power splitters needed for these six inputs.

X Breakout board needed to provide various levels for these inputs.

Outputs

<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. <u>Bite SW</u>	<u>TBD</u>	<u>DMM (HP-3478B)</u>
2.		<u>Scope (TEK-2465)</u>
3.		
4.		
5.		

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Apply proper signal levels to all inputs of IF/BB/Coder module. Measure output of Bite SW with DMM. Observe signal presence and level on scope.

Data Reduction: Send input levels and output of Digital Multimeter to printer. Output of Oscilloscope to plotter.

Equipment List:

- | | | |
|------------------------------------|-------------------------------------|--|
| 1. <u>Func. Gen. (HP-8116A)(3)</u> | 4. <u>Ser. Data Gen. (HP-8018A)</u> | 7. <u>Oscilloscope (TEK-2465)</u> |
| 2. <u>Signal Gen. (HP-8663A)</u> | 5. <u>Log Patt. Gen. (HP-8170A)</u> | 8. <u>Special In house</u> |
| 3. <u>Sweep Gen. (HP-8340A)</u> | 6. <u>DMM (HP-3478B)</u> | <u>designed equipment (See notes on previous page)</u> |
| 10. <u>Computer (HP-9836S)</u> | 11. <u>Printer (HP-2671G)</u> | 12. <u>Plotter (HP-7475A)</u> |

3.1.1.4 PRE AMPLIFIER UNIT MODULE

Contractor: Magnavox

Board Tested: Pre Amp Unit

Test Objective: Measure PAU noise figure

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	Noise Input	TBD	Noise Source (HP-346B)
2.			
3.			
4.			
5.			
6.			
7.			
8.			
9.			
10.			
11.			
12.			

Outputs

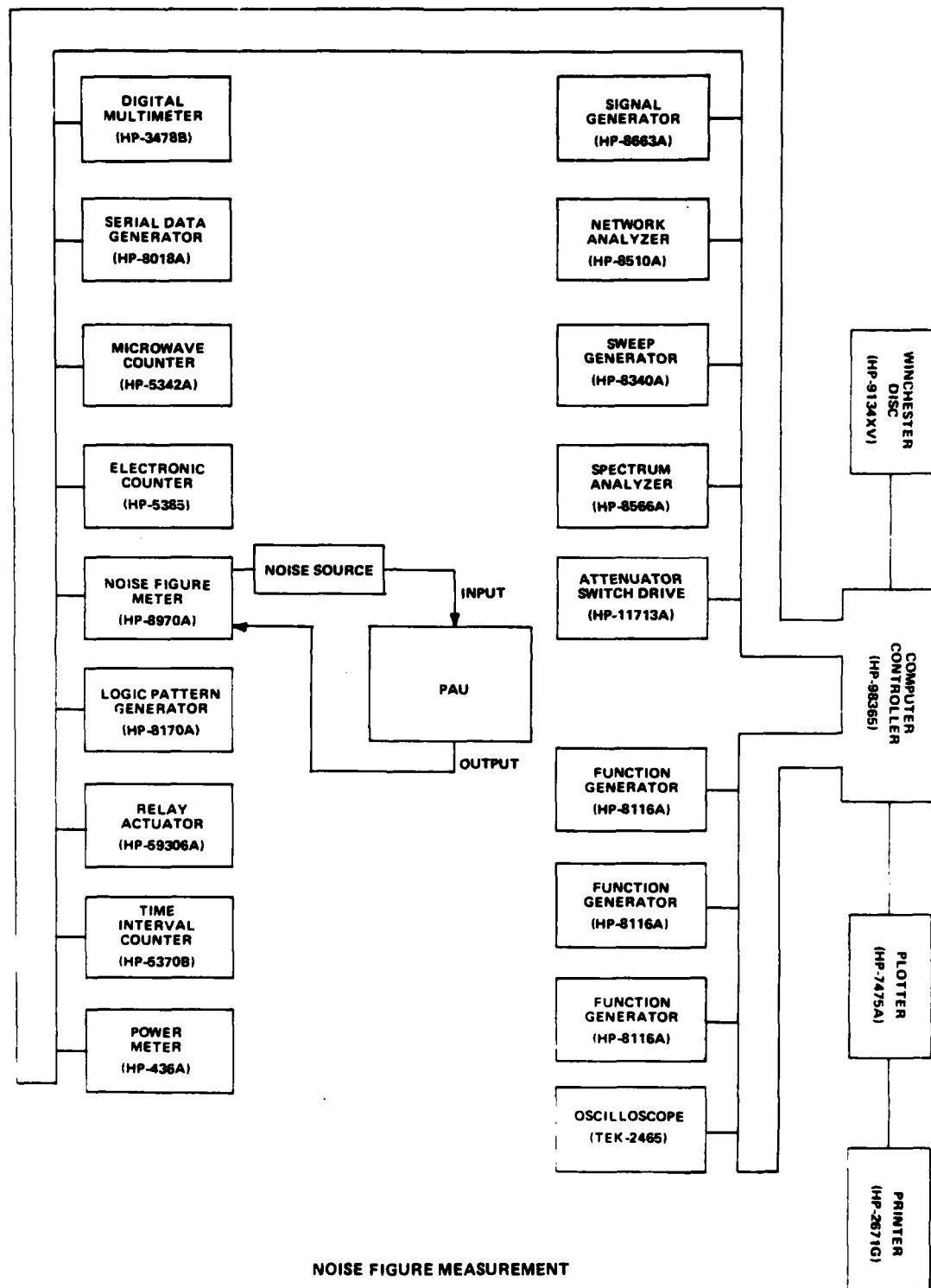
<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. NF	5dB Max	Noise Meter (HP-8970A)
2.		
3.		
4.		
5.		

Test Procedure: Hook up unit under test as shown in diagram. Turn on test equipment. Set measurement equipment to proper ranges. Apply proper input level to PAU. Measure NF with meter.

Data Reduction: Record Noise Figure on printer.

Equipment List:

- | | | |
|---------------------------|-----------------------|----|
| 1. Noise Source (HP-346B) | 4. Printer (HP-2671G) | 7. |
| 2. NF Meter (HP-8970A) | 5. | 8. |
| 3. Computer (HP-9836S) | 6. | 9. |



NOISE FIGURE MEASUREMENT

Contractor: Magnavox
Board Tested: PAU
Test Objective: Verify proper frequency output from Preamp unit

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	L1/L2	1227.60/1575.42MHz	Signal Generator (HP-8663A)
2.			Attenuator
3.			
4.			
5.			
6.			
7.			
8.			
9.			
10.			
11.			
12.			

Outputs

	<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1.	<u>L1/L2</u>	<u>1227.60/1575.42MHz</u>	<u>Micro Ctr (HP-5342A)</u>
2.			<u>Prog Atten (HP-8494G)</u>
3.			<u>Atten Sw Drive (HP-11713A)</u>
4.			
5.			

Test Procedure: Hook up unit under test as shown in diagram.

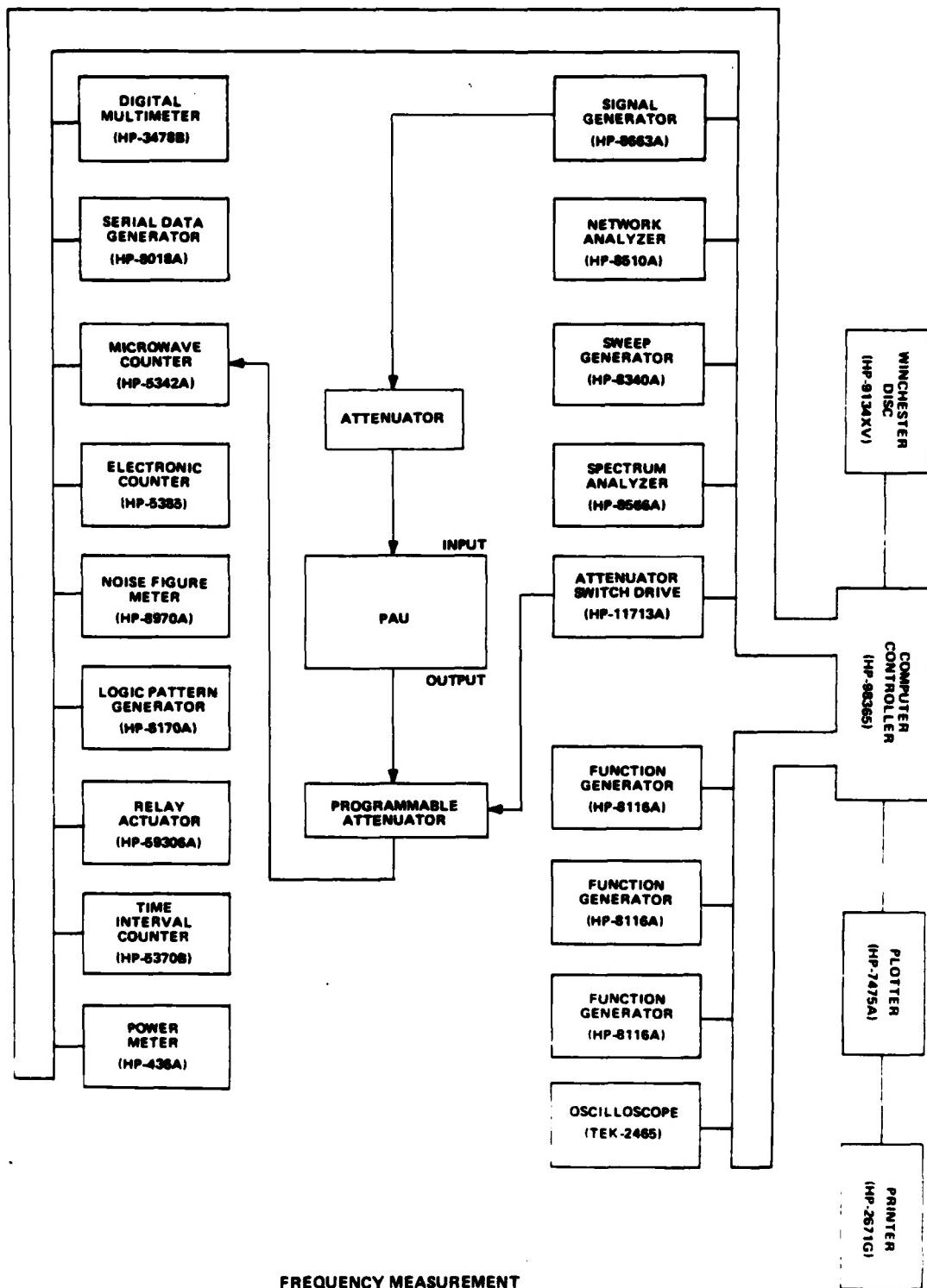
Turn on test equipment. Set measurement equipment to proper ranges.

Apply proper input levels to PAU. Measure PAU output frequency with Micro Counter.

Data Reduction: Send input levels and output of Microwave Counter to printer.

Equipment List:

- | | | |
|---------------------------------|-----------------------------------|-------------------------------------|
| 1. <u>Signal Gen (HP-8663A)</u> | 4. <u>Printer (HP-2671G)</u> | 7. <u>Att. Sw Drive (HP-11713A)</u> |
| 2. <u>Attenuator</u> | 5. <u>Micro Ctr (HP-5342A)</u> | 8. _____ |
| 3. <u>Computer (HP-9836S)</u> | 6. <u>Prog. Atten. (HP-8494G)</u> | 9. _____ |



FREQUENCY MEASUREMENT

Contractor: Magnavox
Board Tested: Pre-Amp Unit
Test Objective: Verify proper power output and gain for PAU

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	L1/L2	-163dbW	Signal Generator (HP-8663A)
2.			Attenuator
3.			
4.			
5.			
6.			
7.			
8.			
9.			
10.			
11.			
12.			

Outputs

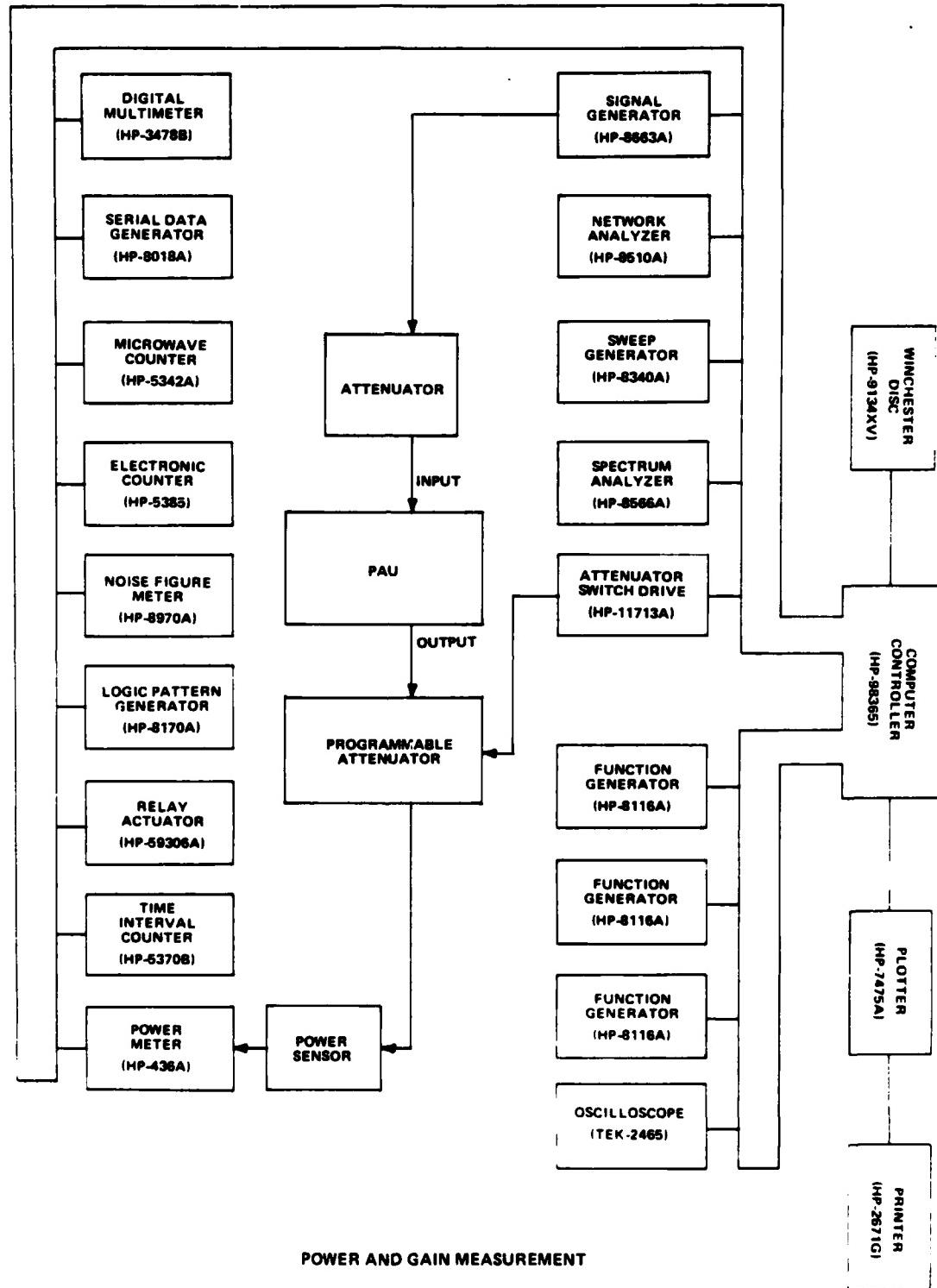
<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. L1/L2	-120dbW	Power Meter (HP-436A)
2. GAIN	43db	Power Sensor (HP-8481A)
3.		Prog Atten (HP-8494G)
4.		Att Sw Drive (HP-11713A)
5.		

Test Procedure: Hook up unit under test as shown in diagram. Turn on test equipment. Set measurement equipment to proper ranges. Apply proper input level to PAU. Measure PAU output power with Power Meter.

Data Reduction: Record input levels and output power measurement on printer. GAIN can be computed.

Equipment List:

- | | | |
|------------------------|----------------------------|------------------------------|
| 1. Sig Gen (HP-8663A) | 4. Printer (HP-2671G) | 7. Prog. Atten. (HP-8494G) |
| 2. Attenuator | 5. Power Meter (HP-436A) | 8. Att. Sw Drive (HP-11713A) |
| 3. Computer (HP-9836S) | 6. Power Sensor (HP-8481A) | 9. |



POWER AND GAIN MEASUREMENT

Contractor: Maqnavox
Board Tested: Pre-Amp Unit
Test Objective: Measure input/output VSWR

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>S-Par Test Set</u>	<u>(1565.42 to 1585.42MHz) @ TED</u>	<u>Sweep Gen. (HP-8340A)</u>
2.	<u>S-Par Test Set</u>	<u>(1217.6 to 1237.6MHz) @ TED</u>	<u>Sweep Gen. (HP-8340A)</u>
3.	<u>S-Par Test Set</u>	<u>(1565.42 to 1585.42MHz) @ TED</u>	<u>Sweep Gen. (HP-8340A)</u>
4.	<u>S-Par Test Set</u>	<u>(1217.6 to 1237.5MHz) @ TED</u>	<u>Sweep Gen. (HP-8340A)</u>
5.			
6.			
7.			
8.			
9.			
10.			
11.			
12.			

Outputs

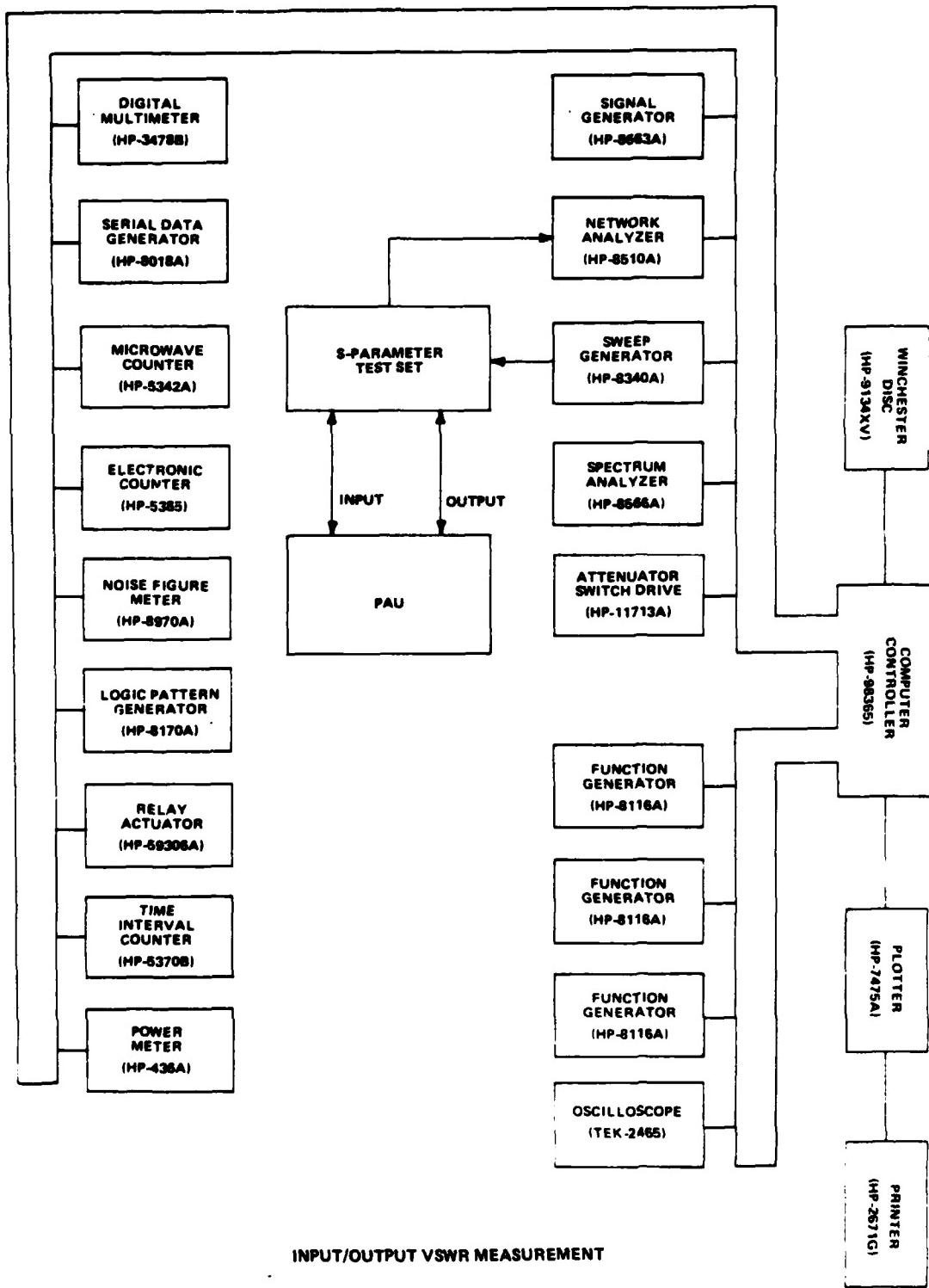
<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. L1	1.5:1 (max)	S-Par Test Set (HP-8515A)
2. L2	1.5:1 (max)	Network Anal. (HP-8510A)
3. RF Out	1.5:1 (max)	
4.		
5.		

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Connect the input of the PAU to one port of the S-parameter test set and the output of the PAU to the other test port. Set Sweep Generator to sweep through L1 and measure S11 (input reflection coefficient) and L1 S22 (output reflection coefficient). Set Sweep Generator to sweep through L2 and repeat measurements.

Data Reduction: Output data from Network Analyzer to plotter and send input levels to printer.

Equipment List:

- | | | | |
|--------------------------|------------------------------|----|--|
| 1. Computer (HP-9836S) | 4. S-Par Test Set (HP-8515A) | 7. | |
| 2. Printer (HP-2671G) | 5. Network Anal. (HP-8510A) | 8. | |
| 3. Sweep Gen. (HP-834CA) | 6. Plotter (HP-7475A) | 9. | |



INPUT/OUTPUT VSWR MEASUREMENT

Contractor: Magnavox
Board Tested: Pre-Amp Unit
Test Objective: Check spectral content, and selectivity
(3db and 60dB Bandwidth) of PAU output

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	L1/L2	1227.6/1575.42MHz	Sig. Gen. (HP-8663A)
2.		@ -163dbW	Attenuator
3.			
4.			
5.			
6.			
7.			
8.			
9.			
10.			
11.			
12.			

Outputs

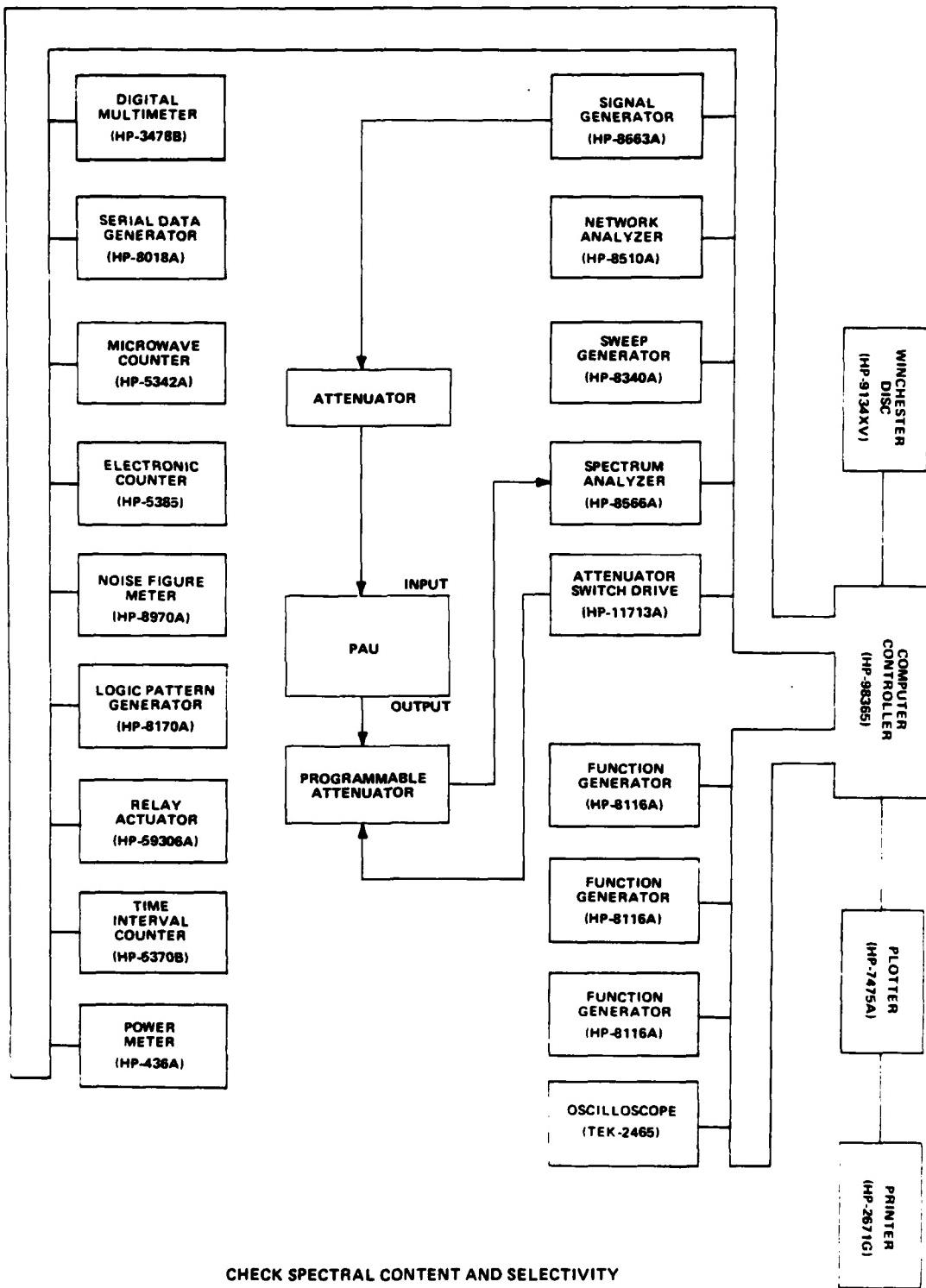
	<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1.	L1/L2	1227.6/1575.42 @ -120dBW	Spec Analyzer (HP-8566A)
2.	3dB BW	14MHz	Prog Atten (HP-8494G)
3.	60dB BW	200MHz	Att Sw Drive (HP-11713A)
4.			
5.			

Test Procedure: Hook up unit under test as shown in diagram. Turn on test equipment. Set measurement equipment to proper ranges. Apply proper input level to PAU. Measure output quantities using Spectrum Analyzer.

Data Reduction: Record input levels with printer. Output Spectrum Analyzer data to plotter.

Equipment List:

1. Sig Gen (HP-8663A)
2. Attenuator
3. Computer (HP-9836S)
4. Spec Anal (HP-8566A)
5. Prog Atten (HP-8494G)
6. Att SW Drive (HP-11713A)
7. Printer (HP-2671G)
8. Plotter (HP-7475A)
9. _____



CHECK SPECTRAL CONTENT AND SELECTIVITY

Contractor: Magnavox
Board Tested: Pre-Amp Unit
Test Objective: Verify burnout protection level

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	In Band Signal	up to +30dbm	Sig Gen. (HP-8663A)
2.			Amplifier
3.			Prog. Atten. (HP-8494G)
4.			Att Sw Drive (HP-11713A)
5.			Power Sensor (HP-8481A)
6.			Power Meter (HP-436A)
7.			
8.			
9.			
10.			
11.			
12.			

Outputs

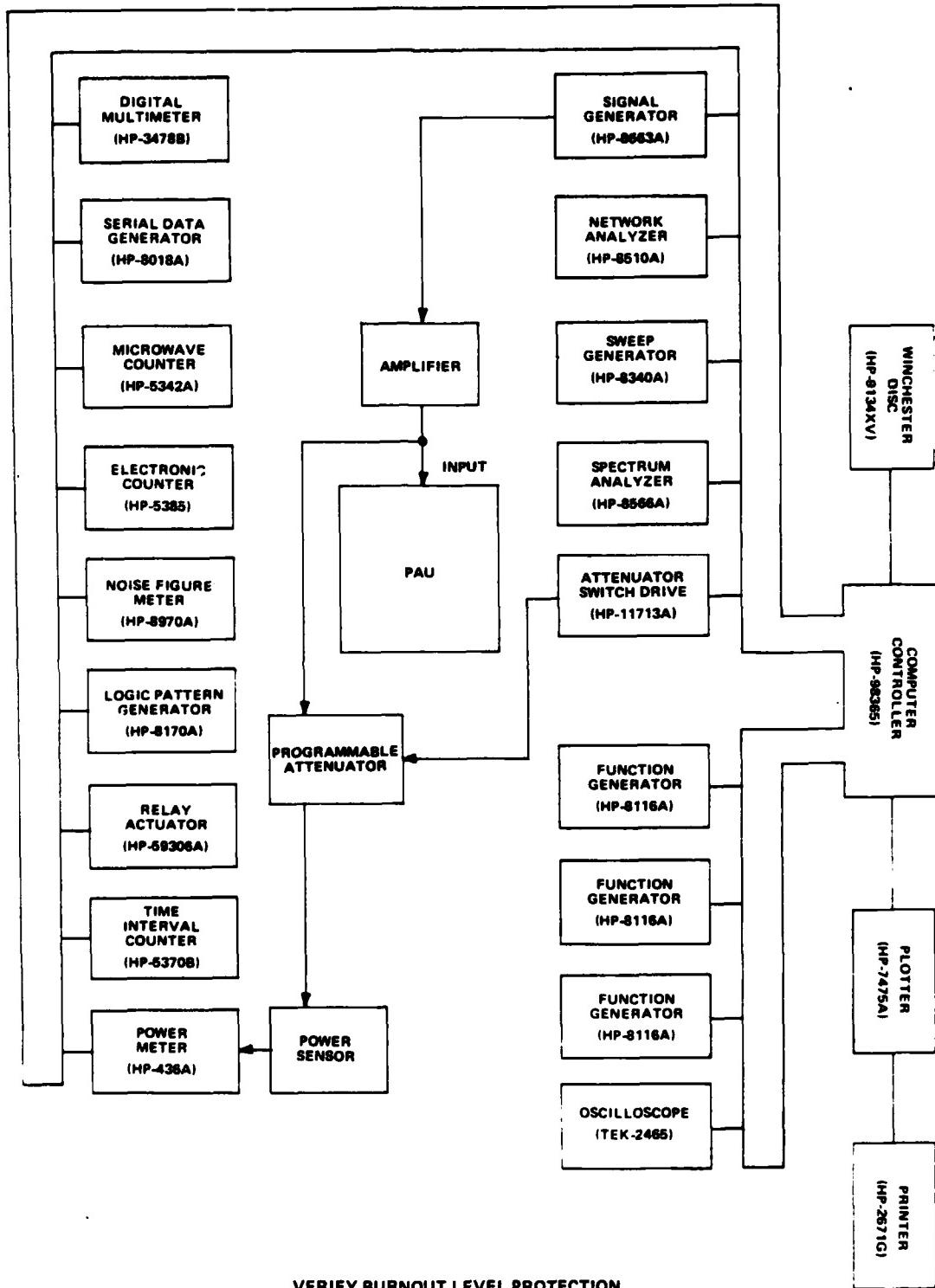
<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1.		
2.		
3.		
4.		
5.		

Test Procedure: Hook up unit under test as shown in diagram. Turn on test equipment. Set measurement equipment to proper ranges. Apply input to PAU increasing to max of +30dbm. Repeat Gain and Noise Figure Tests to insure no permanent damage to PAU.

Data Reduction: Record input levels with printer.

Equipment List:

- | | | |
|------------------------|----------------------------|------------------------------|
| 1. Sig Gen (HP-8663A) | 4. Power Meter (HP-436A) | 7. Program Atten. (HP-8494G) |
| 2. Computer (HP-9836S) | 5. Power Sensor (HP-8481A) | 8. Amplifier |
| 3. Printer (HP-2671G) | 6. Att. Sw Dr. (HP-11713A) | 9. _____ |



VERIFY BURNOUT LEVEL PROTECTION

Contractor: Magnavox

Board Tested: Pre-Amp Unit

Test Objective: Measure group delay

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	L1	1575.42MHz @ TBD	Signal Gen. (HP-8663A)
2.	L2	1227.6MHz @ TBD	Signal Gen. (HP-8663A)
3.			
4.			
5.			
6.			
7.			
8.			
9.			
10.			
11.			
12.			

Outputs

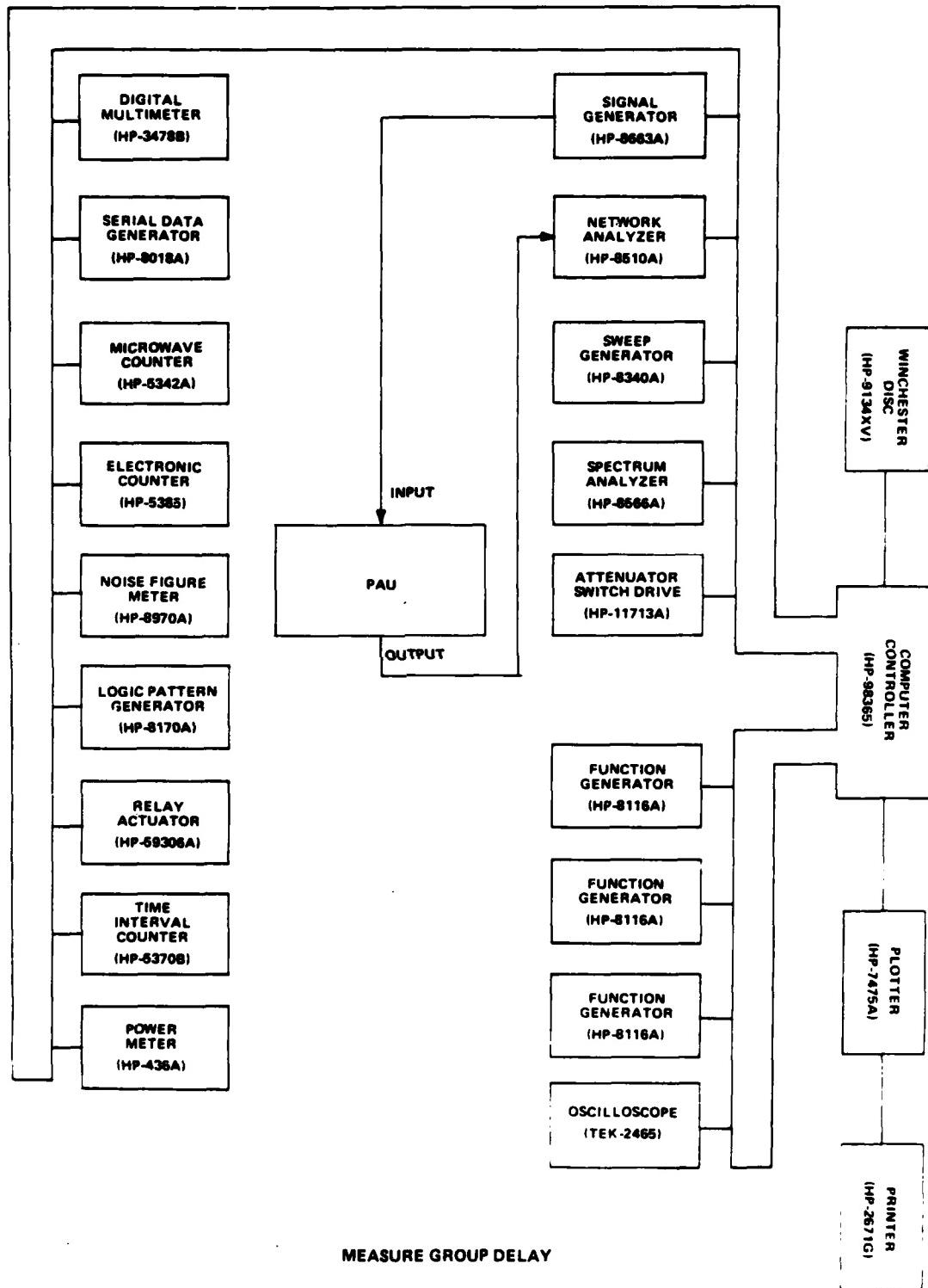
<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. RF Out	TBD	Network Anal. (HP-8510A)
2.		
3.		
4.		
5.		

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Apply L1 and L2 to PAU measure group delay with Network Analyzer.

Data Reduction: Output data from Network Analyzer to plotter and send inputs to printer.

Equipment List:

1. Computer (HP-9836S)	4. Network Anal. (HP-8510A)	7.
2. Printer (HP-2671G)	5. Plotter (HP-7475A)	8.
3. Signal Gen. (HP-8663A)	6.	9.



MEASURE GROUP DELAY

3.1.1.5 REFERENCE OSCILLATOR MODULE

Contractor: Magnavox

Board Tested: Reference Oscillator

Test Objective: Verify proper frequency output.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	DC Voltage	TBD	Function Gen. (HP-8116A)
2.			
3.			
4.			
5.			
6.			
7.			
8.			
9.			
10.			
11.			
12.			

Outputs

<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. <u>Oscillator Output</u>	<u>10.23MHz</u>	<u>Electronic Counter (HP-5385)</u>
2.		<u>Atten. Switch Drive (HP-11713A)</u>
3.		<u>Prog. Atten. (HP-8494G)</u>
4.		
5.		

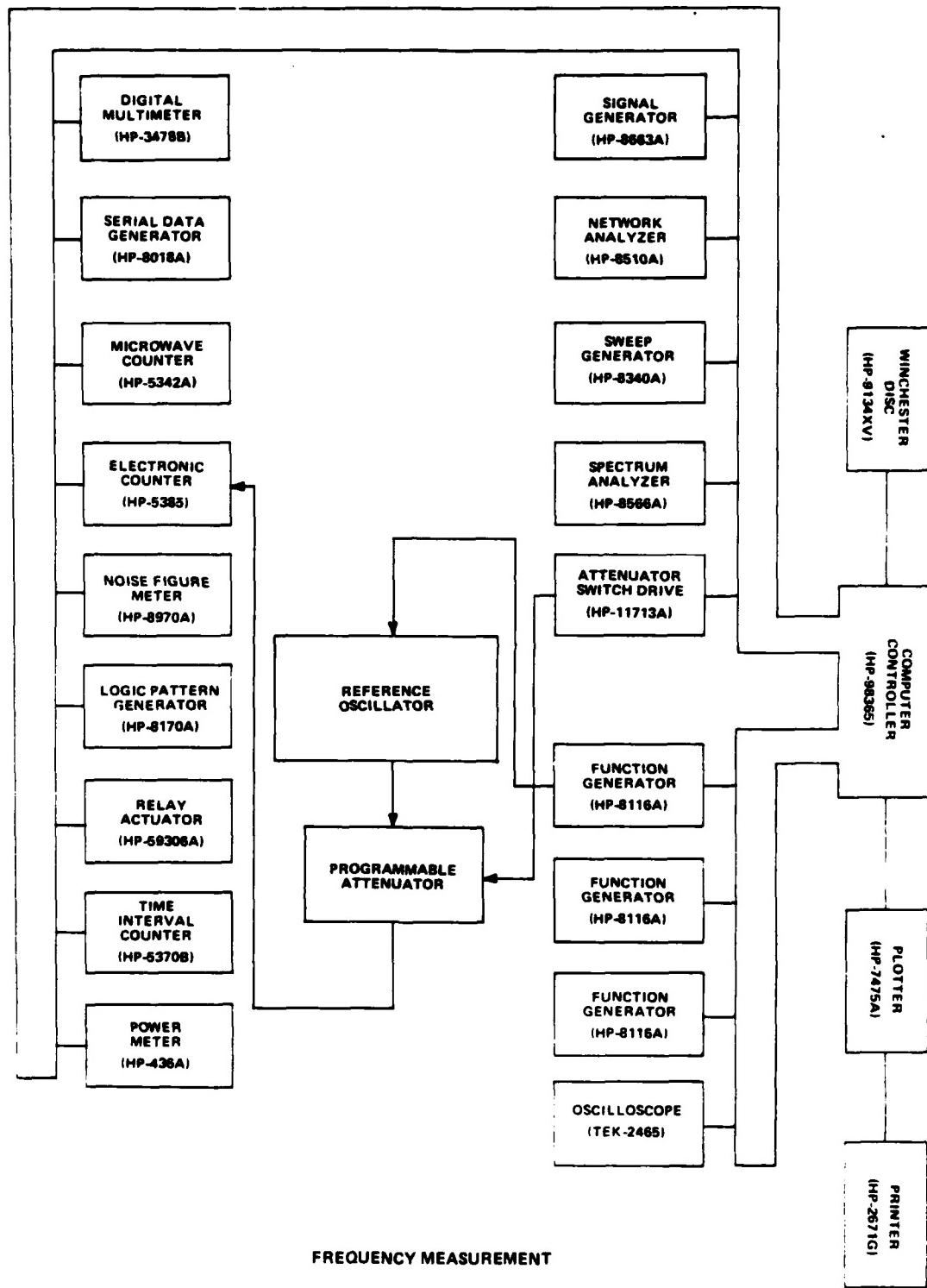
Test Procedure: Turn on test equipment. Set measurement equipment

to proper ranges. Apply DC power to oscillator board. Measure output
frequency with Electronic Counter.

Data Reduction: Send input levels and output of Electronic Counter
to printer.

Equipment List:

- | | | |
|-------------------------------------|-----------------------------------|----------|
| 1. <u>Func. Gen. (HP-8116A)</u> | 4. <u>Prog. Atten. (HP-8494G)</u> | 7. _____ |
| 2. <u>Elec Ctr (HP-5385)</u> | 5. <u>Printer (HP-2671G)</u> | 8. _____ |
| 3. <u>Atten. Sw Dr. (HP-11713A)</u> | 6. <u>Computer (HP-9836S)</u> | 9. _____ |



FREQUENCY MEASUREMENT

Contractor: Magnavox

Board Tested: Reference Oscillator

Test Objective: Verify proper oscillator output power level.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	DC Voltage	TBD	Function Gen. (HP-8116A)
2.			
3.			
4.			
5.			
6.			
7.			
8.			
9.			
10.			
11.			
12.			

Outputs

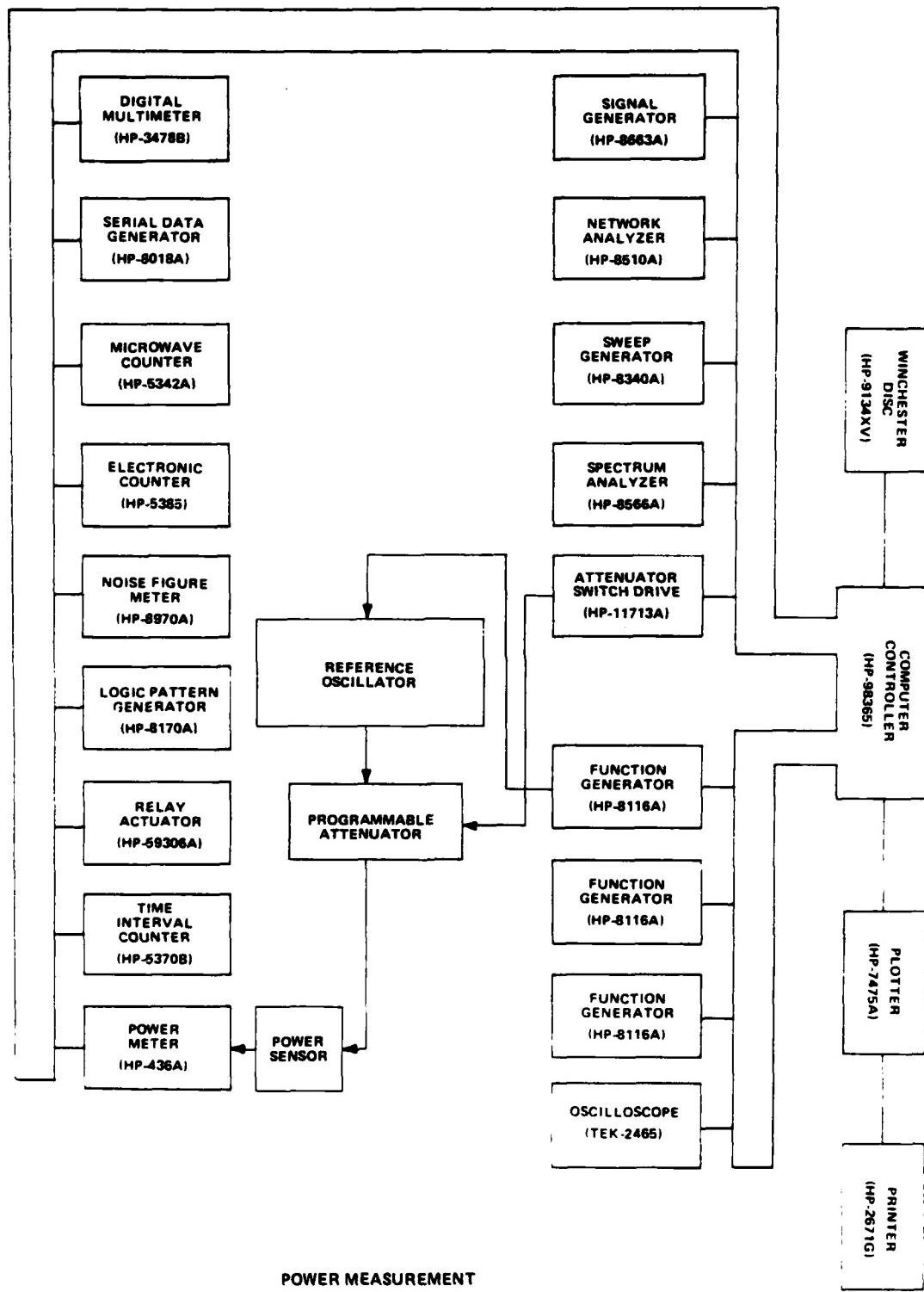
<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. <u>Oscillator Output</u>	<u>+7dBm ±1dB</u>	<u>Power Sensor (HP-8481A)</u>
2.		<u>Power Meter (HP-436A)</u>
3.		<u>Prog Atten. (HP-8494G)</u>
4.		<u>Atten. Sw Drive (HP-11713A)</u>
5.		

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Apply DC power to oscillator board. Measure output power level with Power Meter.

Data Reduction: Send input levels and output of Power Meter to printer.

Equipment List:

- | | | |
|-----------------------------------|-------------------------------------|-------------------------------|
| 1. <u>Func. Gen. (HP-8116A)</u> | 4. <u>Atten. Sw Dr. (HP-11713A)</u> | 7. <u>Computer (HP-9836S)</u> |
| 2. <u>Power Sensor (HP-8481A)</u> | 5. <u>Prog. Atten. (HP-8494G)</u> | 8. _____ |
| 3. <u>Power Meter (HP-436A)</u> | 6. <u>Printer (HP-2671G)</u> | 9. _____ |



POWER MEASUREMENT

Contractor: Magnavox
Board Tested: Reference Oscillator
Test Objective: Check oscillator output for spectral content.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	DC Voltage	TBD	Function Gen. (HP-8116A)
2.			
3.			
4.			
5.			
6.			
7.			
8.			
9.			
10.			
11.			
12.			

Outputs

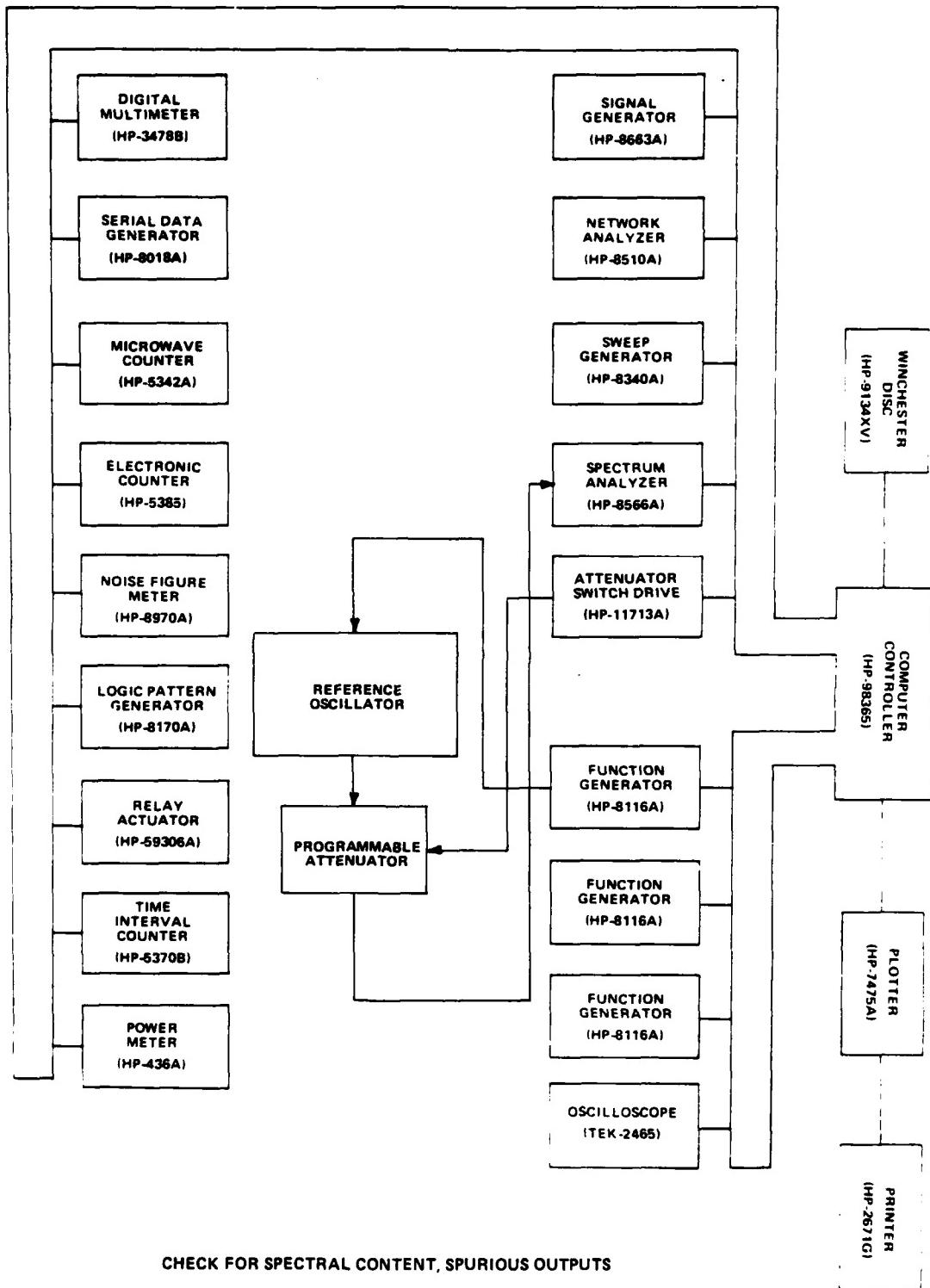
<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. <u>Oscillator output</u>	<u>+7dBm at 10.23MHz</u>	<u>Spect. Analyzer (HP-8566A)</u>
2.		<u>Atten. Sw Dr. (HP-11713A)</u>
3.		<u>Prog. Atten. (HP-8494G)</u>
4.		
5.		

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Apply DC power to oscillator board. Check spectral content of oscillator output for spurious outputs, harmonic levels etc.

Data Reduction: Send input levels to printer. Output of Spectrometer Analyzer to plotter.

Equipment List:

- | | |
|--------------------------------------|-----------------------------------|
| 1. <u>Func. Gen. (HP-8116A)</u> | 4. <u>Prog. Atten. (HP-8494G)</u> |
| 2. <u>Spect. Analyzer (HP-8566A)</u> | 5. <u>Plotter (HP-7475A)</u> |
| 3. <u>Att. Sw Dr. (HP-11713A)</u> | 6. <u>Computer (HP-3835C)</u> |



CHECK FOR SPECTRAL CONTENT, SPURIOUS OUTPUTS

Contractor: Magnavox

Board Tested: Reference Oscillator

Test Objective: Check oscillator output for stability, residual FM,
noise sidebands, and phase noise.

Inputs

<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1. DC Voltage	TBD	Function Gen. (HP-8116A)
2.		
3.		
4.		
5.		
6.		
7.		
8.		
9.		
10.		
11.		
12.		

Outputs

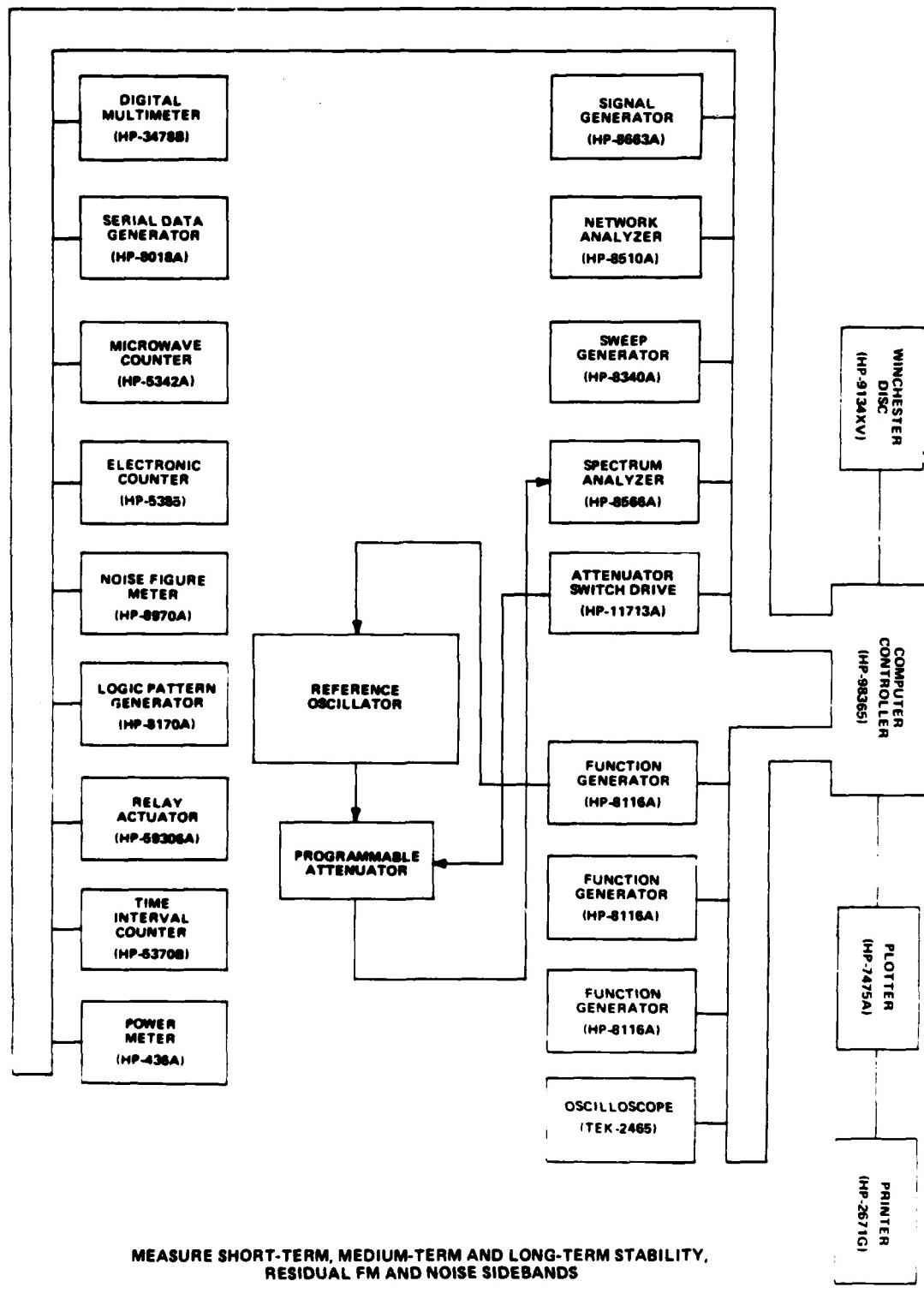
<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. <u>Oscillator Output</u>	Short term Stab. 1×10^{-12} (1-100 sec)	<u>Spect. Analyzer (HP-8566A)</u>
2.	1×10^{-11} (.1-1000 sec)	<u>Prog. Atten. (HP-8494G)</u>
3.	<u>Phase Noise -115dBc (2Hz)</u>	<u>Atten. Sw Drive (HP-11713A)</u>
4.	<u>Long term Stab. - TBD</u> Noise Sidebands - TBD	
5.	<u>Residual FM - TBD</u>	

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Apply DC power to oscillator board. Measure short and long term stability, phase noise, noise sidebands, and residual FM of oscillator output.

Data Reduction: Send input levels to printer. Output of Spectrum Analyzer to plotter.

Equipment List:

- | | | |
|--------------------------------------|-----------------------------------|------------------------------|
| 1. <u>Func. Gen. (HP-8116A)</u> | 4. <u>Prog. Atten. (HP-8494G)</u> | 7. <u>Printer (HP-2671G)</u> |
| 2. <u>Spect. Analyzer (HP-8566A)</u> | 5. <u>Plotter (HP-7475A)</u> | 8. _____ |
| 3. <u>Atten. Sw Dr. (HP-11713A)</u> | 6. <u>Computer (HP-9836S)</u> | 9. _____ |



MEASURE SHORT-TERM, MEDIUM-TERM AND LONG-TERM STABILITY,
RESIDUAL FM AND NOISE SIDEBOARDS

Contractor: Magnavox

Board Tested: Reference Oscillator

Test Objective: Verify within specification operation of
oscillator after 5 minute warm up.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	DC Voltage	TBD	Function Gen. (HP-8116A)
2.			
3.			
4.			
5.			
6.			
7.			
8.			
9.			
10.			
11.			
12.			

Outputs

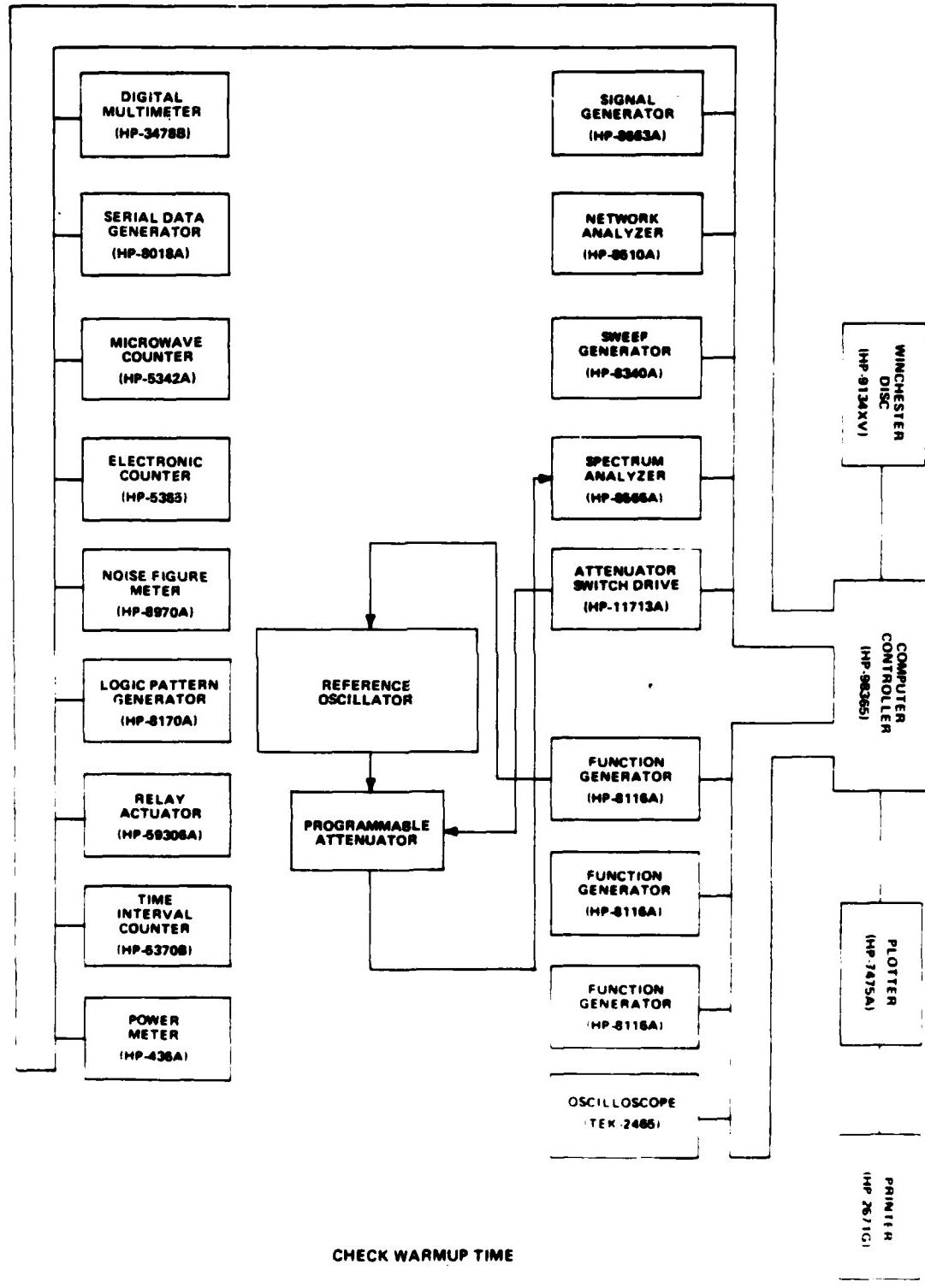
<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. <u>Oscillator Output</u>	---	<u>Spect. Analyzer (HP-8566A)</u>
2.		<u>Atten. Sw Dr. (HP-11713A)</u>
3.		<u>Prog. Atten. (HP-8494G)</u>
4.		
5.		

Test Procedure: Turn on test equipment. Set measurement equipment to proper ranges. Apply DC power to oscillator board. After 5 minute specification period take stability reading with Spectrum Analyzer to verify proper operation after the warm up period.

Data Reduction: Send input levels to printer. Output of Spectrum Analyzer to plotter.

Equipment List:

- | | | |
|-------------------------------------|-----------------------------------|------------------------------|
| 1. <u>Func. Gen. (HP-8116A)</u> | 4. <u>Prog. Atten. (HP-8494G)</u> | 7. <u>Printer (HP-2671G)</u> |
| 2. <u>Spect. Anal. (HP-8566A)</u> | 5. <u>Plotter (HP-7475A)</u> | 8. _____ |
| 3. <u>Atten. Sw Dr. (HP-11713A)</u> | 6. <u>Computer (HP-9836S)</u> | 9. _____ |



CHECK WARMUP TIME

4.0 SOFTWARE REQUIREMENTS

This section contains the software requirements for the bench test procedures defined in Section 3. These software requirements are defined in terms of flowcharts.

The software requirements contained herein are written at the functional level. There is a flowchart for each test procedure that illustrates the sequence of events that will be required in order to perform these tests by computer. In addition to these requirements, all tests will include the following capabilities:

- o menu driven testing will prompt operator through:
 1. test set-ups
 2. testing
 3. data reduction/storage/hardcopy
- o Each test will have a program module number and will be contained in a testing menu. All tests with the same set-ups can be grouped together to be run one after the other.
- o Each test will incorporate a full error-checking capability during operator inputs so that no manual error can be entered into the system.
- o Data reduction based on baseline data and/or range limits. The specified range values and/or tolerances will be stored and compared to the results obtained from the actual test. The values will be displayed and any differences can quickly be seen.
- o There will be real-time monitoring for any bus available information during testing.
- o There will be off-line storage or transfer of data for interfacing with the DPSSF/NSL system capabilities. This capability will be used for any information that is needed for the DPSSF, NSL or for RFL data reduction requirements.

- o All software will be stored including back-up disks and hardcopy listings in a library which will contain as a minimum the following information in its catalog:
 - o program module name and number (disk #)
 - o back-up copy # (disk #)
 - o date written, by whom
 - o date last revised, by whom

There are two floppy disk drives which have a memory of 170k each built into the computer. All information will be stored on either of these floppy disks. There is also a Winchester hard disk provided. This can be used as a temporary storage for data and information obtained while testing, if needed. For example, if a series of tests are being run and many data points are needed to be stored and all of the memory is being used in the computer, this information is stored temporarily on the Winchester disk until processing is complete and then archived onto the floppy disks.

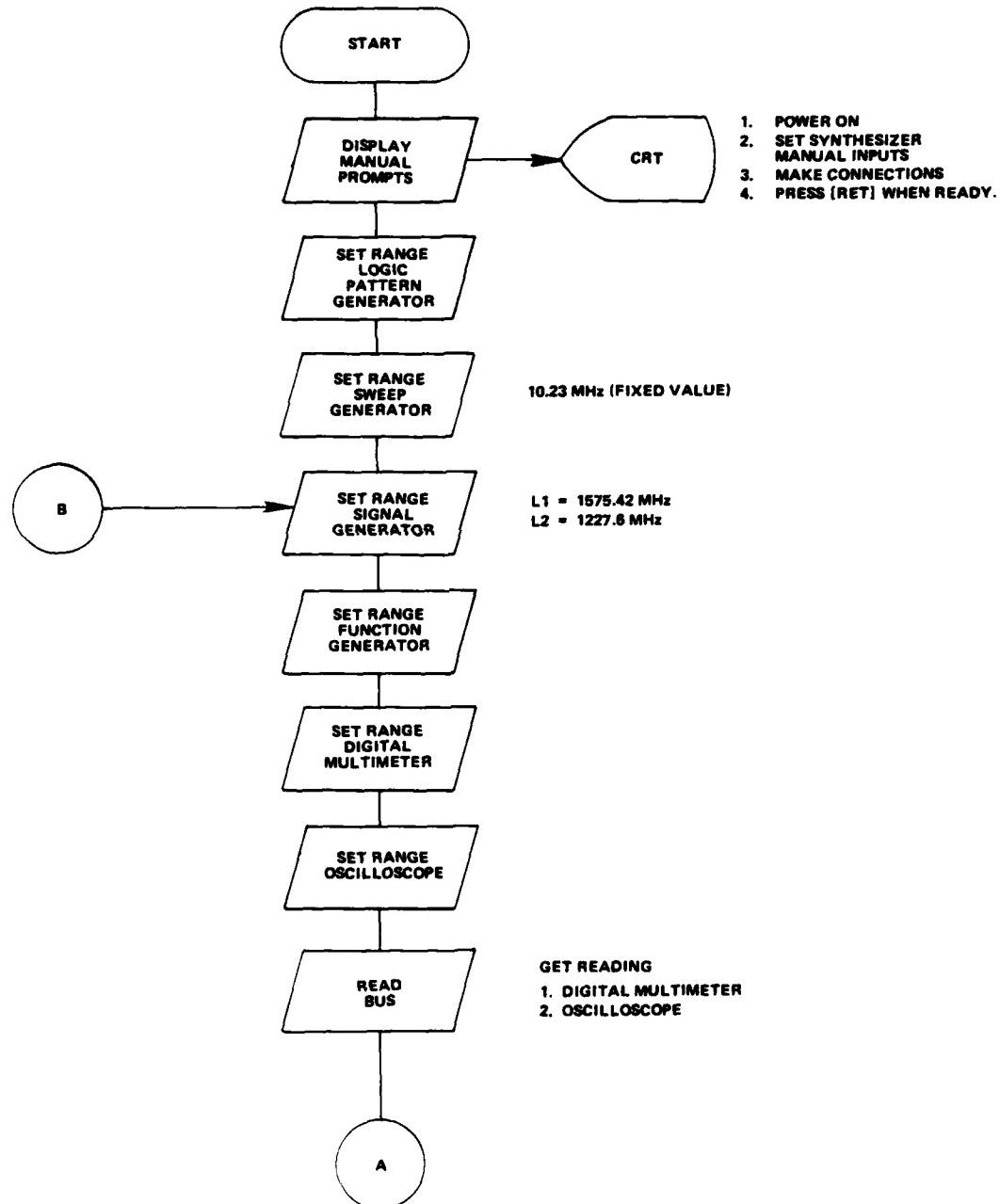
4.1 MAGNAVOX SOFTWARE REQUIREMENTS

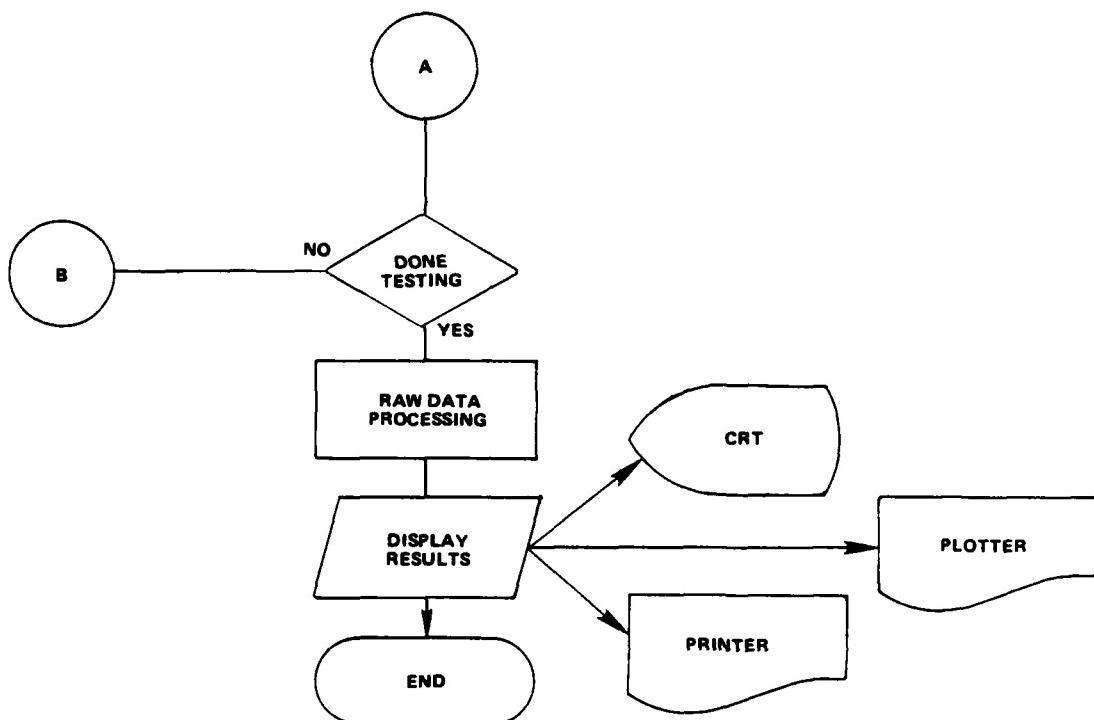
The following sheets contain the software requirements for the Bench test procedures for each of the boards identified in Section 3.1. The software requirements will also be refined and expanded as more detailed information becomes available for the bench tests.

4.1.1 RF SYNTHESIZER MODULE

GAIN AF1 MEASUREMENT

PROGRAM MODULE NAME: _____



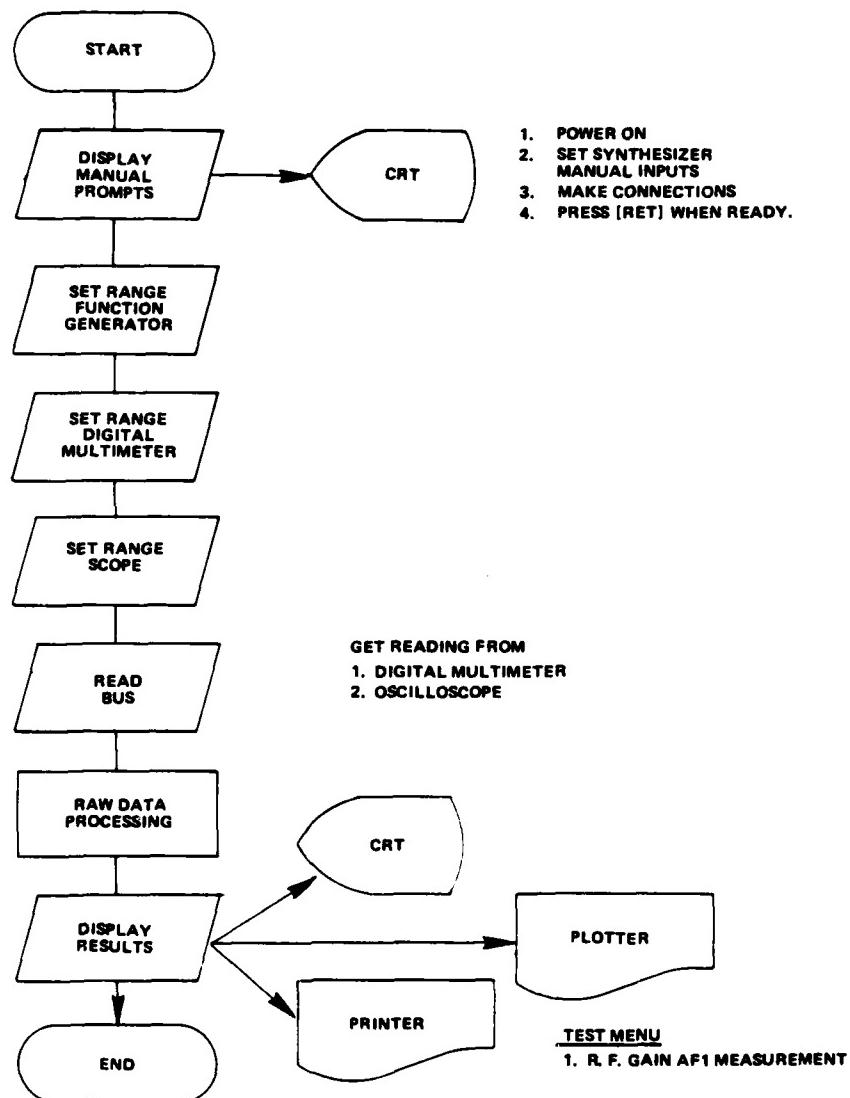


TEST MENU

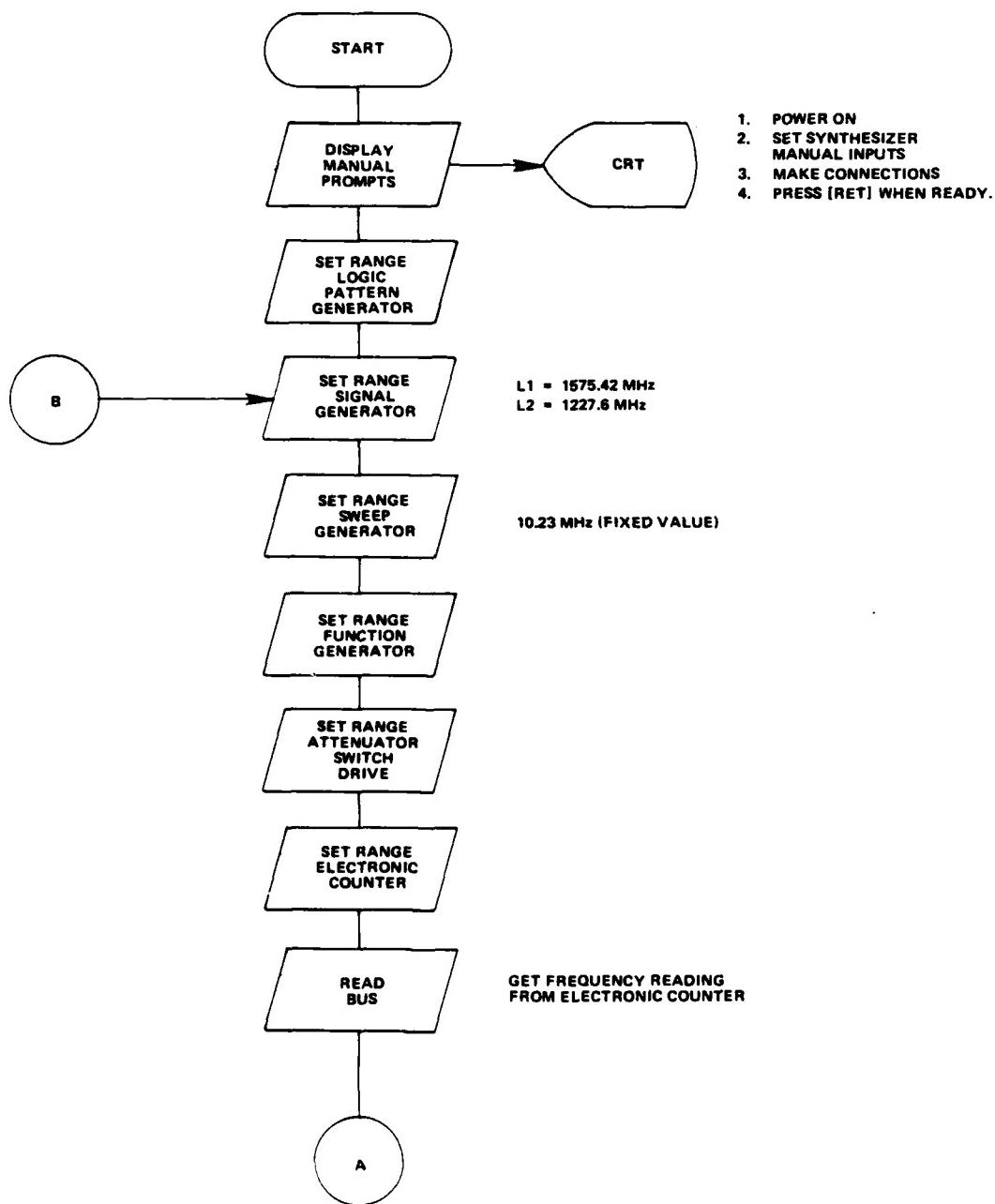
1. GAIN AF1 MEASUREMENT

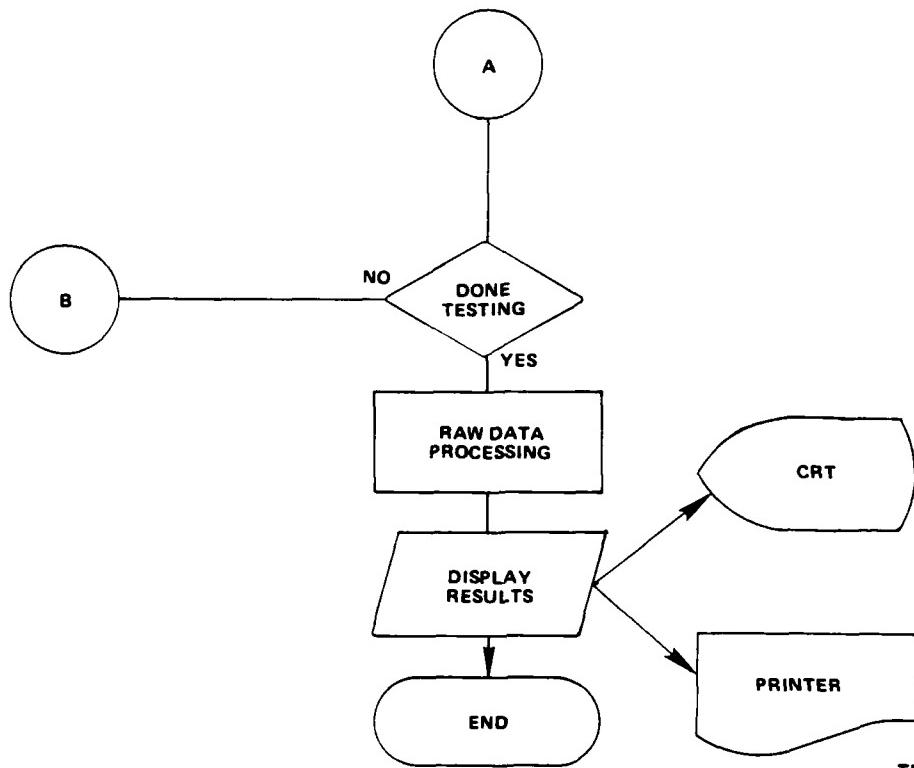
R. F. GAIN AF1 MEASUREMENT

PROGRAM MODULE NAME: _____



38F(IF) OUTPUT FREQUENCY
PROGRAM MODULE NAME: _____

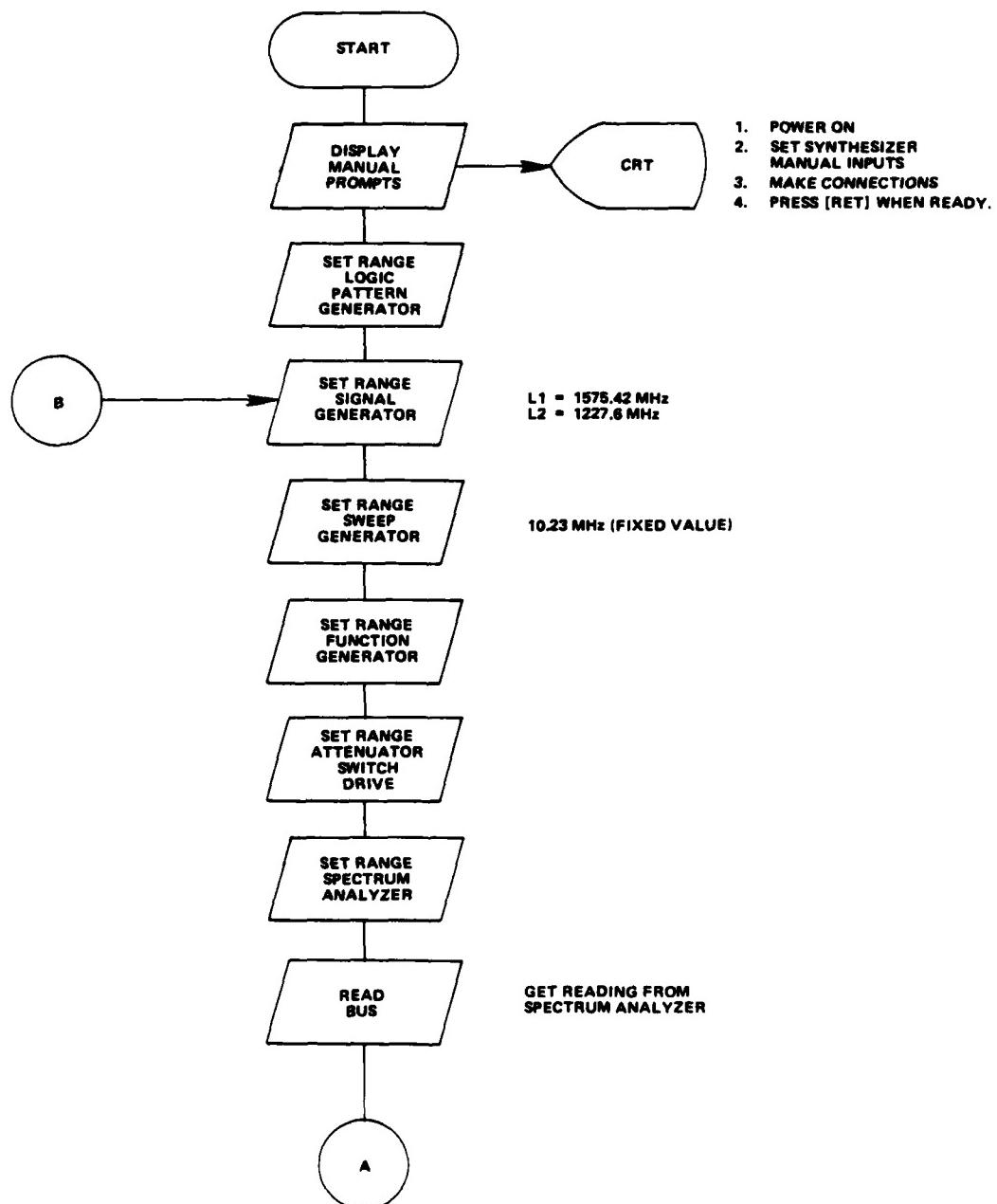


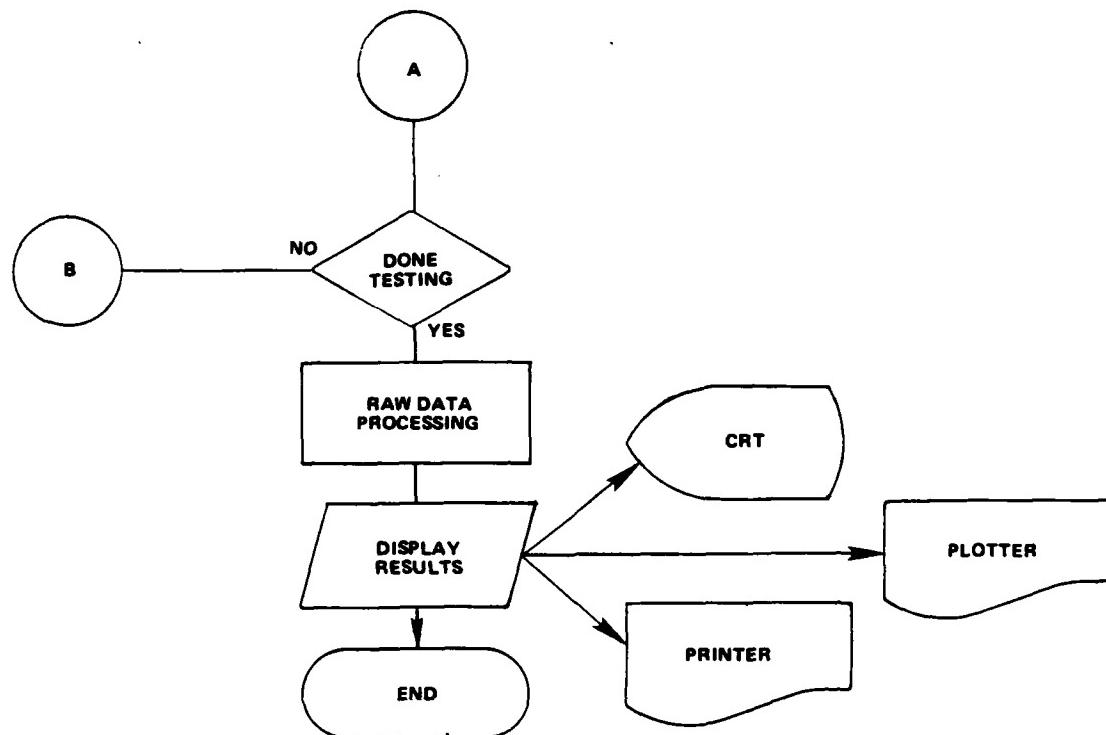


TEST MENU
1. 36F(IF) OUTPUT FREQUENCY

36F(IF) SPECTRAL CONTENT

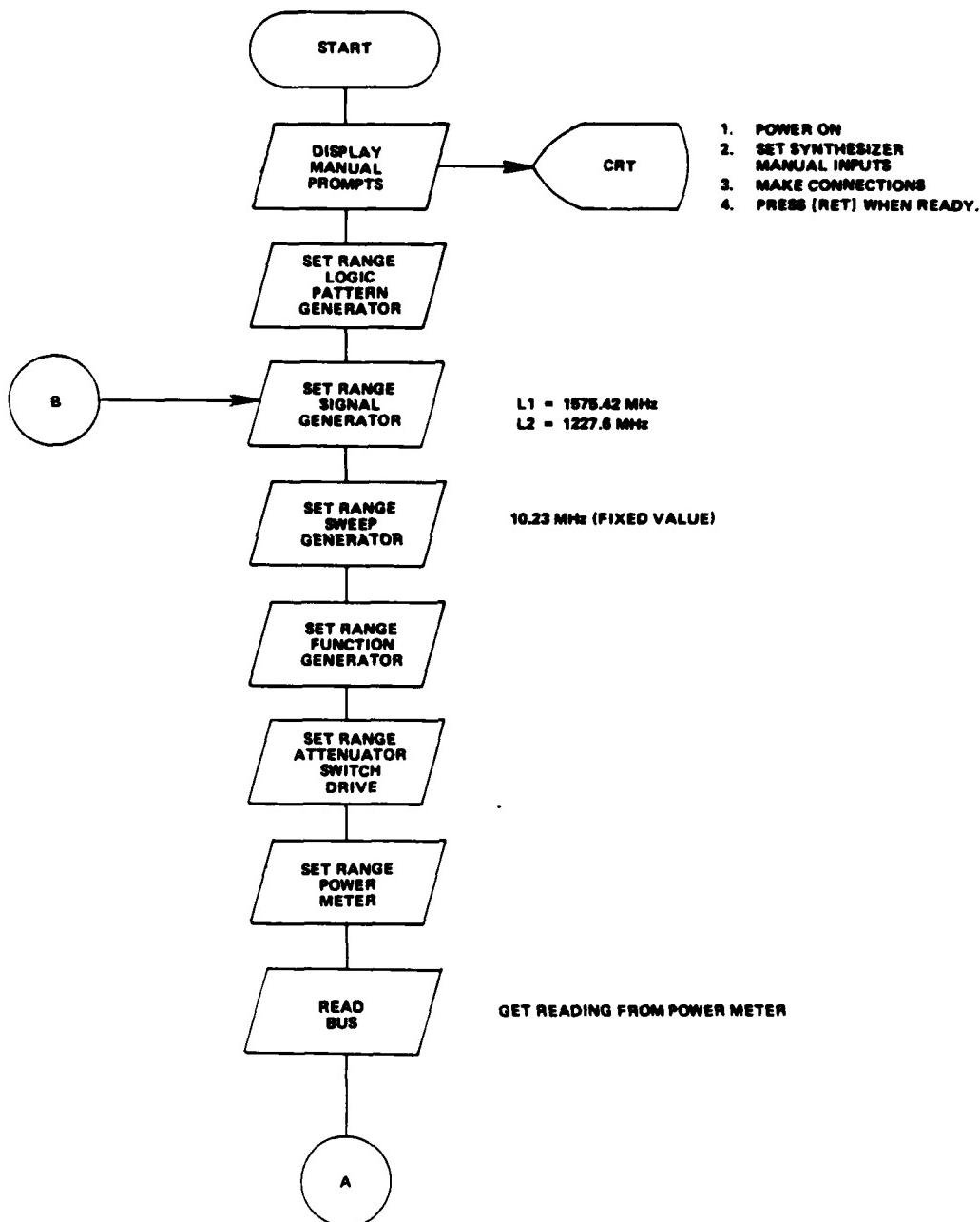
PROGRAM MODULE NAME: _____

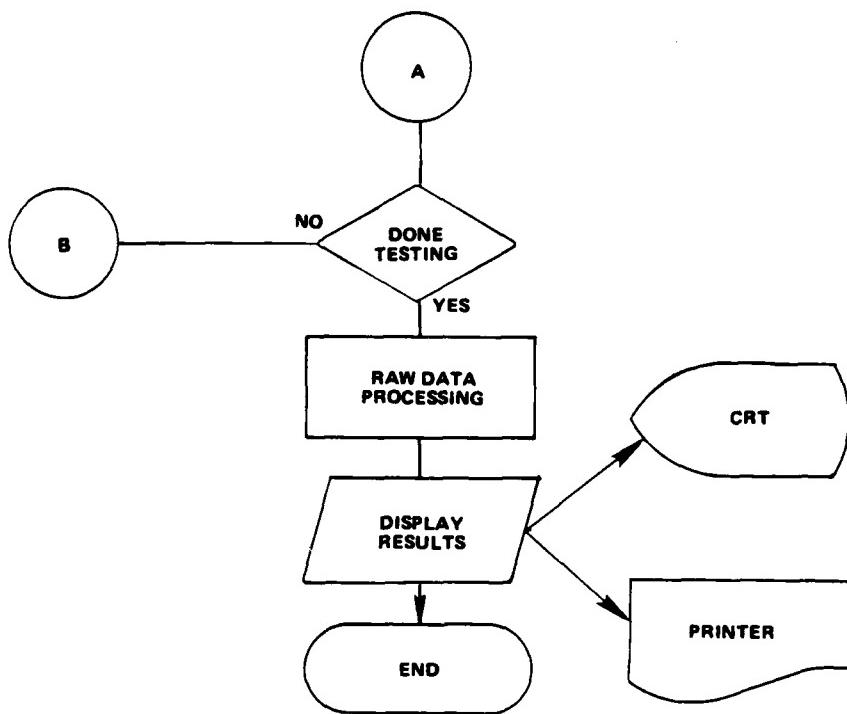




TEST MENU
1. 36F(IF) SPECTRAL CONTENT

36F(IF) OUTPUT POWER MEASUREMENT
PROGRAM MODULE NAME: _____



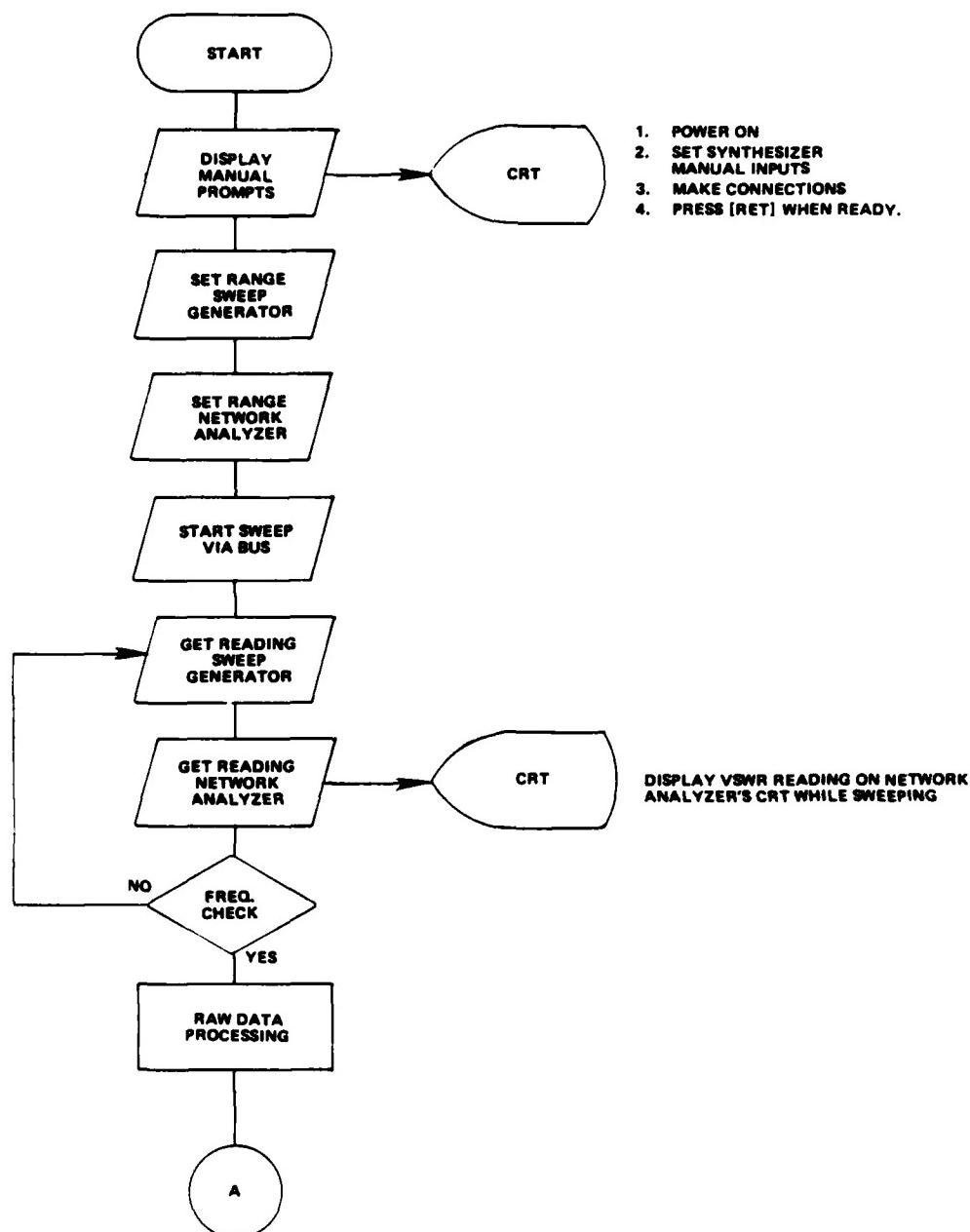


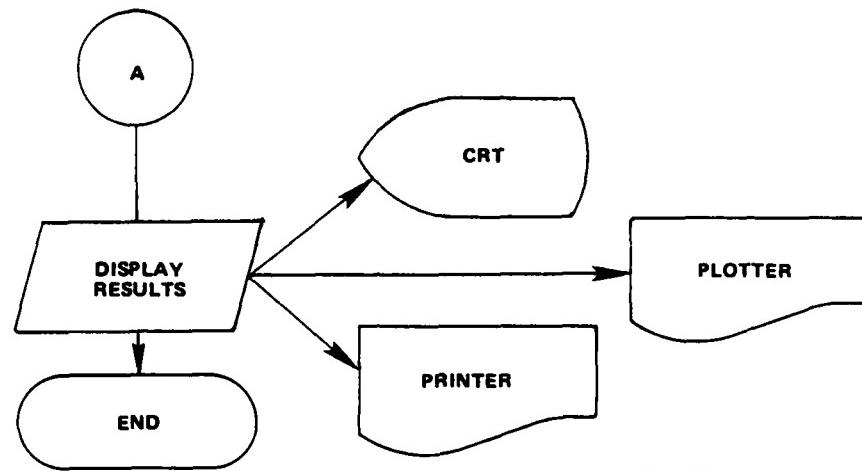
TEST MENU

1. 36F(I) OUTPUT POWER MEASUREMENT

36F(IF) VSWR AND IMPEDANCE MEASUREMENT

PROGRAM MODULE NAME: _____



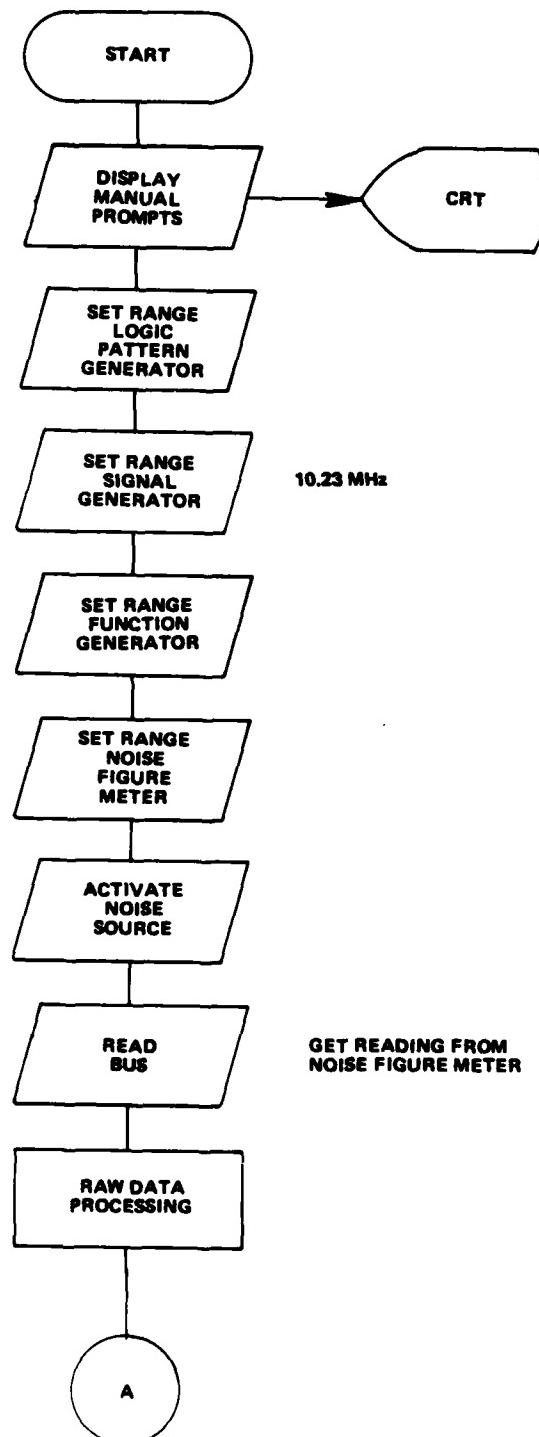


TEST MENU

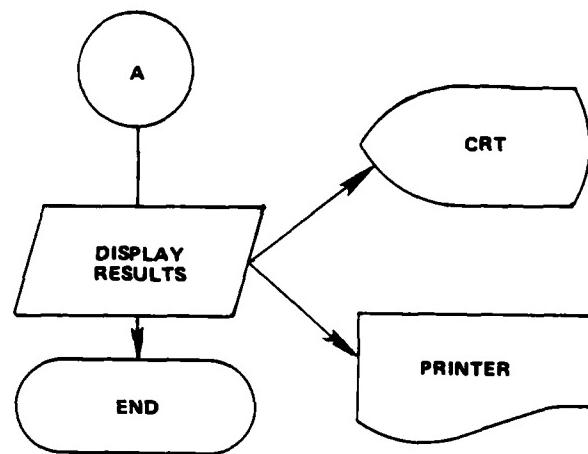
1. 36F(IF) VSWR AND IMPEDANCE MEASUREMENT

36F(IF) NOISE FIGURE MEASUREMENT

PROGRAM MODULE NAME: _____



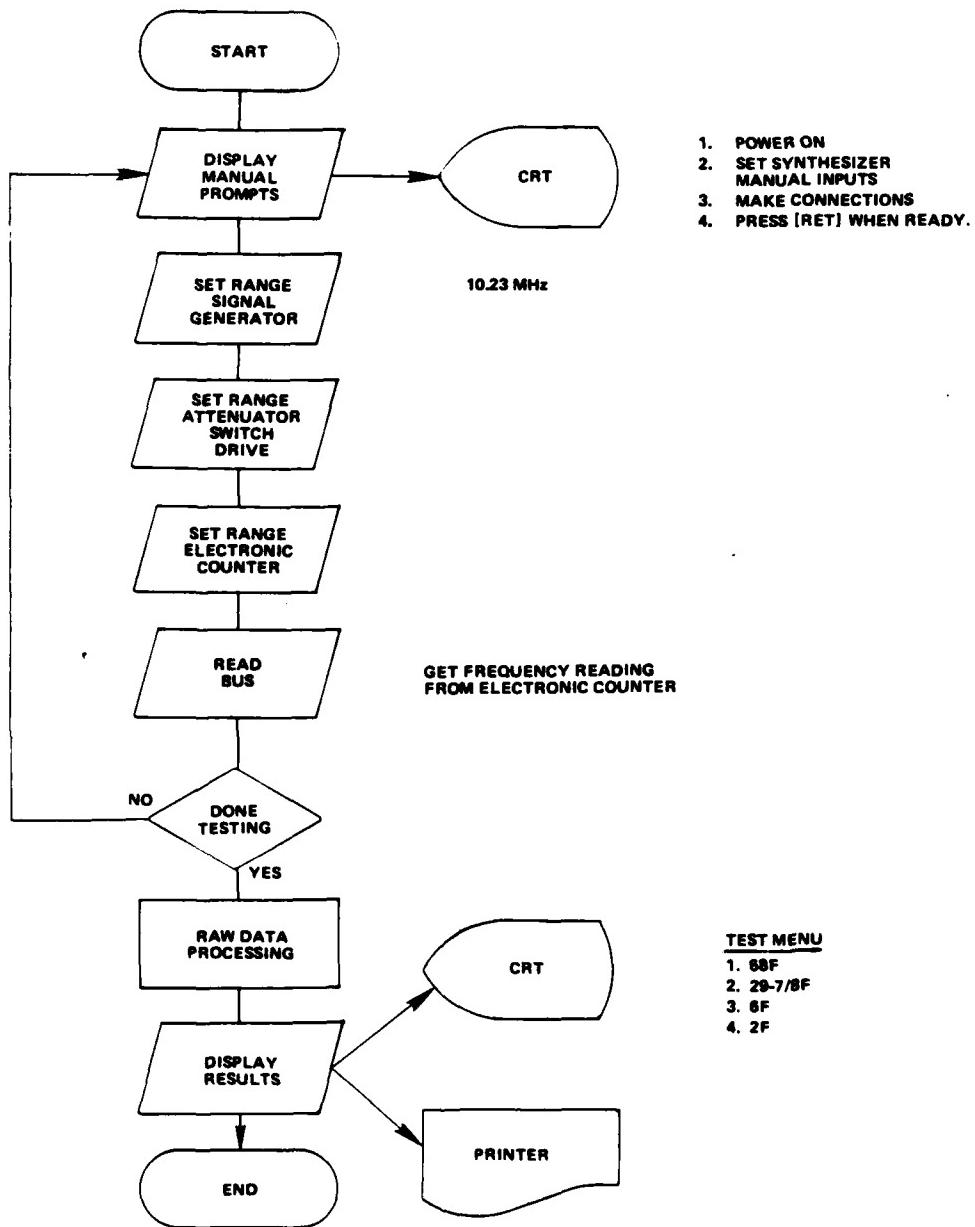
1. POWER ON
2. SET SYNTHESIZER
MANUAL INPUTS
3. MAKE CONNECTIONS
4. PRESS [RET] WHEN READY.



TEST MENU

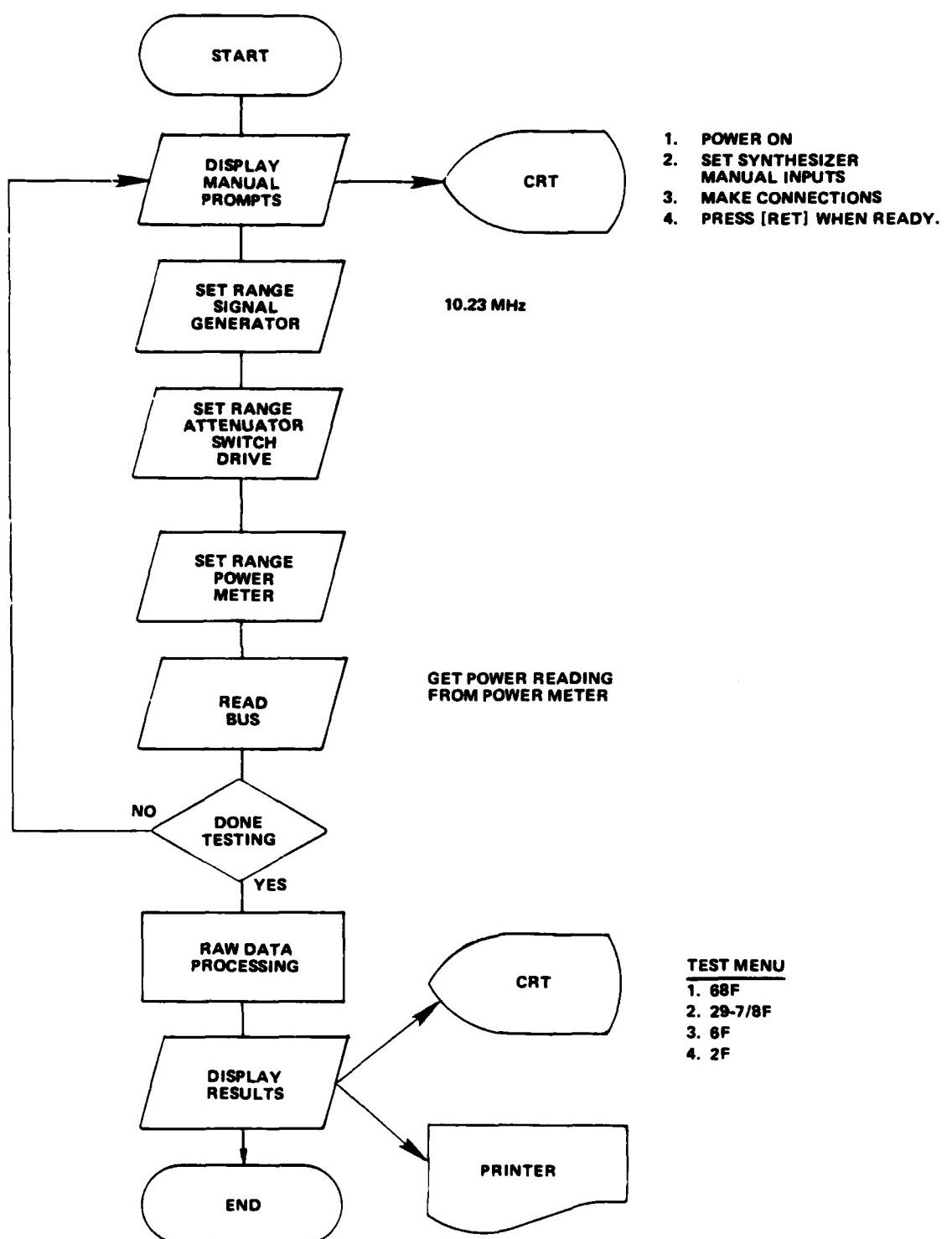
1. 36F(IF) NOISE FIGURE MEASUREMENT

OUTPUT FREQUENCY MEASUREMENTS
 PROGRAM MODULE NAME: _____

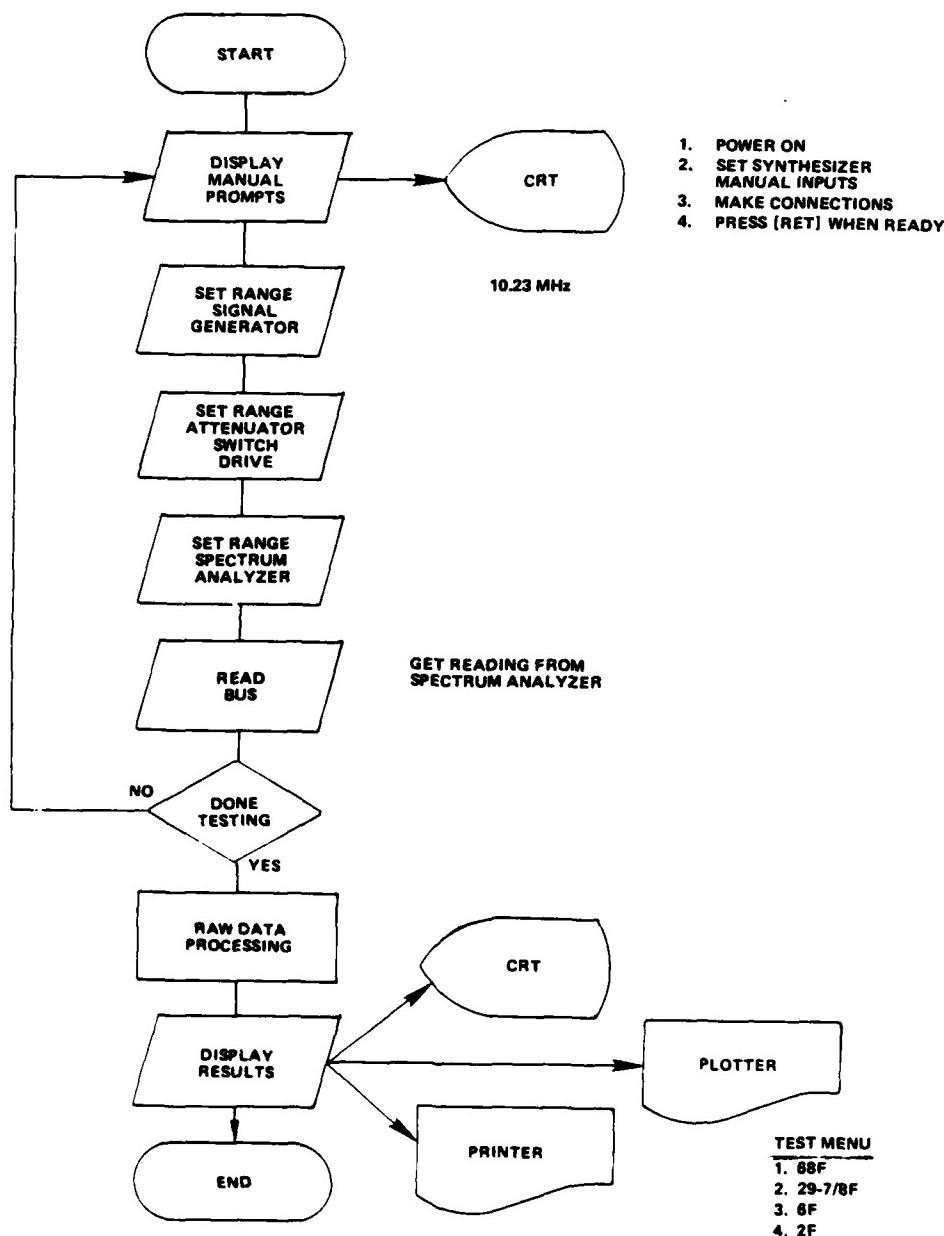


OUTPUT POWER MEASUREMENT

PROGRAM MODULE NAME: _____

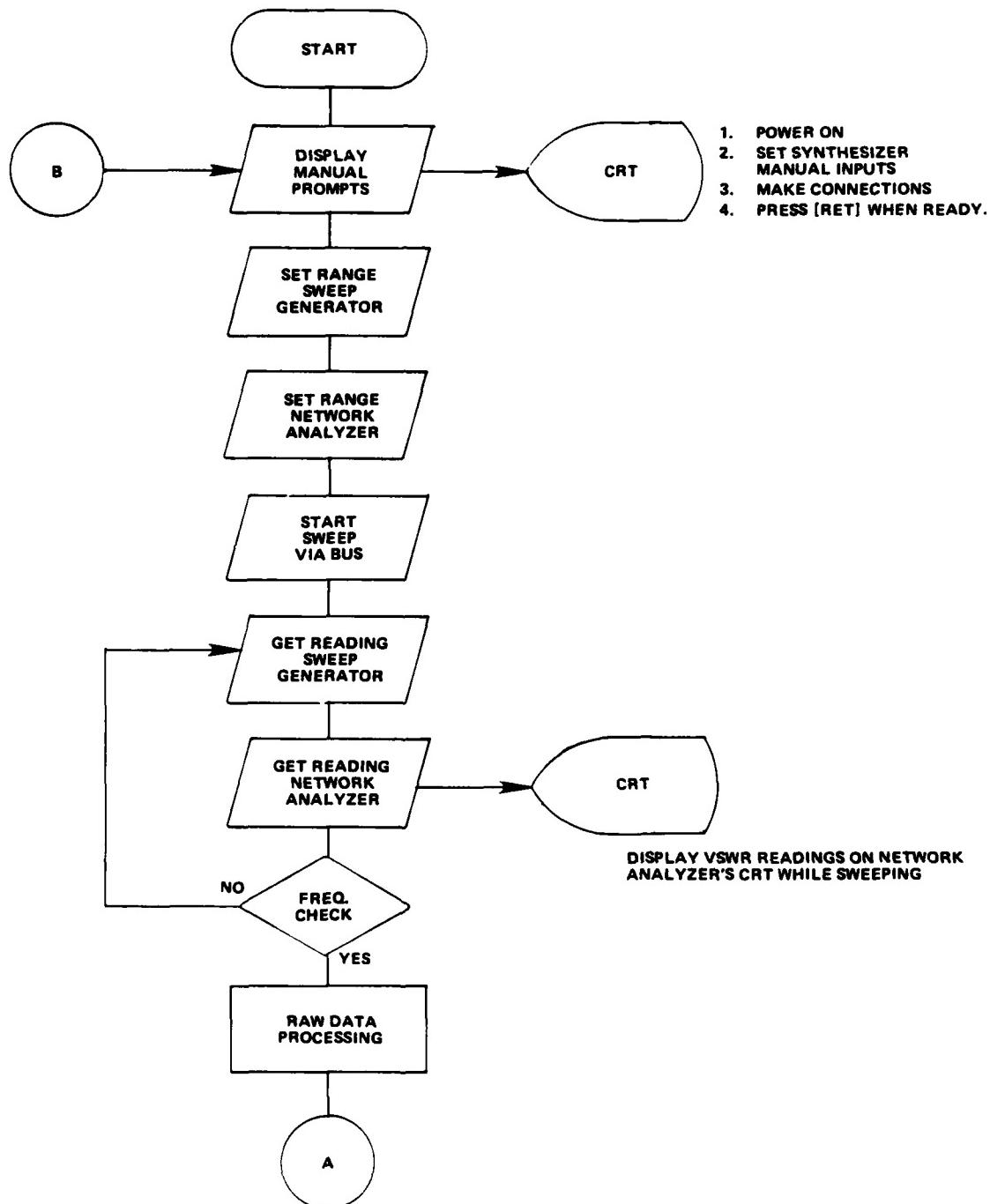


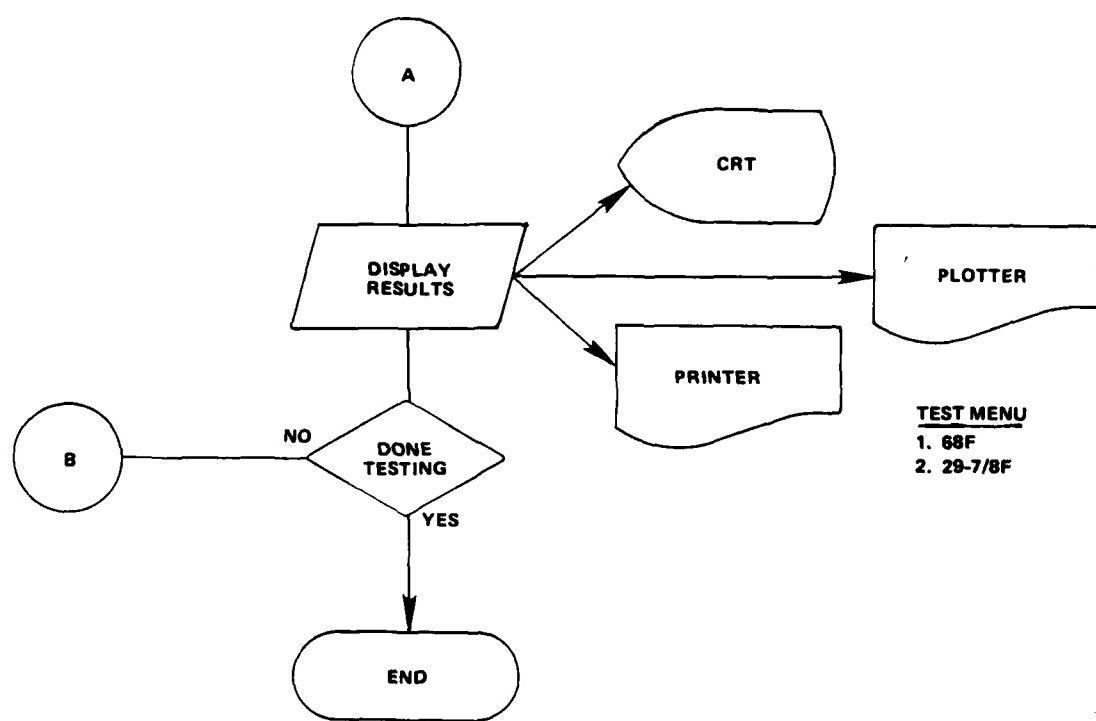
MEASURE SPECTRAL CONTENT
PROGRAM MODULE NAME: _____



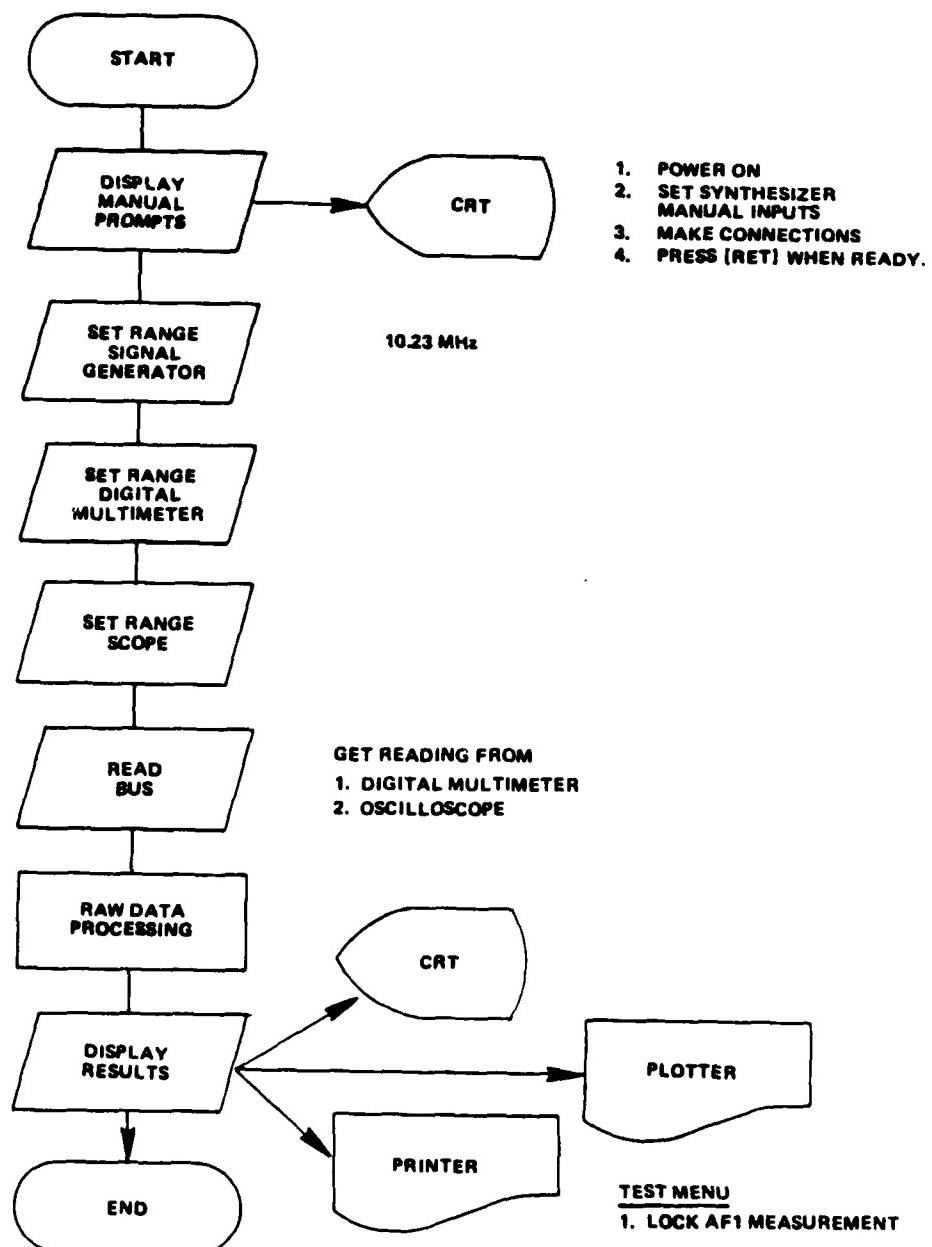
VSWR AND IMPEDANCE MEASUREMENT

PROGRAM MODULE NAME: _____



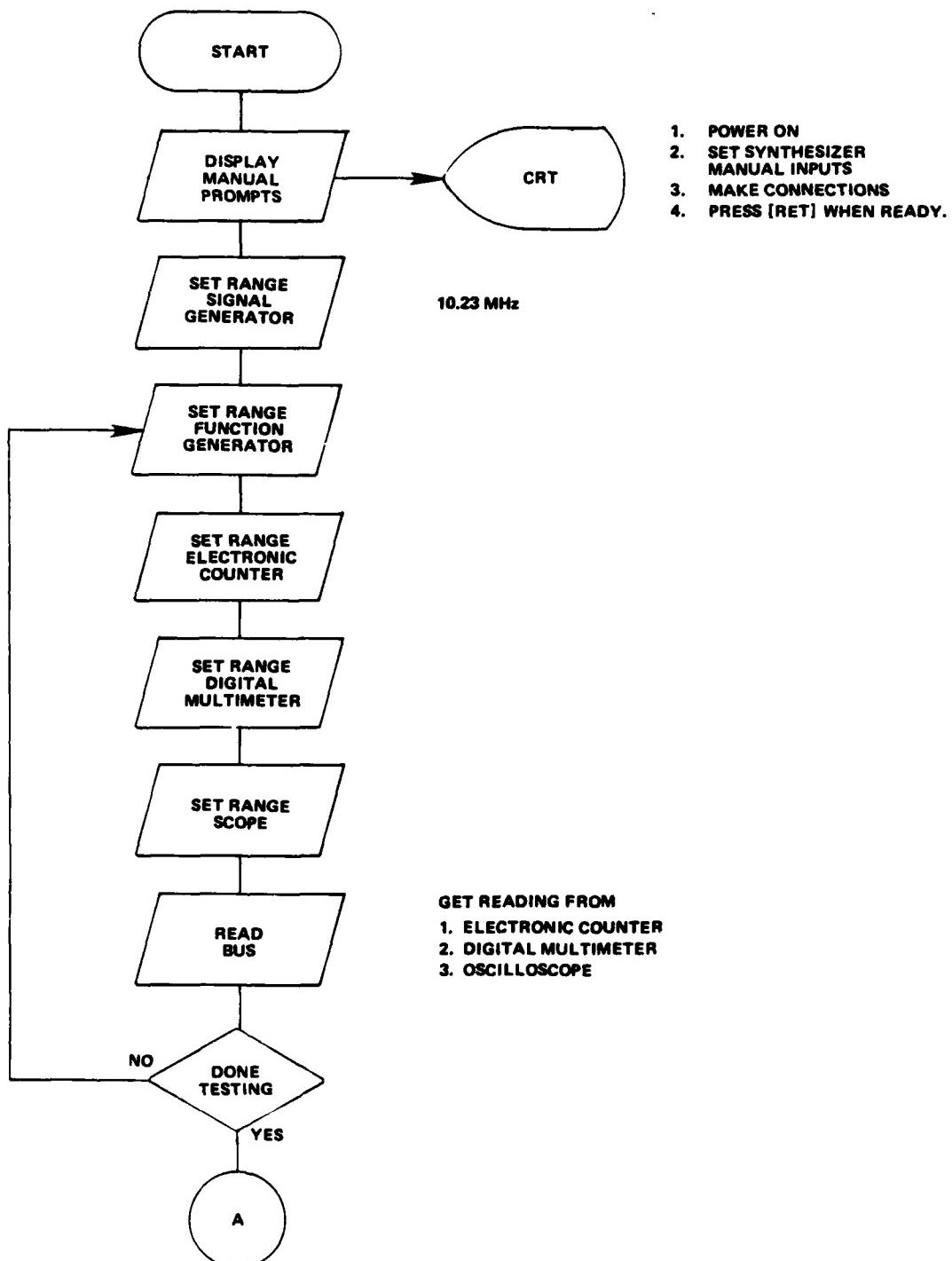


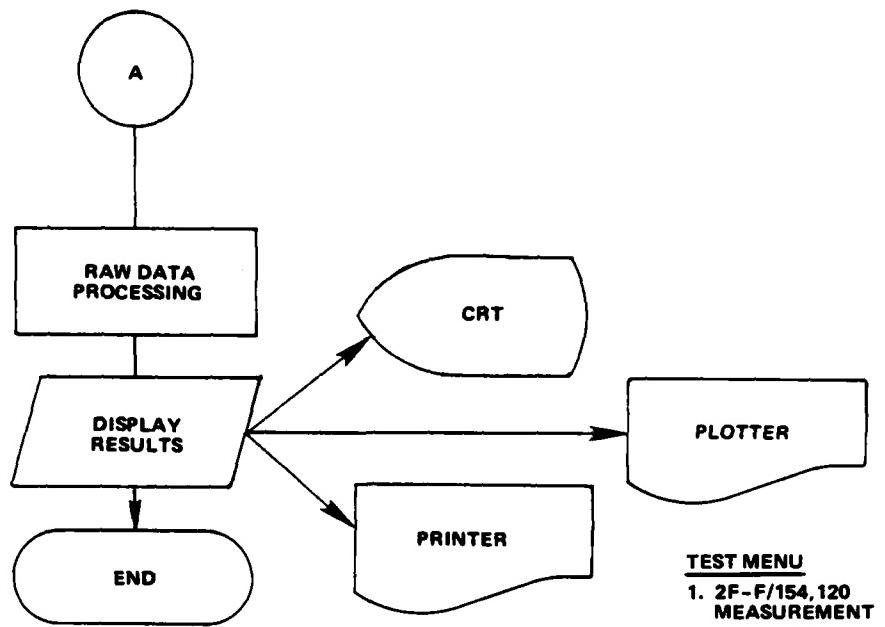
MEASURE LOCK AF1
PROGRAM MODULE NAME: _____



MEASURE 2F-F/154,120

PROGRAM MODULE NAME: _____

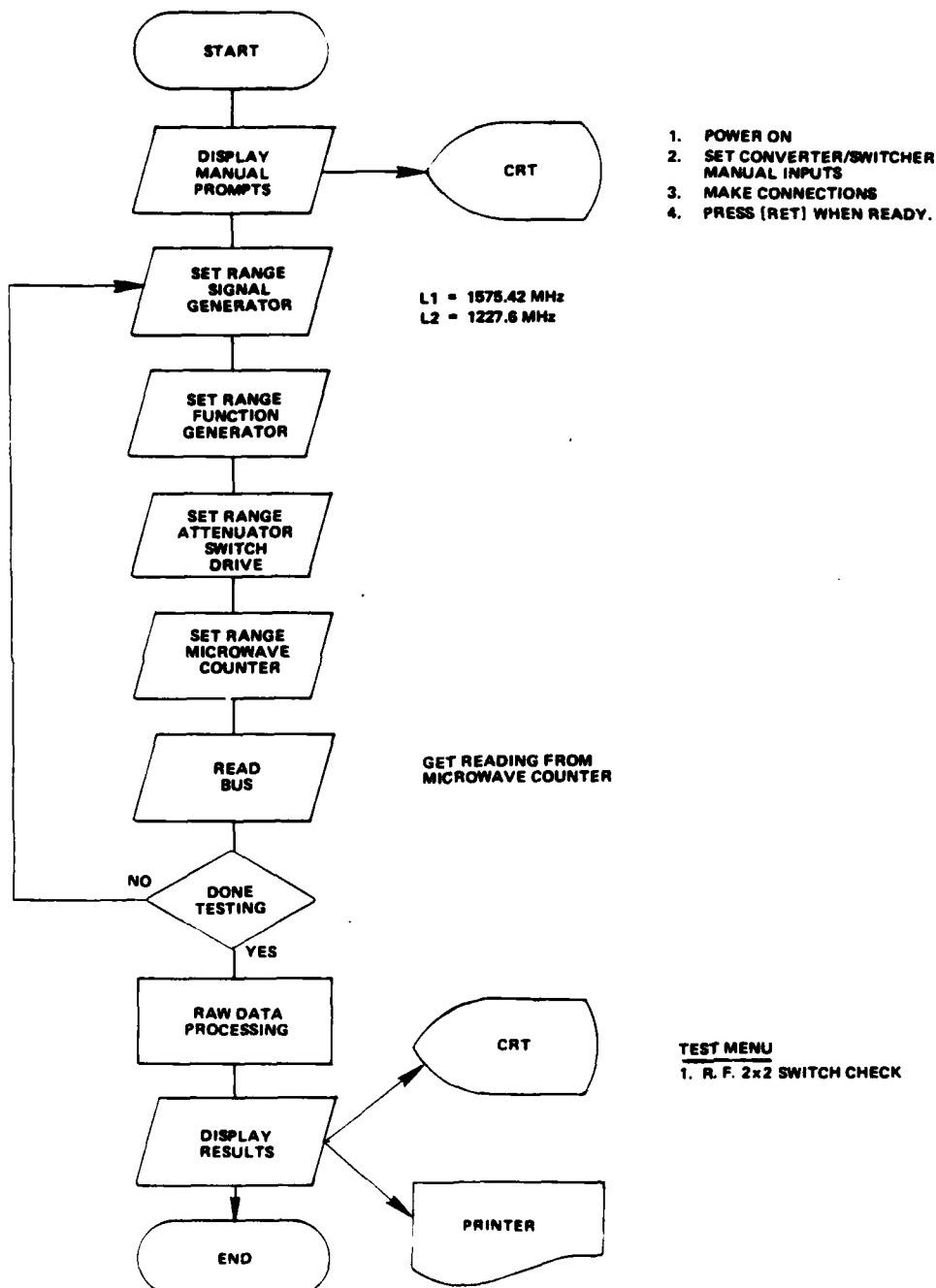




4.1.2 CONVERTER/CHANNEL SWITCHER MODULE

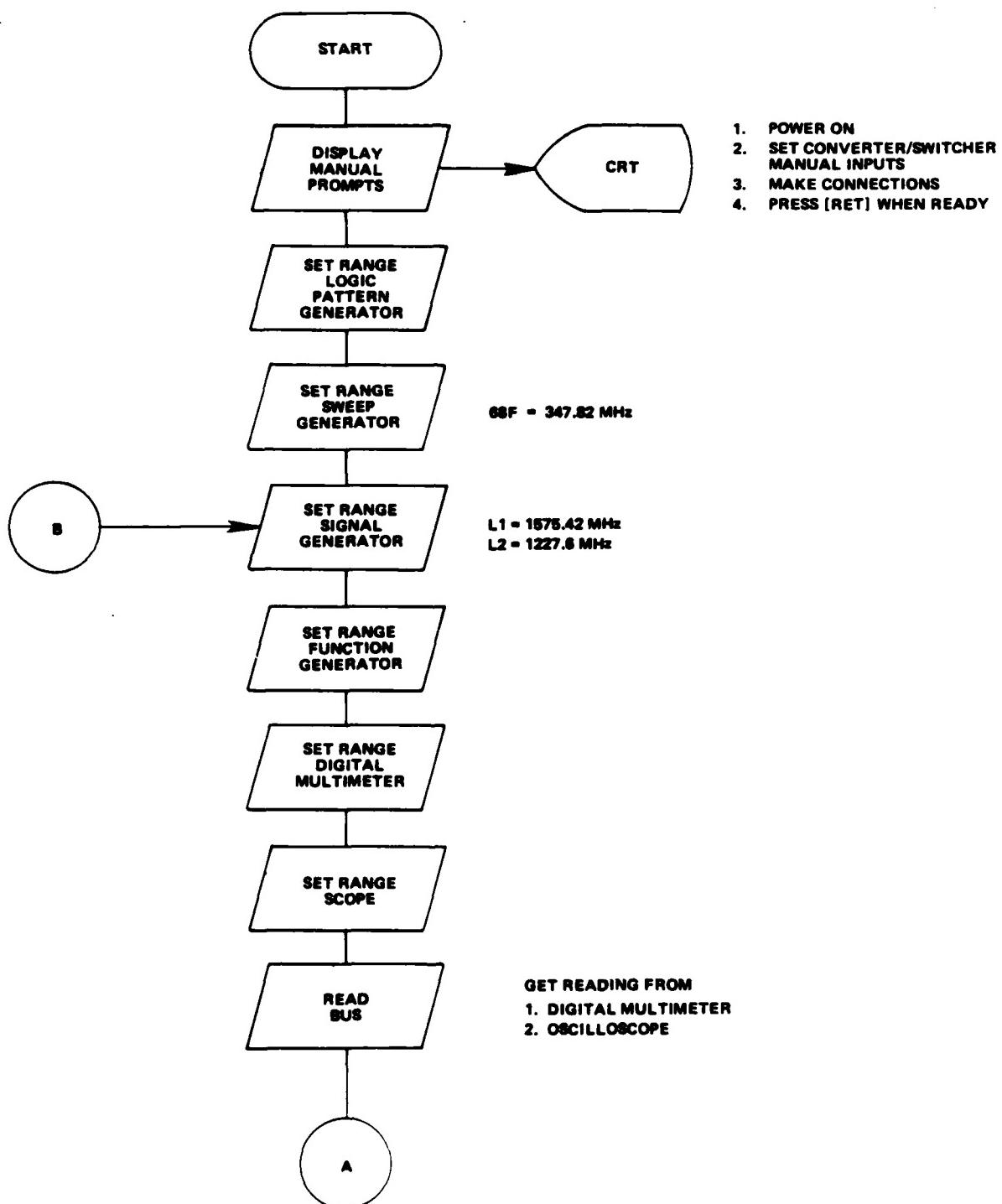
R. F. 2x2 SWITCH CHECK

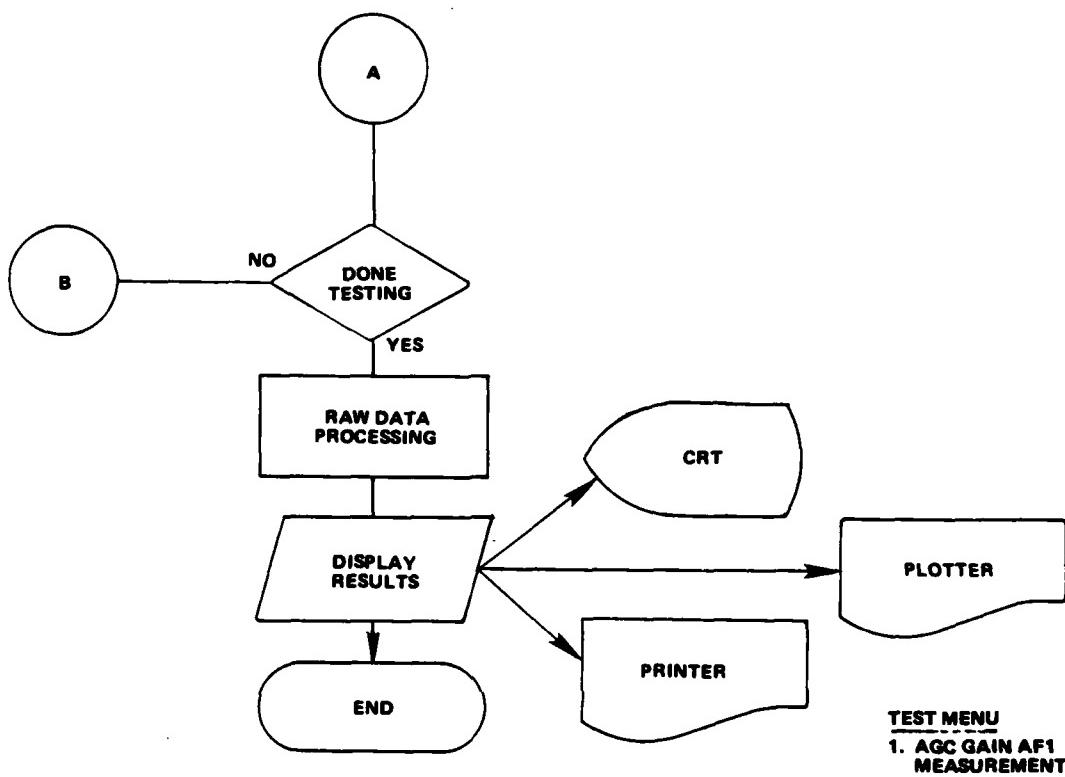
PROGRAM MODULE NAME: _____



MEASURE AGC GAIN AF1

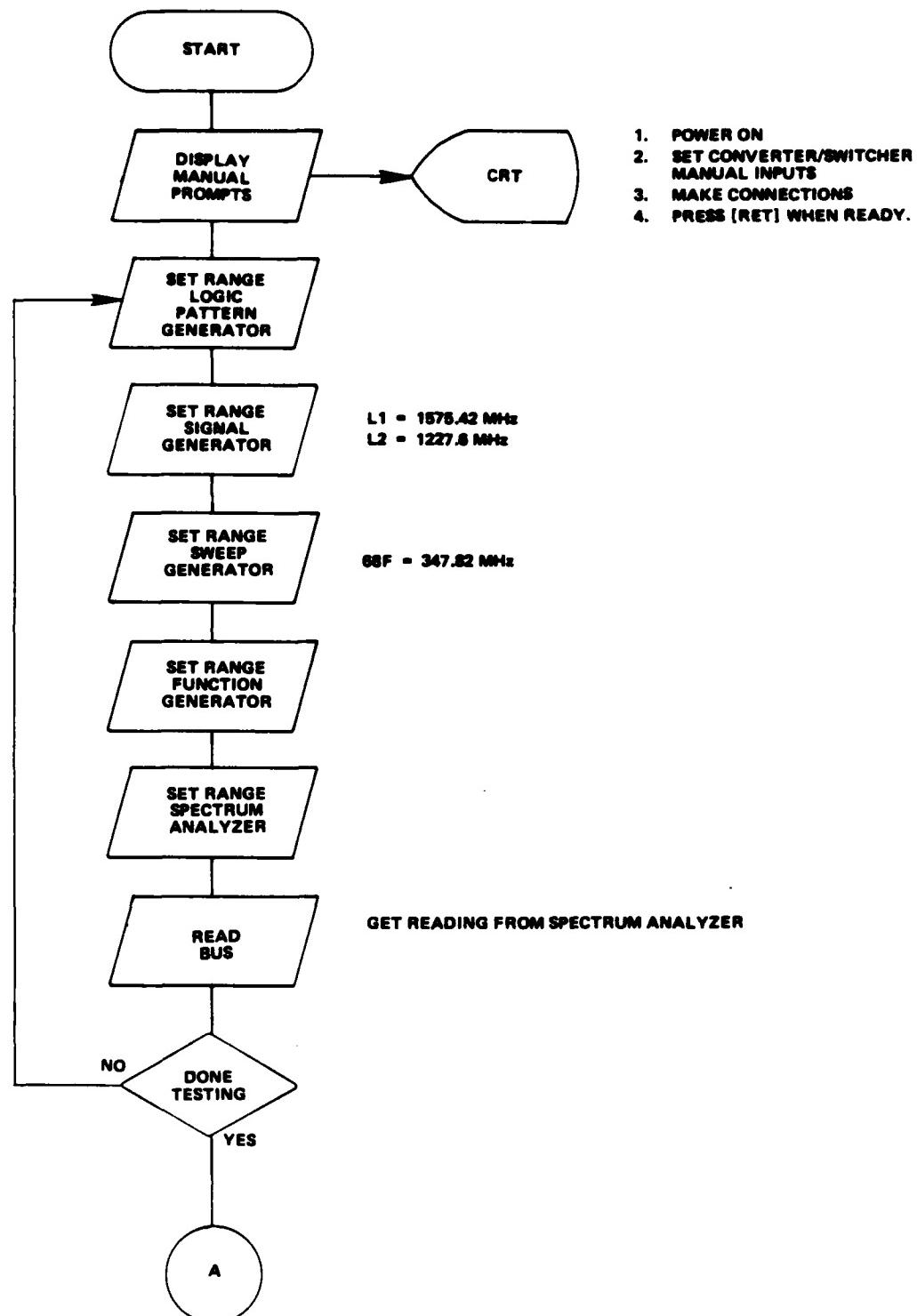
PROGRAM MODULE NAME: _____

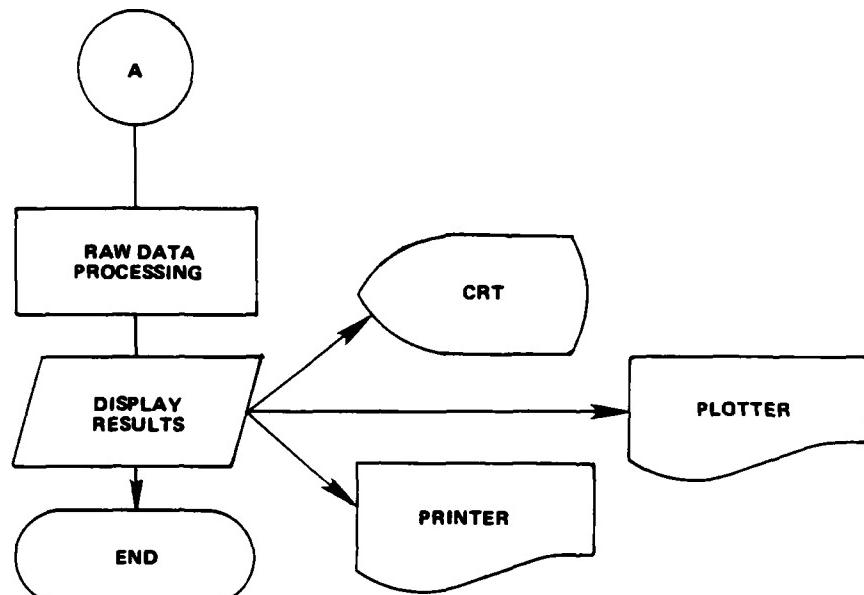




38F(IF) SWITCH OPERATION

PROGRAM MODULE NAME: _____

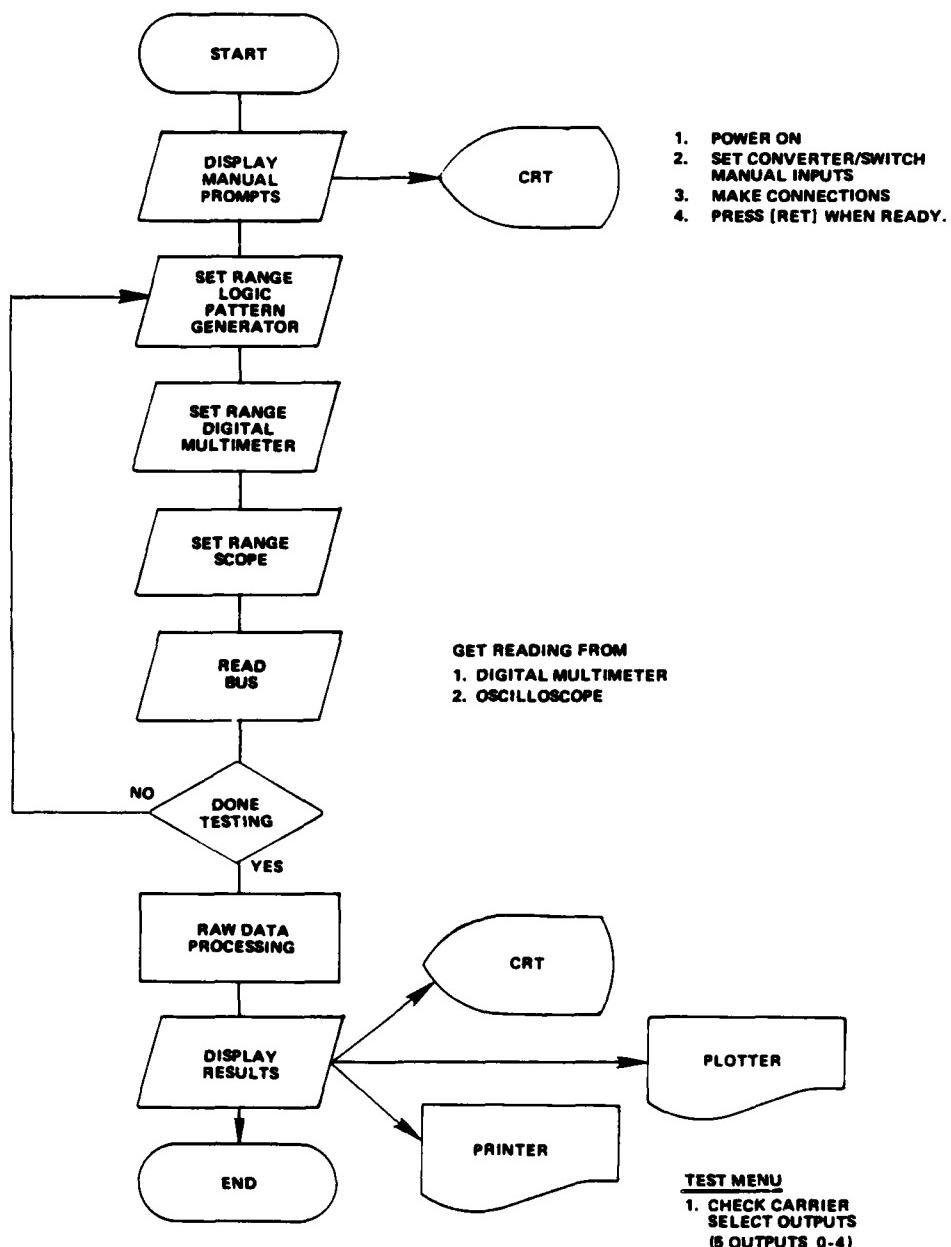




TEST MENU

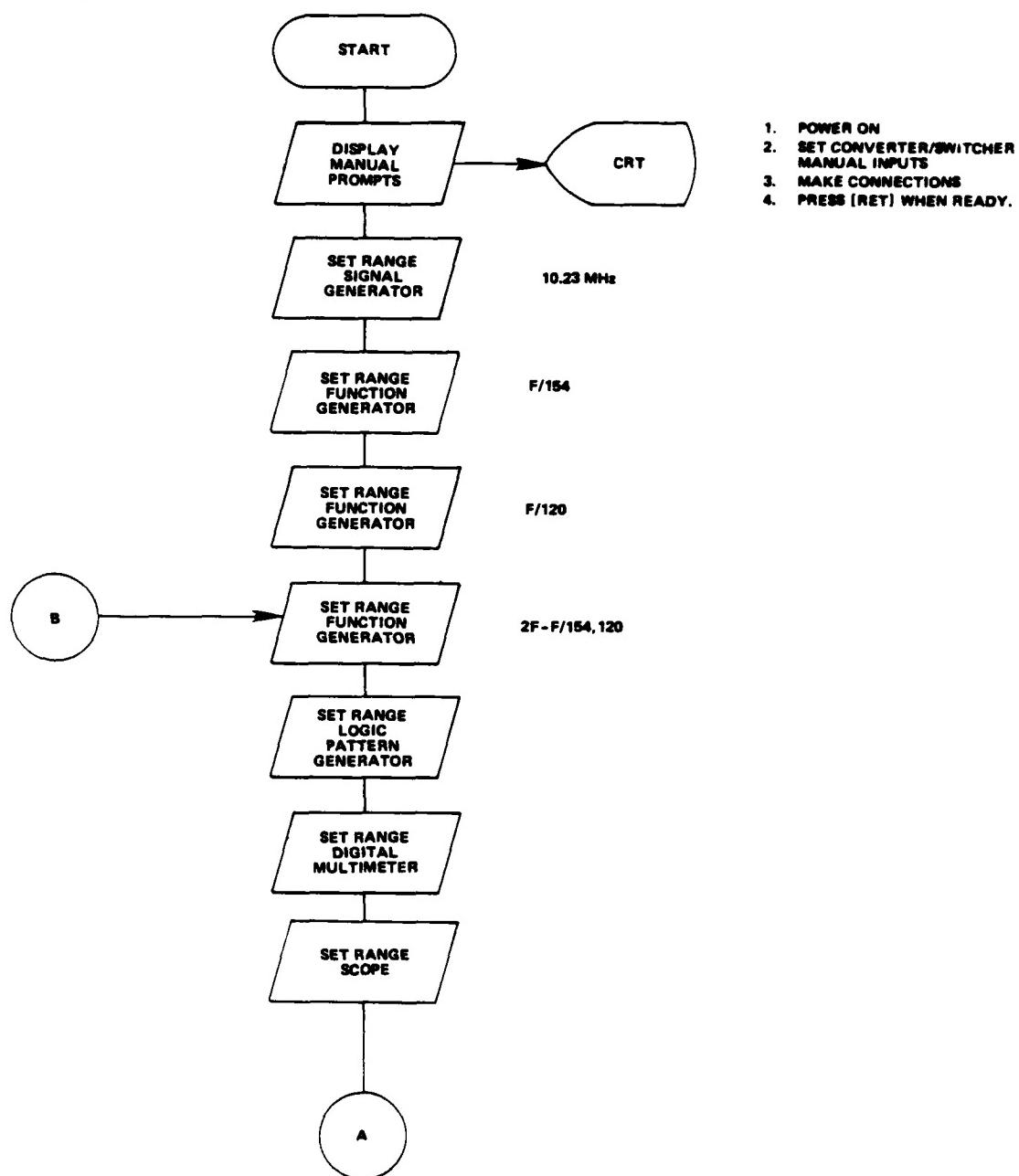
1. 36F(IF) SWITCH OPERATION
5-CHANNEL (0 - 4)

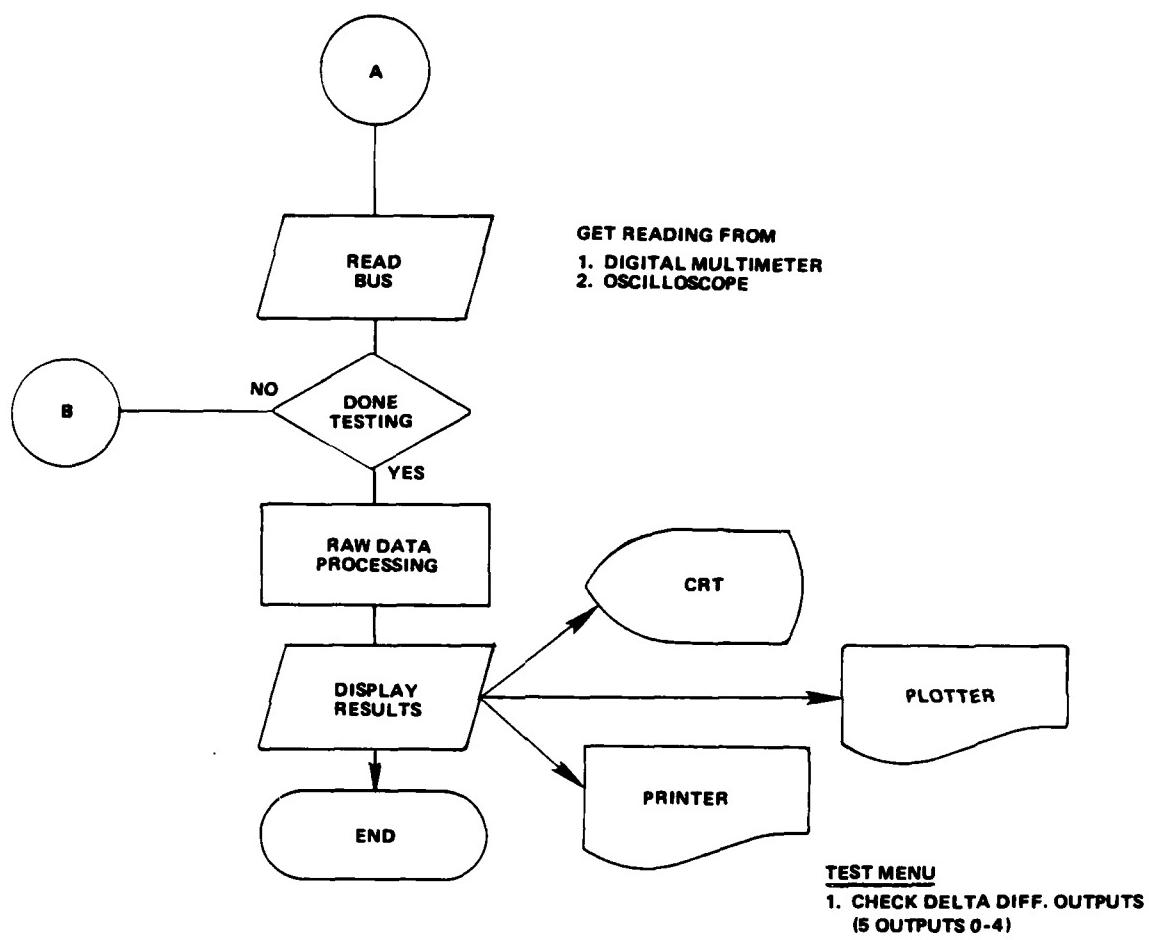
CHECK CARRIER SELECT OUTPUTS
 PROGRAM MODULE NAME: _____



CHECK DELTA DIFF. OUTPUTS

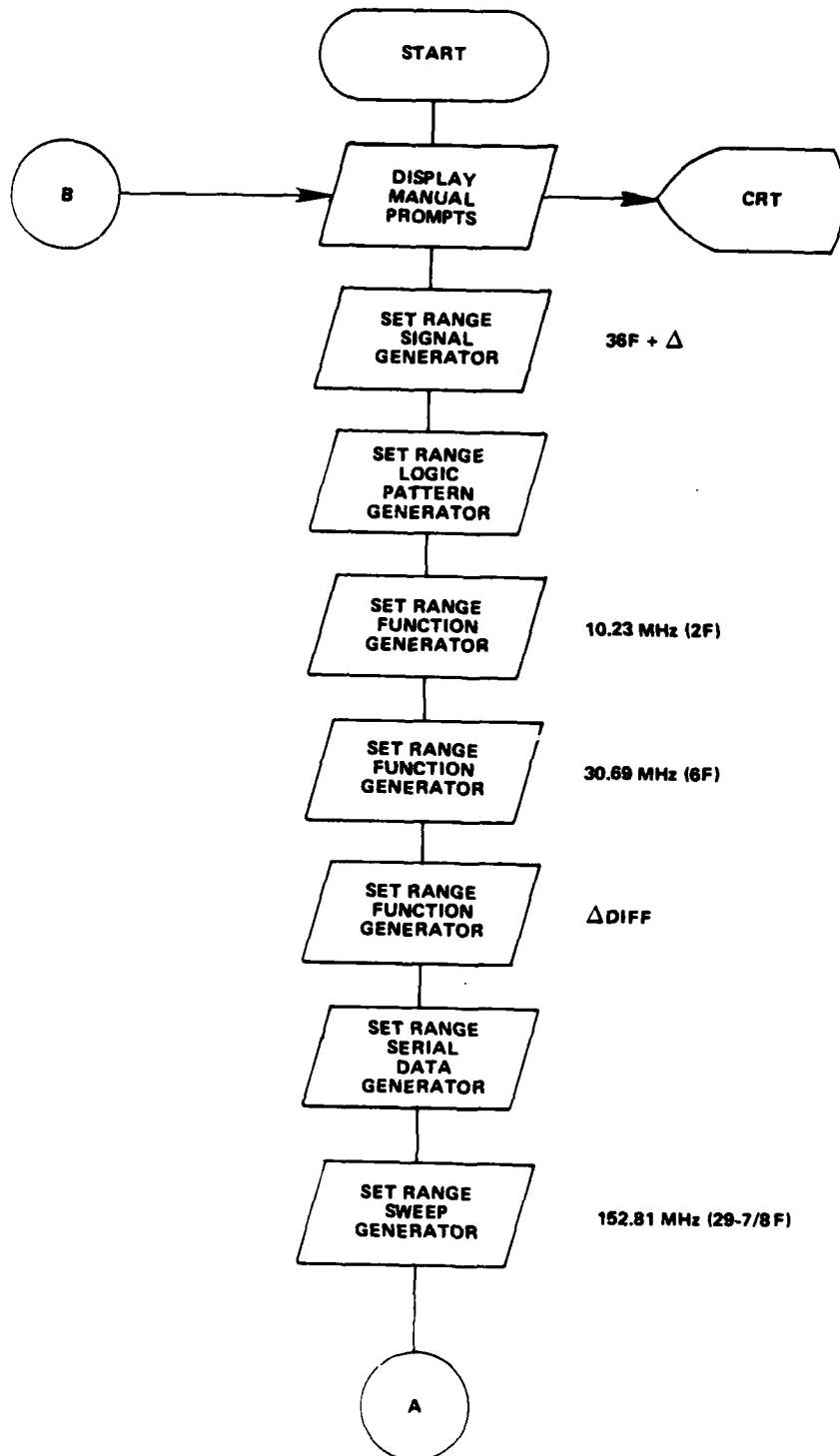
PROGRAM MODULE NAME: _____



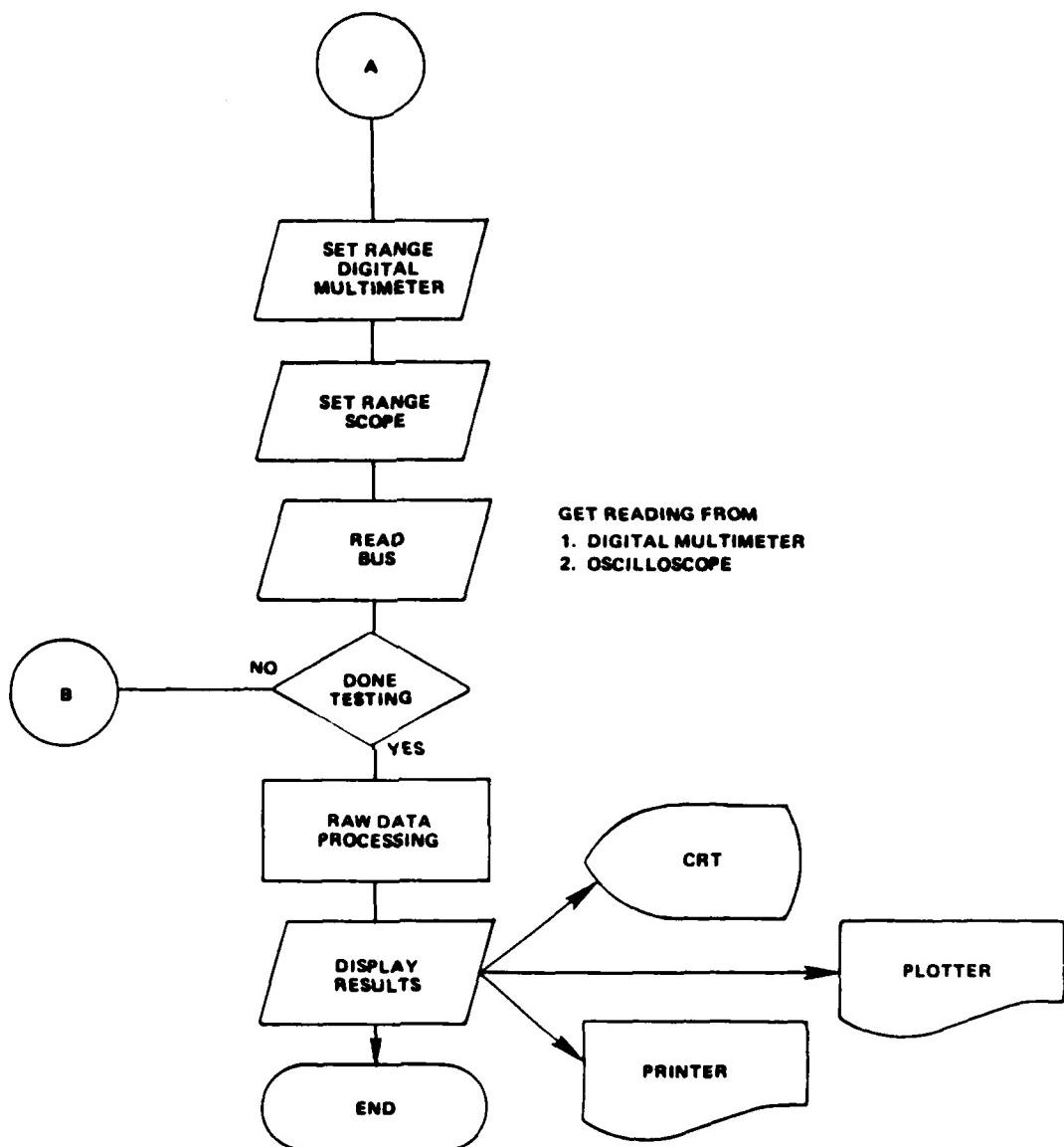


4.1.3 IF/BB/CODER MODULE

MEASURE DIGITAL OUTPUT SIGNALS
PROGRAM MODULE NAME: _____



1. POWER ON
2. SET IF/BB/CODER
MANUAL INPUTS
3. MAKE CONNECTIONS
4. PRESS [RET] WHEN READY



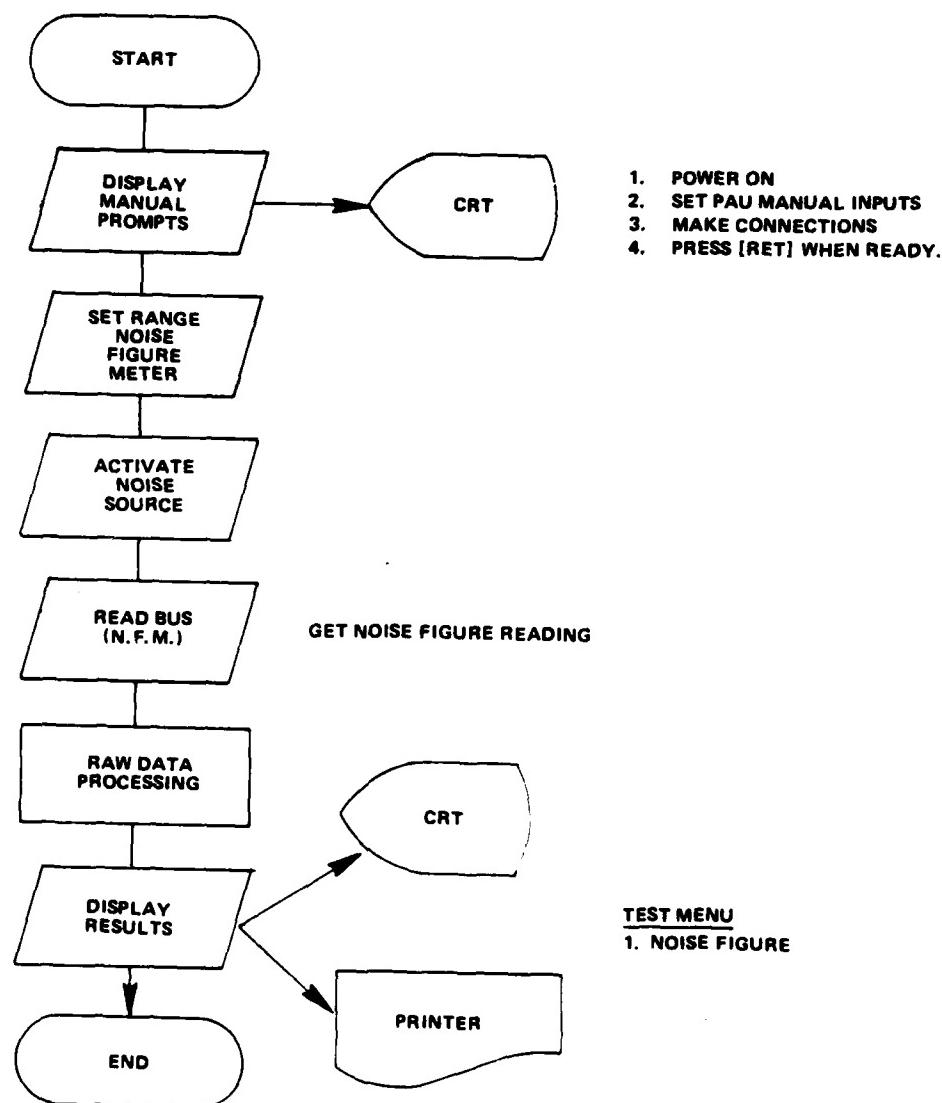
TEST MENU

1. MEASURE SLEW DNE SIGNAL
2. COARSE RANGE EPOCH (CRE)
3. FINE RANGE EPOCH (FRE)
4. AGC S/F1 SIGNAL
5. CARRIER SELECT A SIGNAL
6. CANCEL A SIGNAL
7. R. F. SELECT SIGNAL
8. NOISE ON/OFF SIGNAL
9. ANTENNA SELECT SIGNAL
10. BITE SW SIGNAL

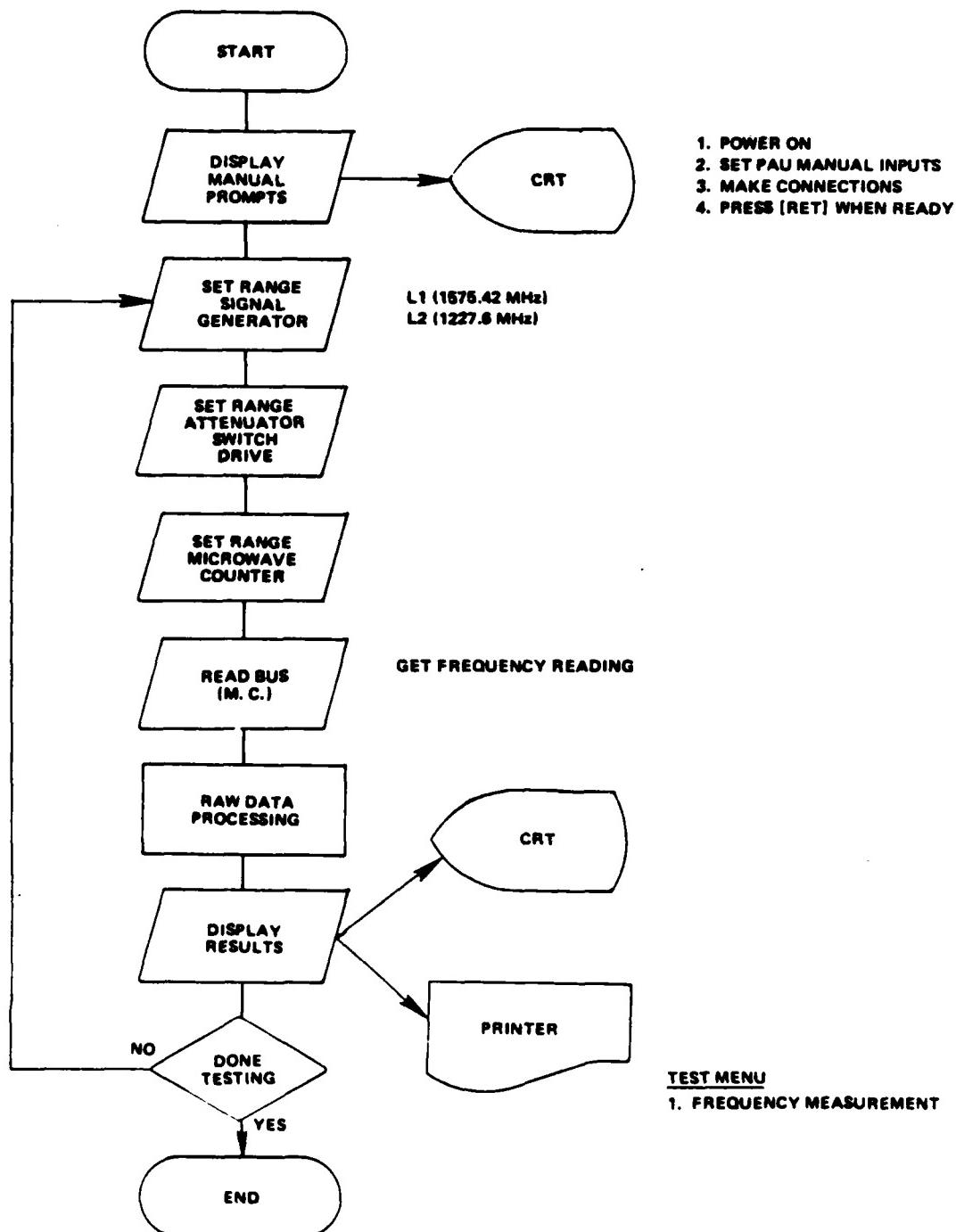
4.1.4 PREAMPLIFIER UNIT MODULE

NOISE FIGURE

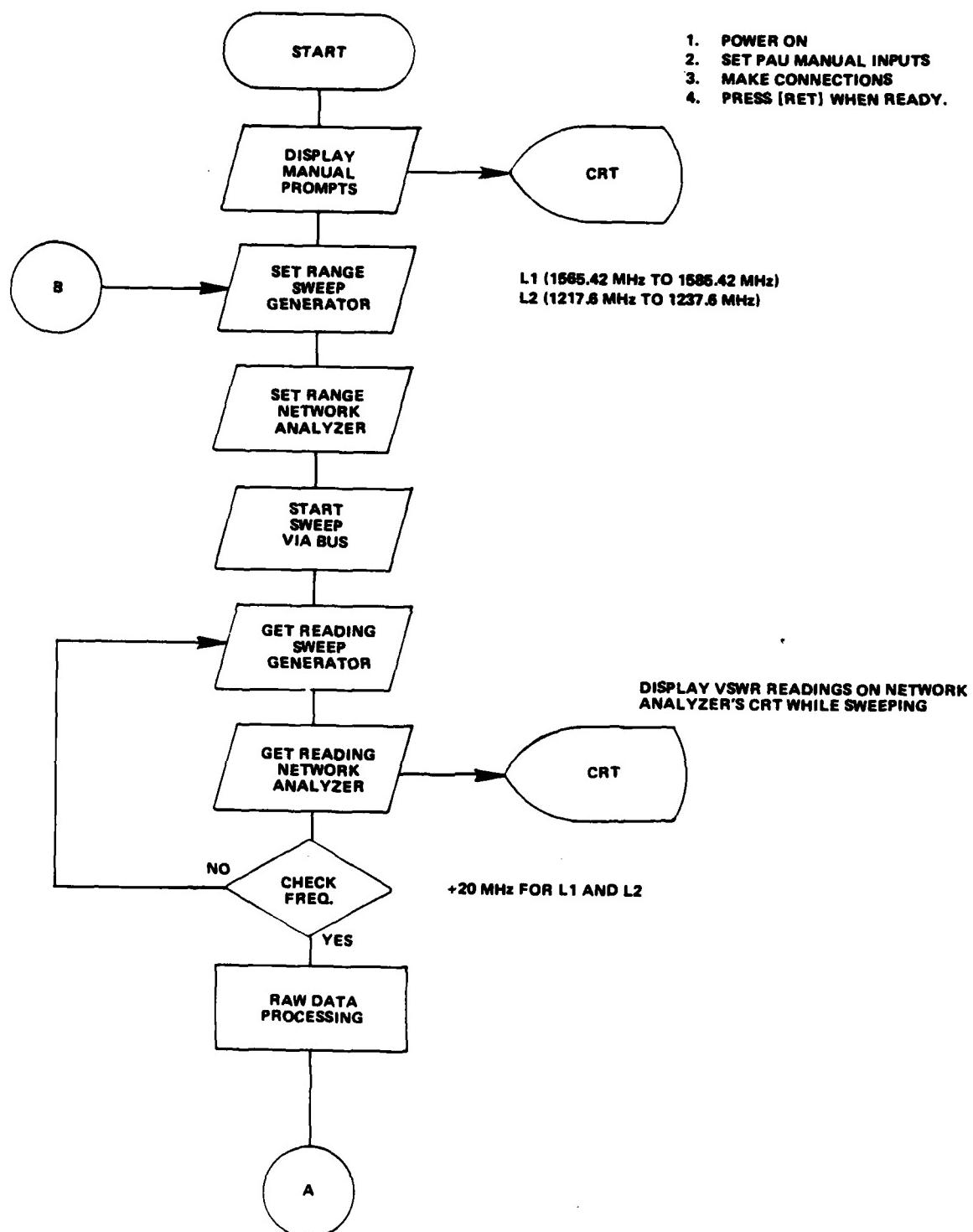
PROGRAM MODULE NAME: _____

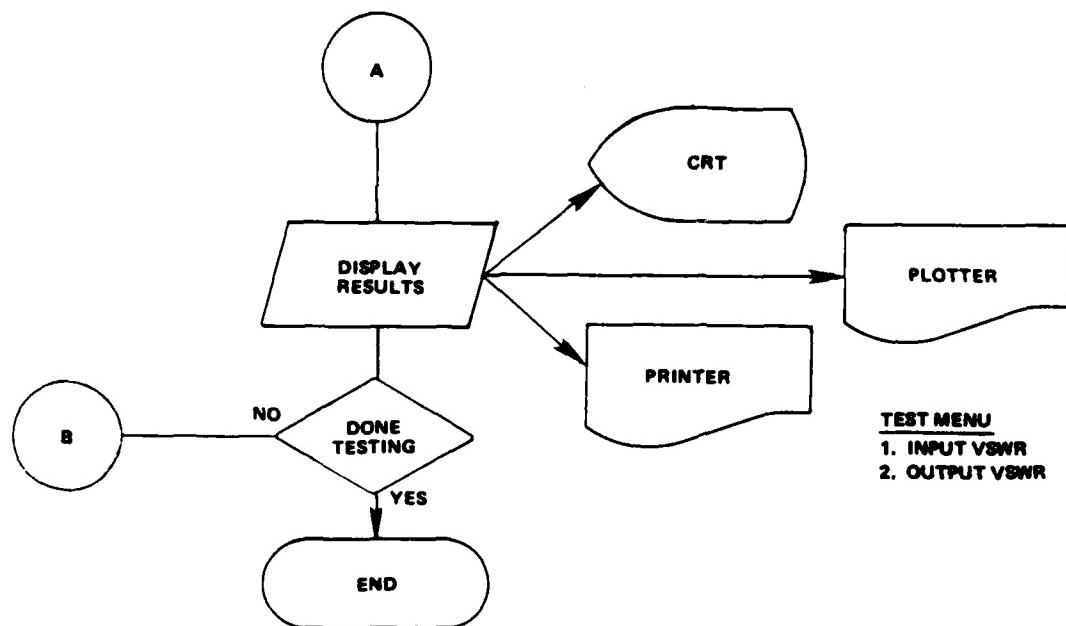


FREQUENCY MEASUREMENT
 PROGRAM MODULE NAME: _____



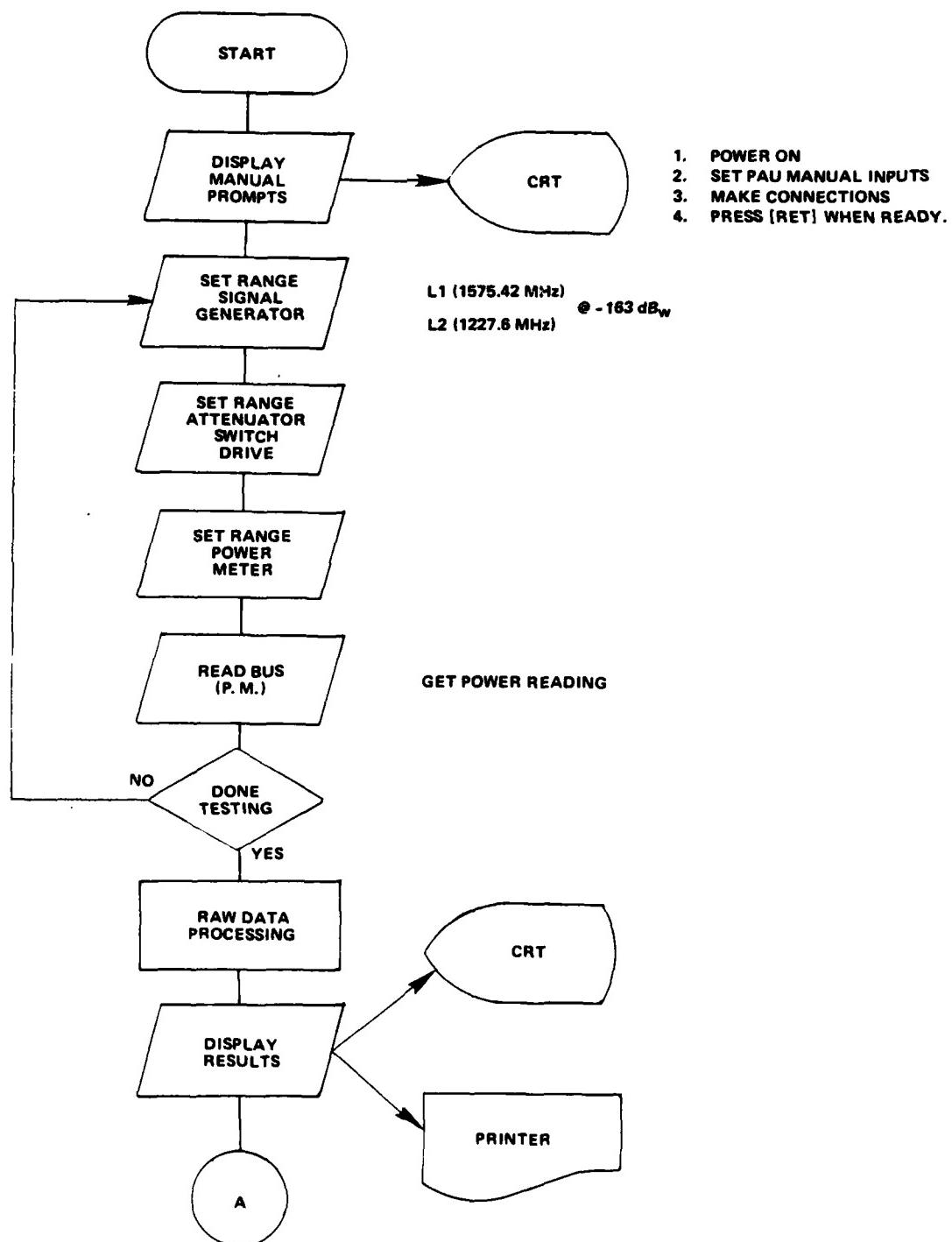
INPUT/OUTPUT VSWR
PROGRAM MODULE NAME: _____

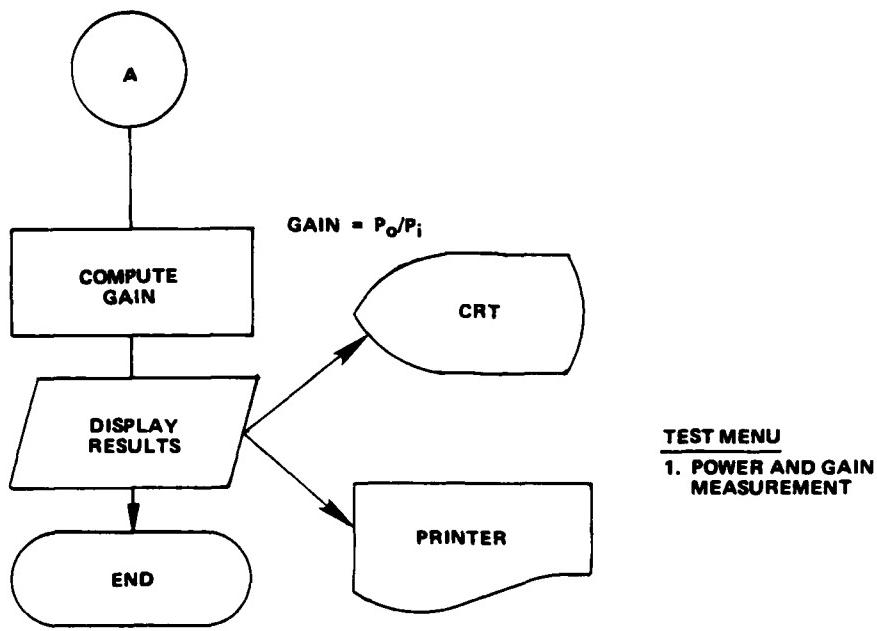




GAIN AND POWER MEASUREMENT

PROGRAM MODULE NAME: _____

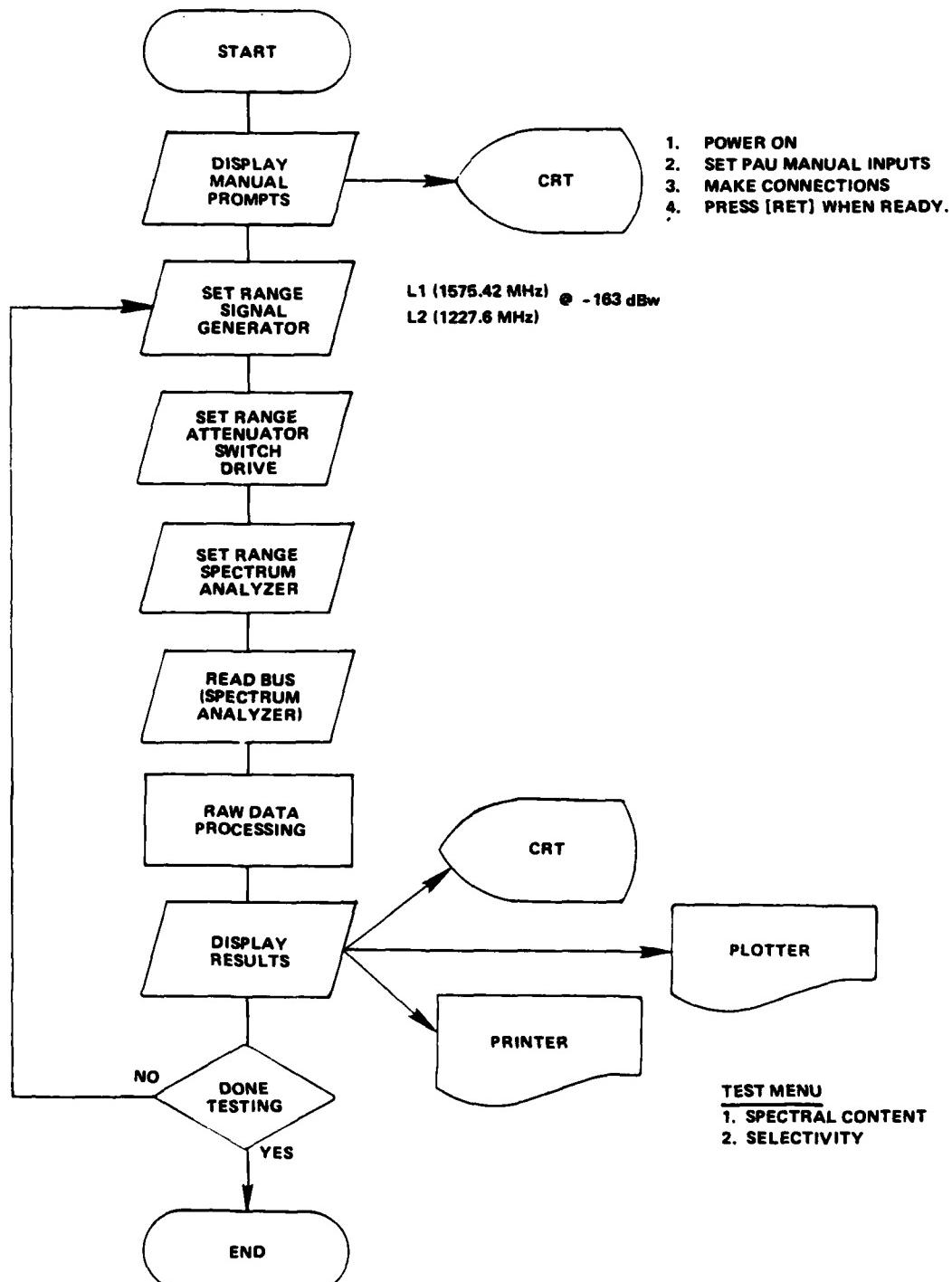




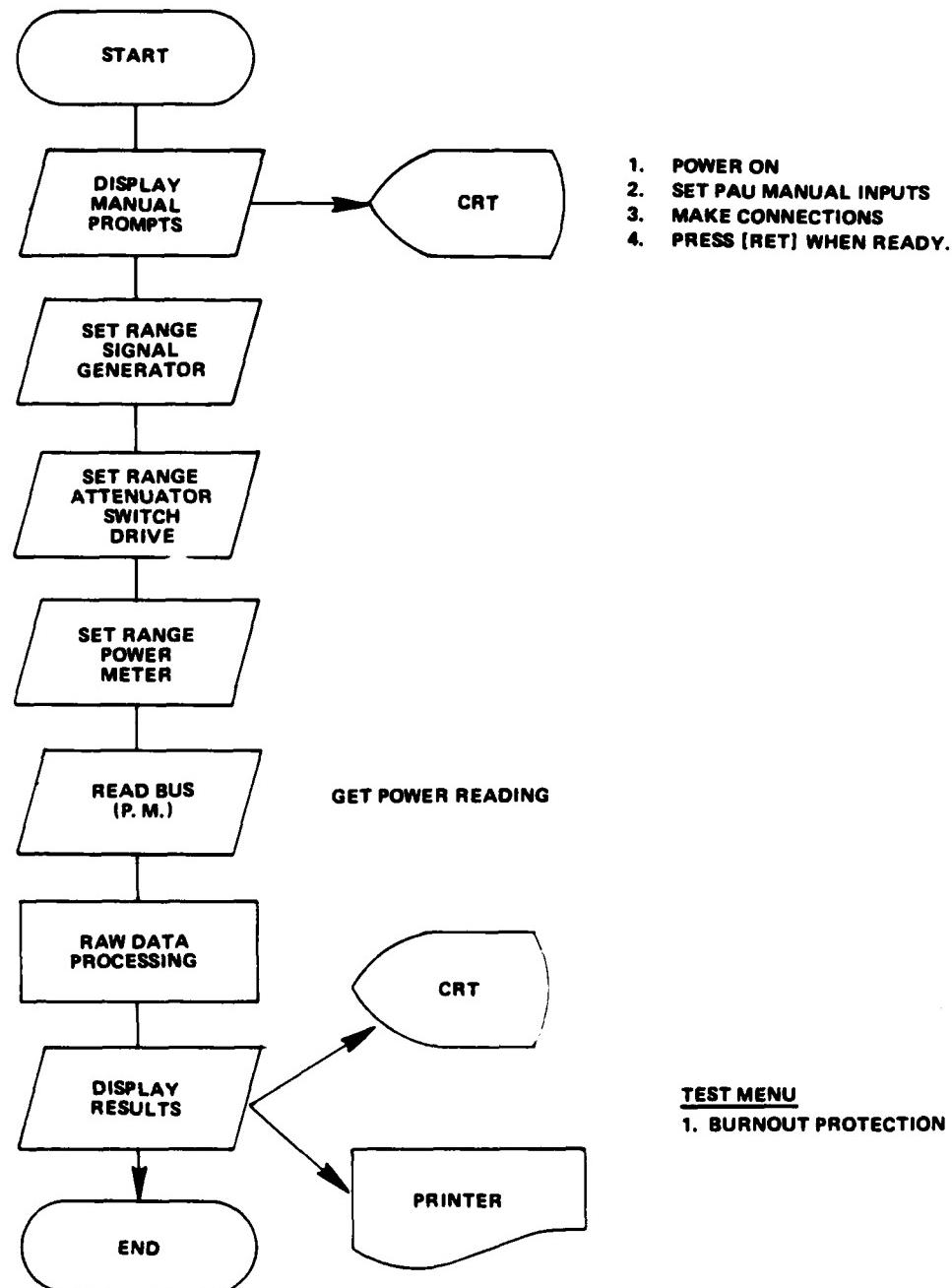
TEST MENU
1. POWER AND GAIN
MEASUREMENT

SPECTRAL CONTENT AND SELECTIVITY MEASUREMENT

PROGRAM MODULE NUMBER: _____

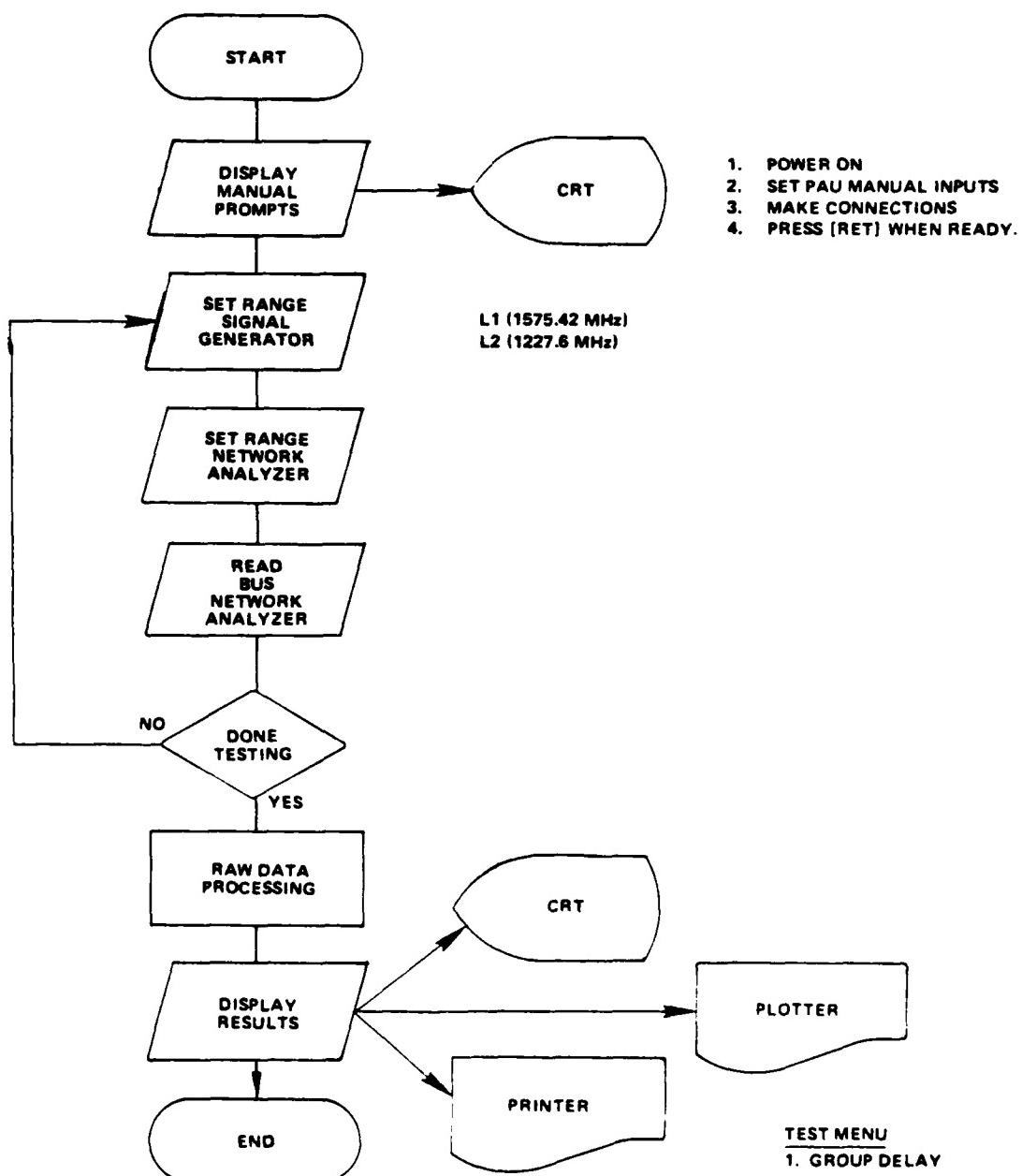


VERIFY BURNOUT PROTECTION
 PROGRAM MODULE NAME: _____



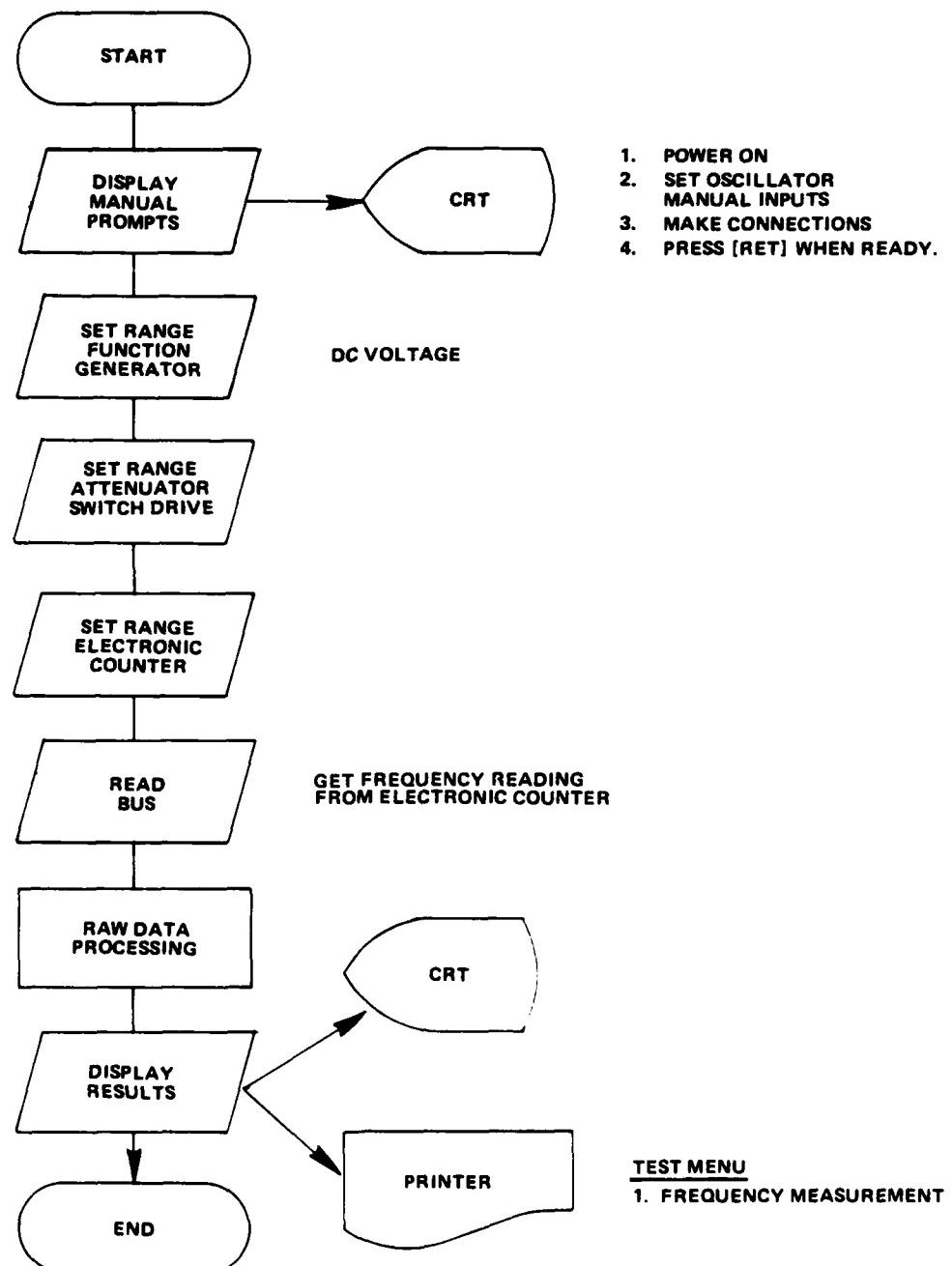
GROUP DELAY

PROGRAM MODULE NAME: _____

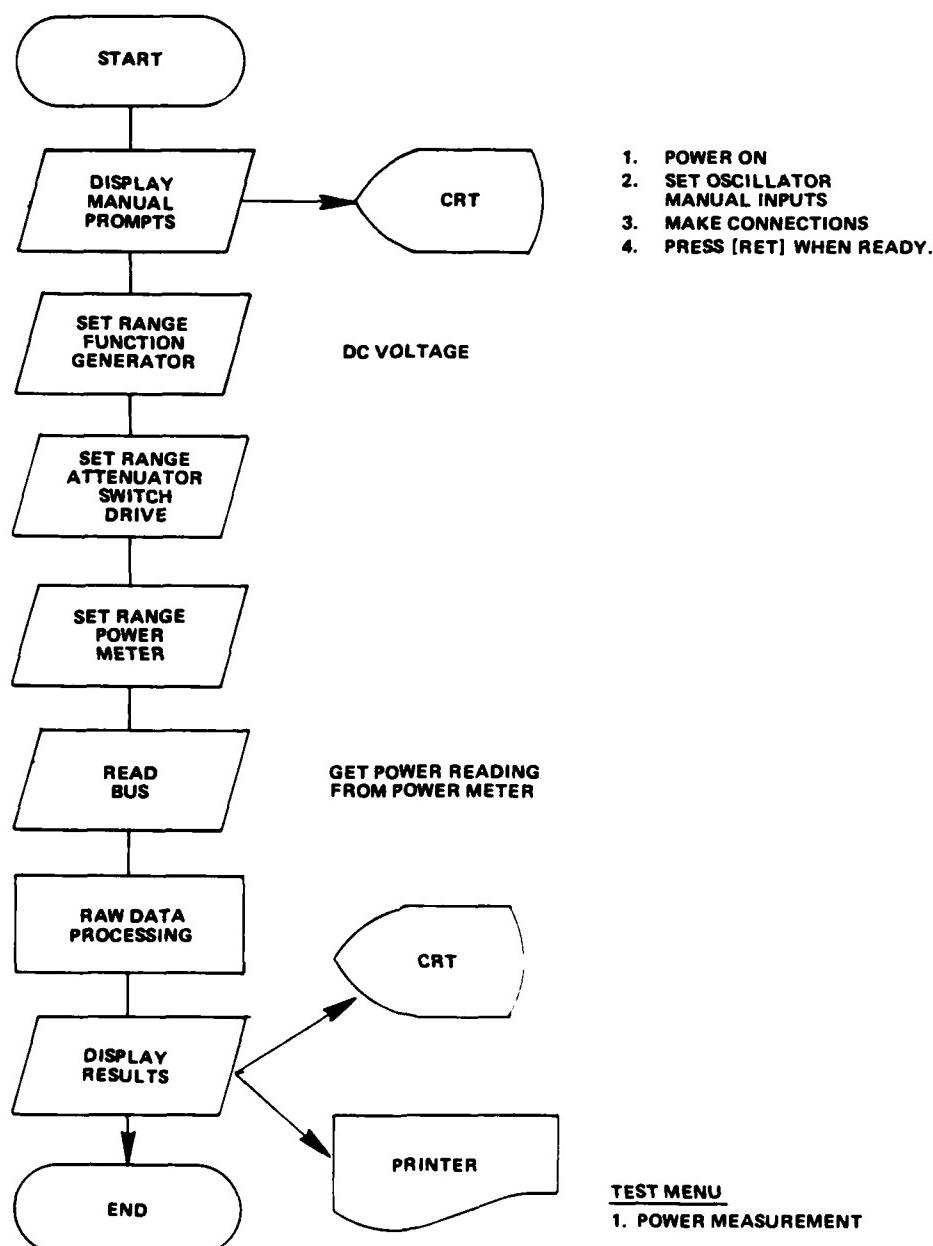


4.1.5 REFERENCE OSCILLATOR MODULE

FREQUENCY MEASUREMENT
PROGRAM MODULE NAME: _____



POWER MEASUREMENT
PROGRAM MODULE NAME: _____



CHECK SPECTRAL CONTENT

PROGRAM MODULE NAME: _____

